

OSC61, OSC52

CLOCK OSCILLATOR

SMD



TYPICAL APPLICATIONS

PC main board, VGA card

DIMENSION

OSC61: 6.0 x 3.5 x 1.8[mm]
OSC52: 7.0 x 5.0 x 1.4[mm]

TYPICAL SPECIFICATIONS

Nominal frequency range	0.5 ~ 107MHz	0.5 ~ 125MHz
Frequency stability	$\pm 25 \times 10^{-6}$, $\pm 50 \times 10^{-6}$, $\pm 100 \times 10^{-6}$	
Temperature range(°C)	Operating	0°C ~ +70°C
	Storage	-55°C ~ +125°C
Power Supply Voltage (Vdd)	+5V(±5%)	+3.3V
Current consumption	10 ^(†15) mA Max.: 0.5 ~ 14.9MHz	10 ^(†10) mA Max.: 0.5~14.9MHz
	15 ^(†20) mA Max.: 15 ~ 29.9MHz	15 ^(†15) mA Max.: 15~29.9MHz
	25 ^(†30) mA Max.: 30 ~ 39.9MHz	20 ^(†25) mA Max.: 30~39.9MHz
	35 ^(†40) mA Max.: 40 ~ 49.9MHz	25 ^(†30) mA Max.: 40~49.9MHz
	40 ^(†50) mA Max.: 50 ~ 59.9MHz	30 ^(†35) mA Max.: 50~59.9MHz
	40 ^(†60) mA Max.: 60 ~ 69.9MHz	35 ^(†45) mA Max.: 60~79.9MHz
	50 ^(†70) mA Max.: 70 ~ 89.9MHz	45 ^(†60) mA Max.: 80~99.9MHz
	60 ^(†80) mA Max.: 90 ~ 107MHz	50 ^(†70) mA Max.: 100~125MHz
Output	TTL-CMOS (See Table A)	

TABLE A

Output level	CMOS		TTL
Load	15pF(50pF Max.)		2TTL or 5TTL or 10TTL
Output voltage	"0" level	VOL : 0.1Vdd Max.	VOL : 0.4V Max.
	"1" level	VOH : 0.9Vdd Min.	VOH : 2.4V Min.
Rise & fall time	10ns Max. : F≤40MHz		
	6ns Max. : F>40MHz		
	at 10%~90% Vdd		at 0.4~2.4 VDC
Symmetry	45~55% at 50% Vdd F≤50MHz		40~60% at +1.4V
	40~60% at 50% Vdd F>50MHz		
Period Jitter RMS pk-pk	10ps Max, 6.5ps Typical F<100MHz		
	8ps Max, 5ps Typical F≥100MHz		

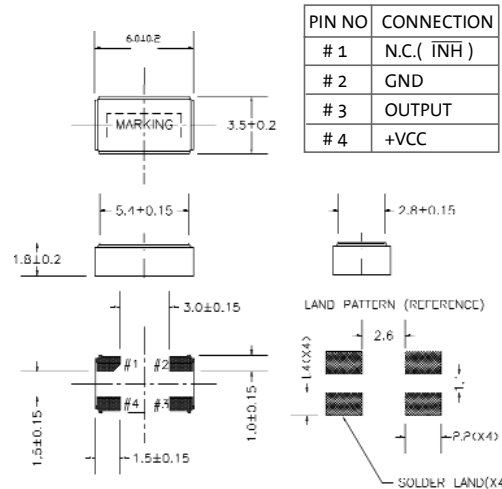
* Please consult with us for other specification.

† Load: 50pF

OUTLINE DIMENSIONS

Unit:mm

OSC-61



OSC-52

