

OTC-046

8-BIT CONSTANT CURRENT LED DRIVER IC

DATE: May 20, 2001

PRELIMINARY

■ FEATURES

- CMOS process
- Low supply voltage: 3V to 5V
- Data transfer rate: up to 20 MHz
- 8 constant current output terminals
- Open-drain NMOS output
- Output sustaining voltage: 7V
- Output “ON” voltage: 0.7V
- Constant current output range: 5mA to 90mA
- Constant current matching ($V_{DD} = 5V$, $T_a = 25^{\circ}C$)
 - Bit-to-Bit: TBD @ $I_{out} = 88mA$
 - Bit-to-Bit: TBD @ $I_{out} = 20mA$
- Package: P-DIP16; SOP16

■ APPLICATIONS

- LED dot matrix driver
- LED back light driver
- LED message indicator driver

■ GENERAL DESCRIPTIONS

The OTC-046 is specially designed as a constant current-sink drive device for LED display applications. The current value at all the constant current outputs is set with an external resistor. The device incorporates with 8-bit shift register and data latch. In addition, there are serial data-input and data-output terminals for being cascaded with other devices.



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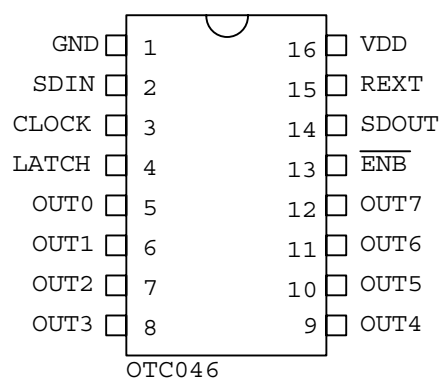
NO.8 INNOVATION ROAD 1,
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ORDERING INFORMATION

PART NO.	PACKAGE	NOTE
OTC-046K	16-Pin PDIP	SKINNY TYPE
OTC-046S	16-Pin SOP	-

PIN ASSIGNMENT (TOP VIEW)



PIN DESCRIPTIONS

NO.	NAME	TYPE	DESCRIPTION
1	GND	Ground	Ground
2	SDIN	Input	Serial data input
3	CLOCK	Input	Clock input
4	LATCH	Input	Latch input of data strobe
5~12	OUTn	Output	Constant current output terminals
13	$\overline{\text{ENB}}$	Input	Output enable
14	SDOUT	Output	Serial data output
15	REXT	Input	Connection for external resistor
16	VDD	Power	Supply voltage

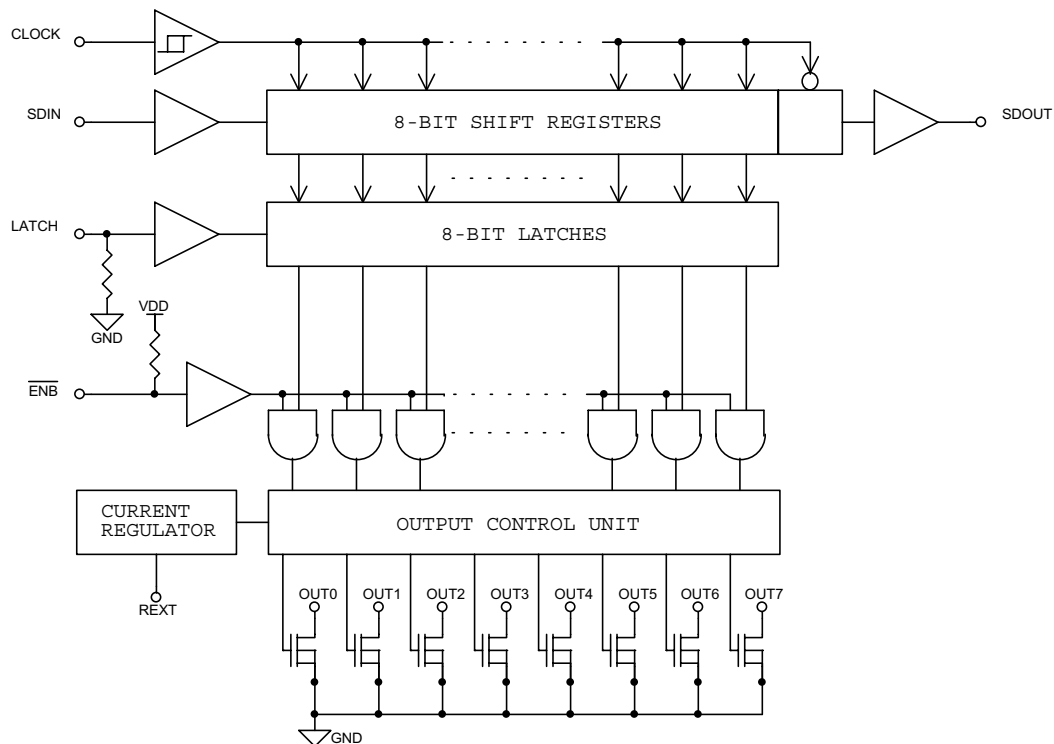


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■ FUNCTION BLOCK DIAGRAM



■ FUNCTION DESCRIPTIONS

The OTC-046 incorporates 8-bit shift register and data latch. Data at the serial data input terminal, SDIN, are transferred into shift registers and shift stage by stage on the rising edge of CLOCK and finally shift out via SDOUT port. The data in shift registers go through the data latch only if LATCH holds high state. Once LATCH goes from high to low the data are latched in the data latch.

All constant current outputs are disabled when $\overline{\text{ENB}}=1$. While $\overline{\text{ENB}}$ goes low and holds low state, the constant current output turn ON if the data latch content is high and turn OFF if the data latch content is low.

The REXT terminal is with fixed voltage and a resistor externally connected with this terminal provides a reference current. The current value at each constant current output is about 14 times greater than reference current.



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■ ABSOLUTE MAXIMUM RATINGS

VDD=+5V, Ta=+25°C, UNLESS OTHERWISE SPECIFIED			
CHARACTERISTICS	SYMBOL	RATING	UNIT
Supply Voltage	VDD	2.5 ~ 5.5	V
Input Voltage	VIN	-0.4 ~ VDD+0.4	V
Clock Frequency	FCLK	20M	Hz
Output Sustaining Voltage	VOUT	7	V
Continuous Constant Current Output	IOUT	90	mA
GND Terminal Current	IGND	720	mA
Power Dissipation, each Output	PDISS	90 (PDIP)	mW
		70 (SOP)	mW
Operating Temperature	TOPR	-40 ~ +85	°C
Storage Temperature	TSTR	-50 ~ +150	°C

■ RECOMMENDED OPERATING CONDITIONS

VDD=+5V, Ta=+25°C, UNLESS OTHERWISE SPECIFIED						
CHARACTERISTIC	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Supply Voltage	VDD	-	4.5	-	5.5	V
Output Voltage	VOUT	OUTn ON or OFF	-	-	7	V
Output Current	IOUT	single OUTn ON	5	-	90	mA
	IOH	SDOUT	-	-	-1	
	IOL	SDOUT	-	-	1	
Input Voltage	VIH	-	0.7VDD	-	VDD+0.3	V
	VIL	-	-0.3	-	0.3VDD	
CLOCK Frequency	FCLK	-	-	-	10	MHz
Power Dissipation	PDISS	Ta=85°C (PDIP)	-	-	0.41	W
		Ta=85°C (SOP)	-	-	0.32	



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■ DC ELECTRICAL CHARACTERISTICS

VDD=+5V, Ta=+25°C, UNLESS OTHERWISE SPECIFIED						
CHARACTERISTIC	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Supply Voltage	VDD	-	3	5	5.5	V
Digital Input Voltage	V _{IH}	-	0.7VDD	-	VDD	V
	V _{IL}	-	GND	-	0.3VDD	V
Digital Output Voltage	V _{OH}	I _{OH} =-1.0mA	4.6	-	-	V
	V _{OL}	I _{OL} =1.0mA	-	-	0.4	V
Voltage Reference	V _{REXT}	REXT=300Ω, Ta=-40~85°C	-	1.23	-	V
Supply Voltage Regulation	%/VDD	REXT=300Ω, Ta=-40~85°C	-	1.5	5.0	%/V
Supply Current	I _{DD(off)}	REXT=OPEN, all OUTn OFF	-	0.3	-	mA
		REXT=910Ω, all OUTn OFF	-	2.84	-	mA
		REXT=200Ω, all OUTn OFF	-	12.6	-	mA
	I _{DD(on)}	REXT=910Ω, all OUTn ON	-	2.84	-	mA
		REXT=200Ω, all OUTn ON	-	12.6	-	mA
	I _{OUT(on)}	REXT=910Ω, all OUTn ON	-	19.7	-	mA
Output Current	I _{OUT(on)}	REXT=300Ω, all OUTn ON	-	59.3	-	mA
		REXT=200Ω, all OUTn ON	-	88.0	-	mA
		REXT=910Ω, all OUTn ON	-	-	-	mA
Output Current Matching (Bit to Bit)	I _{OM}	REXT=910Ω, all OUTn ON	-	-	TBD	%
		REXT=200Ω, all OUTn ON	-	-	TBD	%
Input Resistance	R _{IN}	ENABLE input, Pull up	150	300	600	KΩ
		LATCH input, Pull down	100	200	400	
CLOCK Frequency	FCLK	-	-	10	20	MHz
Output Leakage Current	I _{OUT(OFF)}	V _{OUT} = 5V, all OUTn OFF	-	-	1	μA
Output Sustaining Voltage	V _{OUT(BK)}	all OUTn ON/OFF	-	-	7	V



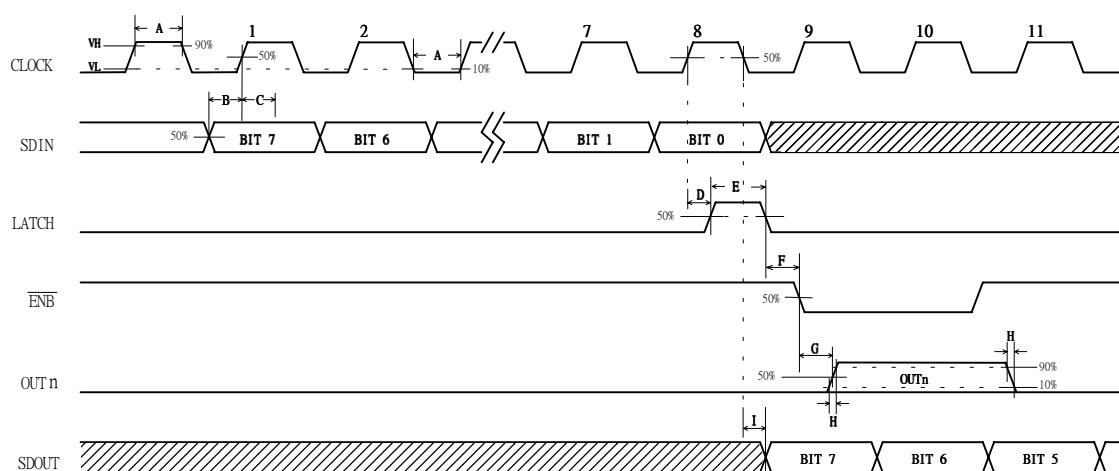
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■ TIMING CHART AND TIMING CONDITIONS

— TIMING CHART



— TIMING CONDITIONS

VDD=+5V, Ta=25°C, REXT=300Ω; CL=13PF@SDOUT					
ITEM	DESCRIPTION	MIN	TYP	MAX	UNIT
A	Clock pulse width	25	50	-	ns
B	Serial data setup time	30	-	-	ns
C	Serial data hold time	30	-	-	ns
D	Time between clock activation and data latch enable	50	-	-	ns
E	Data latch pulse width	50	-	-	ns
F	Time between data latch and output enable	50	-	-	ns
G	Time between output enable and output transition	30	-	-	ns
H	Output current rise/fall time	-	15	20	μs
I	Output delay time of serial data	-	20	25	ns

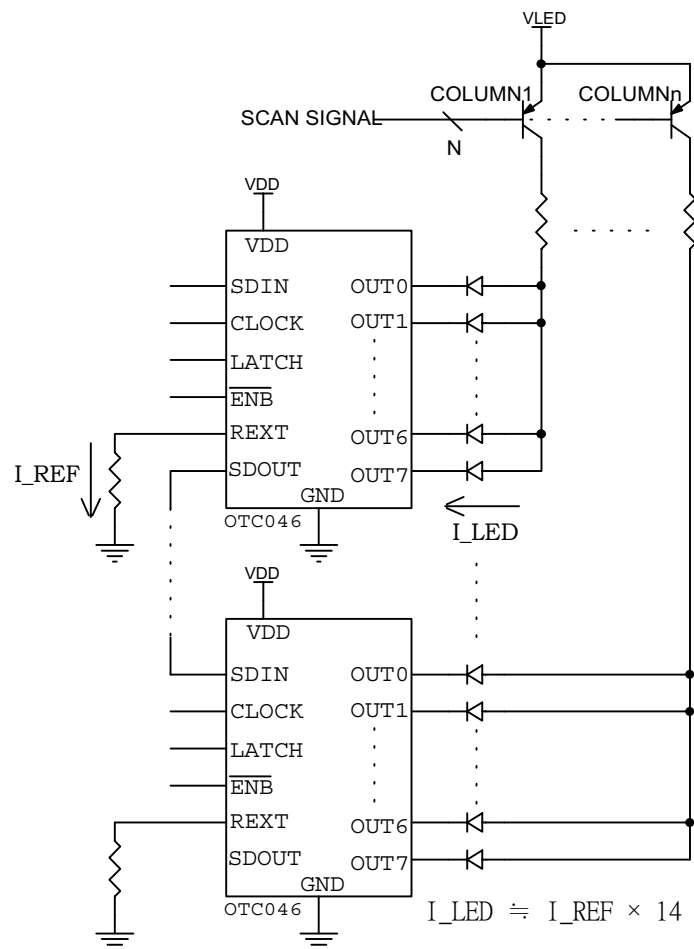


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■ TYPICAL APPLICATION CIRCUIT



■ TRUTH TABLE

SERIAL DATA INPUT	CLOCK INPUT	SHIFT REGISTER CONTENTS					SERIAL DATA INPUT	LATCH INPUT	LATCH CONTENTS					OUTPUT ENABLE INPUT	OUTPUT CONTENTS				
		I1	I2	I3.....IN-1	IN		OUTPUT		I1	I2	I3.....IN-1	IN			I1	I2	I3.....IN-1	IN	
H			R1	R2.....RN-2	RN-1		RN-1												
L			R1	R2.....RN-2	RN-1		RN-1												
X			R1	R2 R3.....RN-1	RN		RN												
		X	X	X.....X	X	X			R1	R2	R3.....RN-1	RN							
		P1	P2	P3.....PN-1	PN	PN			P1	P2	P3.....PN-1	PN	L		P1	P2	P3.....PN-1	PN	
									X	X	X.....X	X	H		L	L	L.....L	L	

L=LOW LOGIC LEVEL H=HIGH LOGIC LEVEL X=IRRELEVANT P=PRESENT STATE R=PREVIOUS STATE



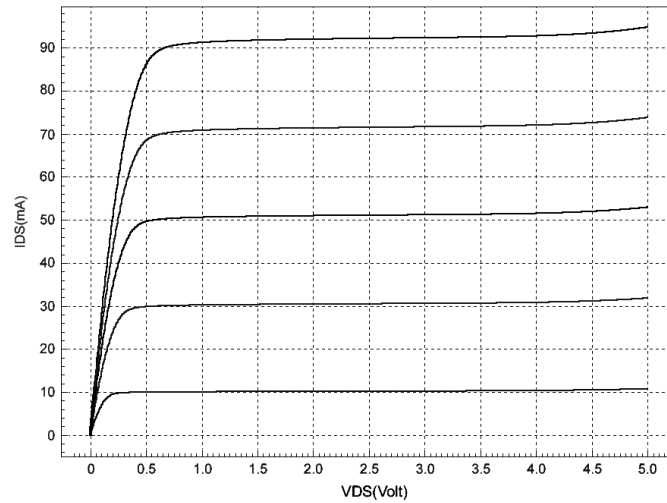
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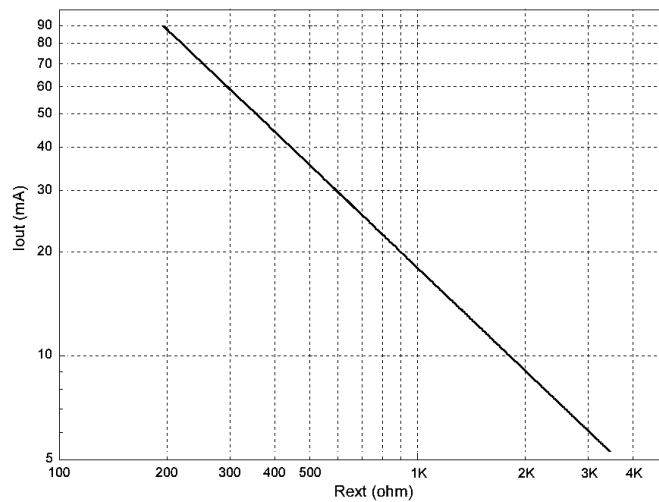
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■ TYPICAL CHARACTERISTIC CURVES

— NMOS Driver I-V Characteristics



— I_{OUT} vs. R_{EXT}

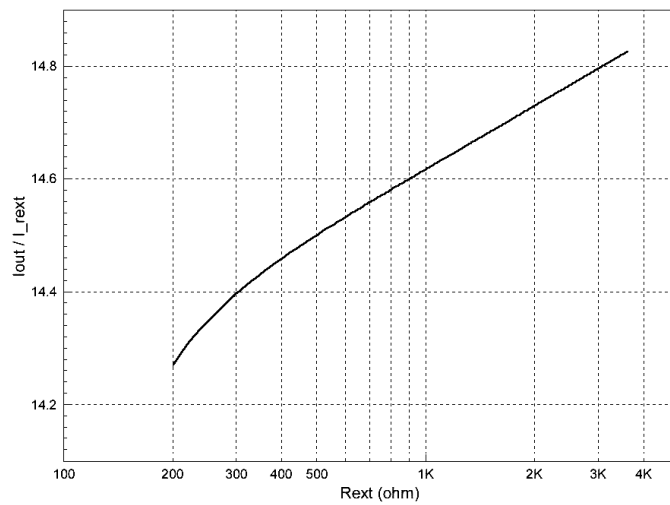


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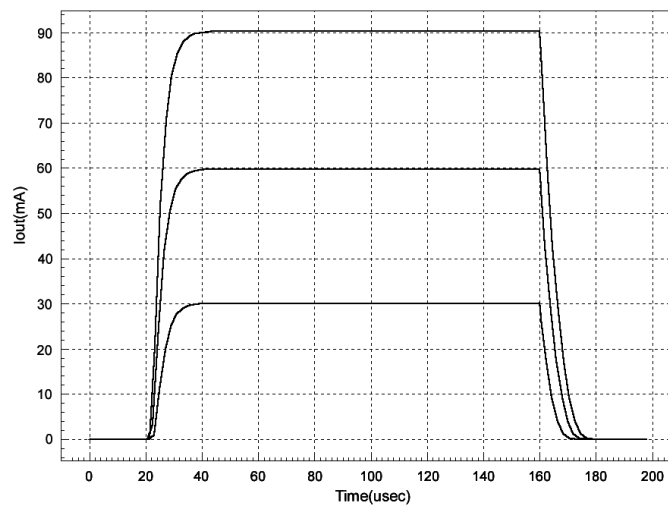
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— I_{OUT} / I_{REXT} vs. R_{EXT}



— Output Current Waveforms



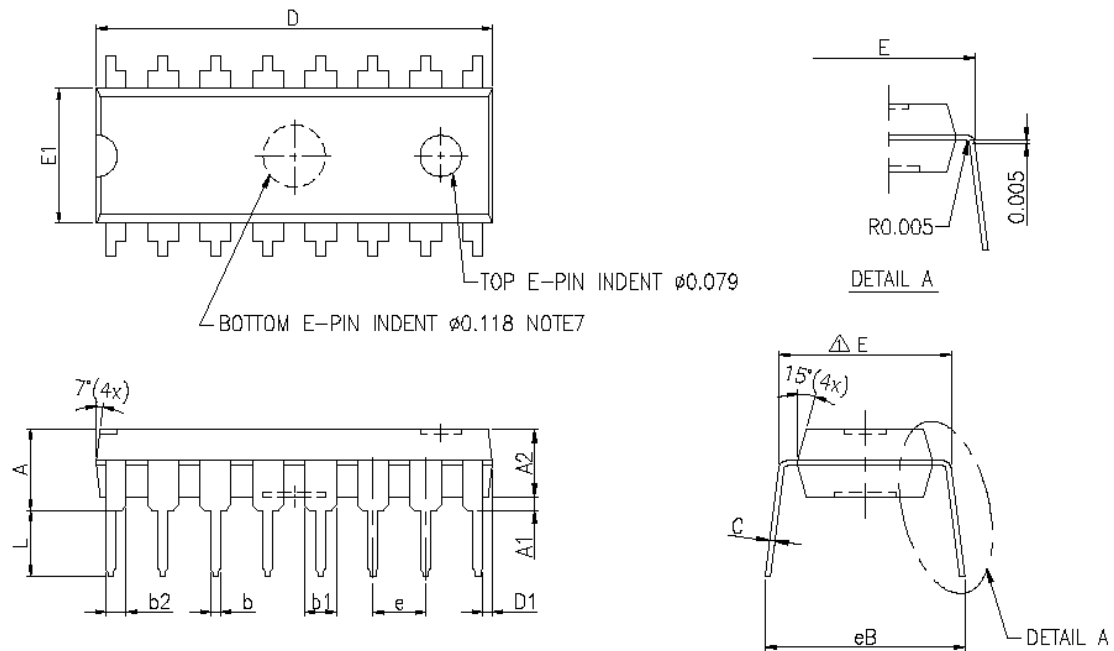
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■ PACKAGE INFORMATION

— PDIP16



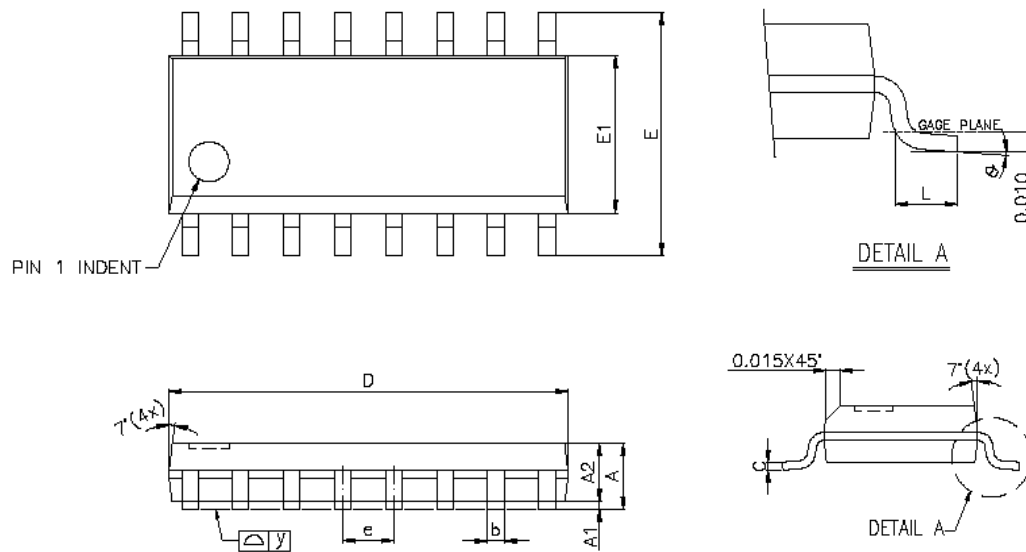
SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	—	—	4.57	—	—	0.180
A1	0.38	—	—	0.015	—	—
A2	3.25	3.30	3.45	0.128	0.130	0.136
B	0.36	0.46	0.56	0.014	0.018	0.022
B1	1.14	1.27	1.52	0.045	0.050	0.060
B2	0.18	0.99	1.17	0.032	0.039	0.046
C	0.20	0.25	0.33	0.008	0.010	0.013
D	18.90	19.15	19.30	0.744	0.754	0.760
D1	0.56	0.69	0.76	0.022	0.027	0.030
E	7.62	—	8.26	0.300	—	0.325
E1	6.35	6.50	6.65	0.250	0.256	0.262
e	—	2.54	—	—	0.100	—
L	3.18	—	—	0.125	—	—
eB	8.64	—	9.65	0.340	—	0.380



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SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	1.47	1.60	1.73	0.058	0.063	0.068
A1	0.10	—	0.25	0.004	—	0.010
A2	—	1.45	—	—	0.057	—
b	0.33	0.41	0.51	0.013	0.016	0.020
C	0.19	0.20	0.25	0.0075	0.008	0.0098
D	9.80	9.91	10.01	0.386	0.390	0.394
E	5.79	5.99	6.20	0.228	0.236	0.244
E1	3.81	3.91	3.99	0.150	0.154	0.157
e	—	1.27	—	—	0.050	—
L	0.38	0.71	1.27	0.015	0.028	0.050
y	—	—	0.076	—	—	0.003
Φ	0°	—	8°	0°	—	8°



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