

June, 1998 Preliminary

AMI 0.6 micron CMOS

Output Driver 350Mhz @ 50Ω

output drivers. Requires a voltage reference.

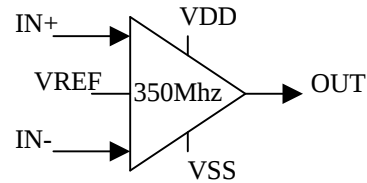
Features

- high speed
- tuned output capable of driving 50Ω load
- architecture places supply current off-chip to minimize on-chip noise

Description

A high speed, single-ended output driver. External matching network required to match the output to 50 ohms. Useful for RF

PIN DESCRIPTION



NAME	TYPE	DESCRIPTION
IN+	Analog Input	Differential IF signal input. (true)
IN-	Analog Input	Differential IF signal input. (false)
OUT	Analog Output	Single-ended IF output.
AVDD	Analog Supply	+3V supply (2.7 - 3.6V)
AVSS	Analog Supply	Analog ground

AC ELECTRICAL CHARACTERISTICS

Unless otherwise stated $T_A = 25^\circ\text{C}$, $V_{CC} = 3\text{V}$

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
V_{CC}	Power supply voltage Range		2.7	3.0	3.6	V
F_{IN}	Input frequency range		TBD		TBD	Mhz
R_{IN}	Input impedance	Single Ended		300		Ω
G	Gain	worst case models	0			dB
V_{OUT}	Output voltage swing	Single Ended, dc offset = 1.0		1.0		V_{P-P}
GBW	Unity gain bandwidth	worst case models		??Mhz		MHz

Special Output conditions