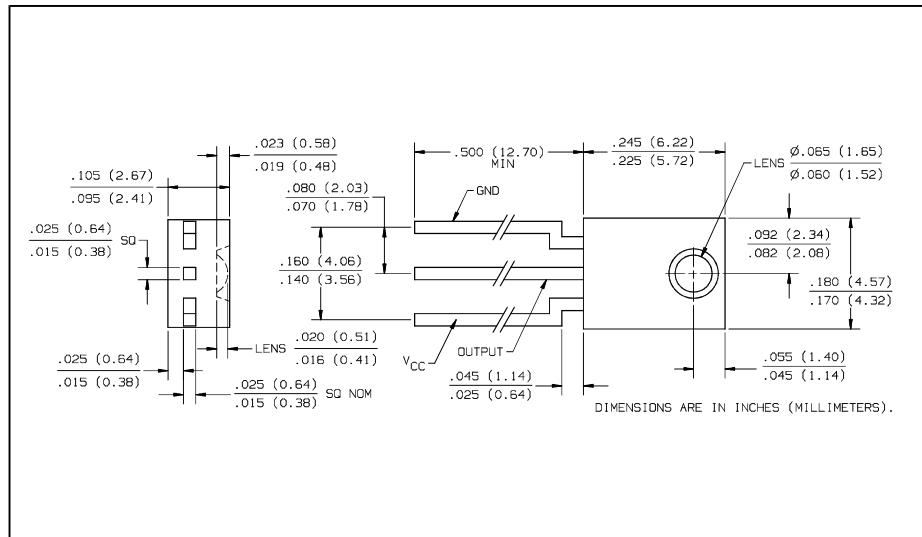


Photologic[®] Sensors

Types OPL535, OPL535-OC, OPL536, OPL536-OC



Features

- Four output options
- High noise immunity
- Direct TTL/LSTTL CMOS interface
- Low cost plastic side-looking package
- Mechanically and spectrally matched to the OP145 and OP245 series LED's
- Data rates to 250 kBaud
- Low power consumption

Description

The OPL535, OPL535-OC, OPL536, OPL536-OC contain a monolithic integrated circuit which incorporates a photodiode, amplifier, voltage regulator, Schmitt trigger and an NPN output transistor on a single silicon chip.

The OPL535 and OPL536 include a 10 KΩ pull-up resistor (R_L) from output to V_{CC}. The OPL535-OC and OPL536-OC have an open-collector output.

These devices exhibit very stable performance over supply voltages ranging from 4.5 V to 16 V and a wide range of irradiance levels.

The Photologic[®] chip is encapsulated in a molded plastic package which has a recessed integral lens for enhanced optical coupling combined with mechanical protection.

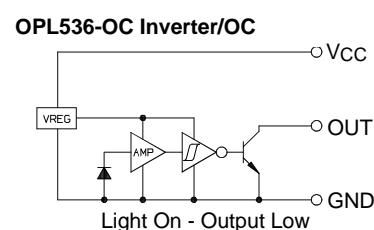
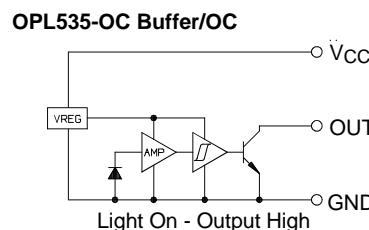
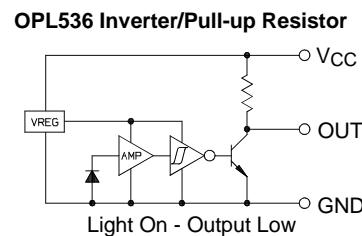
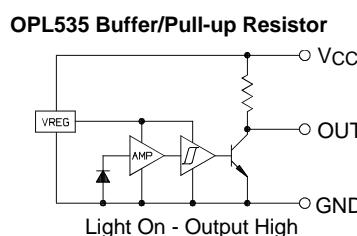
Absolute Maximum Ratings (T_A = 25° C unless otherwise noted)

Supply Voltage, V _{CC}	18 V
Storage Temperature Range	-40° C to +100° C
Operating Temperature Range	-40° C to +85° C
Lead Soldering Temperature Range [1/16 inch (1.6 mm) from case for 5 sec. with soldering iron]	240° C
Power Dissipation	90 mW
Voltage at Output Lead ⁽⁴⁾	35 V
Sinking Current	50 mA

Notes:

- (1) Derate linearly 2.67 mW/° C above 70° C.
- (2) RMA flux is recommended. Duration can be extended to 10 sec. maximum when flow soldering. Max 20 grams force may be applied to the leads when soldering.
- (3) Irradiance measurements are made with $\lambda_i = 935$ nm.
- (4) OC versions only. For I_{CC} on pull-up versions add V_{CC}/10 kΩ.

Schematics



Types OPL535, OPL535-OC, OPL536, OPL536-OC

Electrical Characteristics (-40° C to +85° C unless otherwise noted) V_{CC} = 4.5 V to 16 V

SYMBOL	PARAMETER	MIN	TYP	MAX	UNITS	TEST CONDITIONS
V _{CC}	Operating Supply Voltage	4.5		16.0	V	
	Peak-to-Peak V _{CC} Ripple Necessary to Cause False Triggering of Output			2	V	f = DC to 50 MHz
I _{CC}	Supply Current ⁽⁴⁾		2.7	5.0	mA	E _e = 0 or 1 mW/cm ²
E _{eT(+)}	Positive-Going Threshold Irradiance ⁽³⁾ OPL535, OPL535-OC, OPL536, OPL536-OC OPL535A, OPL535-OCA, OPL536A, OPL536-OCA OPL535B, OPL535-OCB, OPL536B, OPL536-OCB	0.12 0.12 0.23		0.38 0.28 0.38	mW/cm ² mW/cm ² mW/cm ²	T _A = 25° C T _A = 25° C T _A = 25° C
E _{eT(+)} /E _{eT(-)}	Hysteresis Ratio	1.20		1.80		
ΔE _{eT(+)} (ΔT)	Temperature Coefficient >0° C <0° C		-0.6 -1.6		%/° C %/° C	
OPL535, OPL535-OC (Buffers)						
I _{OH}	High Level Output Current ⁽⁴⁾		0.1	10	μA	V _{OH} = 30 V, E _e = 1 mW/cm ²
V _{OL}	Low Level Output Voltage		0.2	0.40	V	I _{OL} = 16 mA, E _e = 0
OPL536, OPL536-OC (Inverters)						
I _{OH}	High Level Output Current ⁽⁴⁾		0.1	10	μA	V _{OH} = 30 V, E _e = 0
V _{OL}	Low Level Output Voltage		0.2	0.40	V	I _{OL} = 16 mA, E _e = 1 mW/cm ²
OPL535, OPL536						
t _r	Output Rise Time		1.5		μs	E _e = 0 or 1 mW/cm ² , C _L = 50 pF
t _f	Output Fall Time		20		ns	
OPL535-OC, OPL536-OC						
t _r	Output Rise Time		50		ns	E _e = 0 or 1 mW/cm ² ,
t _f	Output Fall Time		20		ns	R _L = 300 Ω to 5 V, C _L = 50 pF
OPL535, OPL535-OC, OPL536, OPL536-OC						
tpE _{eT(+)}	Propagation Delay		1.0		μs	E _e = 0 or 1 mW/cm ² ,
tpE _{eT(-)}	Propagation Delay		3.0		μs	R _L = 300 Ω to 5 V, C _L = 50 pF

PHOTODIODE
SENSORS

Optek reserves the right to make changes at any time in order to improve design and to supply the best product possible.

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