

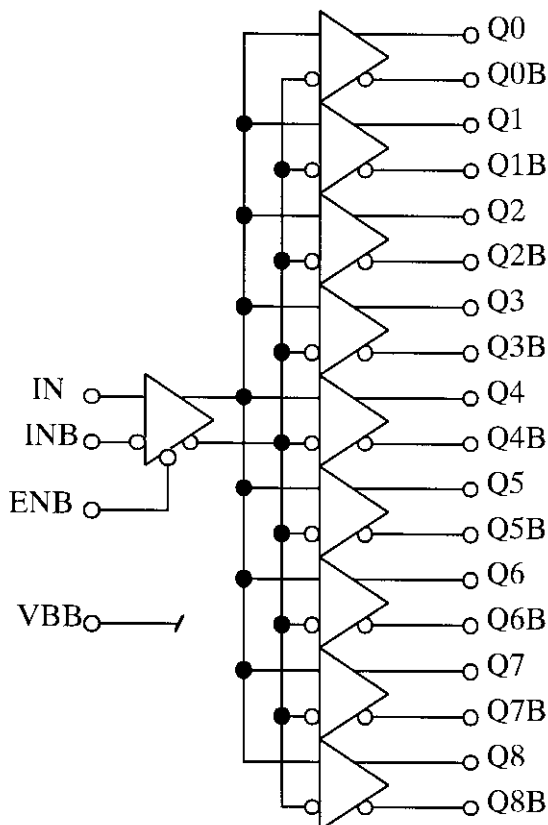
NB10EP111

PINACLE

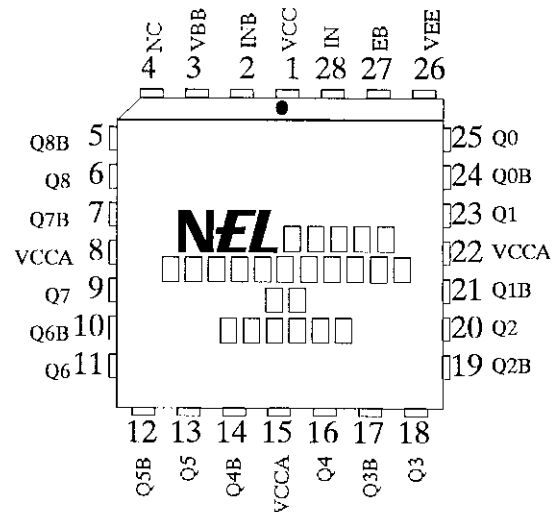
(1:9 Differential Clock Driver)

PIN NAMES

Pin	Function
IN, INB	Differential Input Pair
ENB	Enable
Q0,Q0B~Q8,Q8B	Differential Outputs
VBB	Reference Voltage Output



LOGIC DIAGRAM



PIN ASSIGNMENT
(Top View)

*All VCC and VCCA pins are tied together on the die.



NB10EP111

PINACLE

(1:9 Differential Clock Driver)

DC Characteristics

VEE=-5.2V±5% Ta=25°C

	NB10EP111J		
	MIN	TYP	MAX
I _{IH} (μA)			150
V _{BB} (mV)	-1380		-1190
I _{EE} (mA)	-	35	50
V _{pp} (DC: mV)	250		
V _{CMR} (mV)	-1.6		-0.4

AC Characteristics

VEE=-5.2V±5% Ta=25°C

	NB10EP111J		
	MIN	TYP	MAX
t _r / t _f (ps) :20-80%	275	375	600
t _{PLH} / t _{PHL} (ps) Differential	430		630
t _{PLH} / t _{PHL} (ps) S E	330		730
t _{PLH} / t _{PHL} (ps) Enable	450		850
t _{PLH} / t _{PHL} (ps) Disable	450		850
t _s (ps) Setup Time: ENB to IN	200	0	
t _H (ps) Hold Time: IN to ENB	0	-200	
t _R (ps) Release Time: ENB to IN	300	100	
t _{skew} (ps)		25	50
V _{pp} [AC] (mV) Minimum Input Swing	250		