



NES
NEW ENGLAND SEMICONDUCTOR

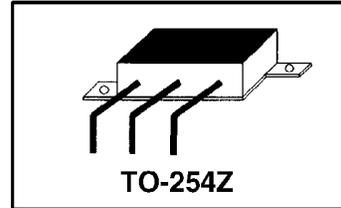
NSG2585

LINEAR VOLTAGE REGULATORS

**3 TERMINAL
NEGATIVE
ADJUSTABLE**

- Output Voltage Adjustable Form -1.2V to -37V
- 1.5A Output Current Guaranteed -55°C to +150°C
- Line Regulation Typically 0.01% /V
- Load Regulation Typically 0.3%
- Output Short Circuit Protected

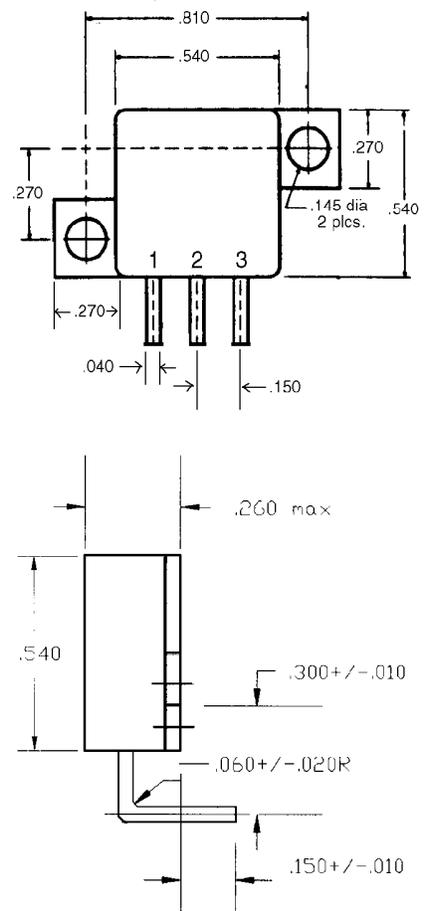
Pinout	
Pin 1	Adjust
Pin 2	Input
Pin 3	Output
Tab	Isolated



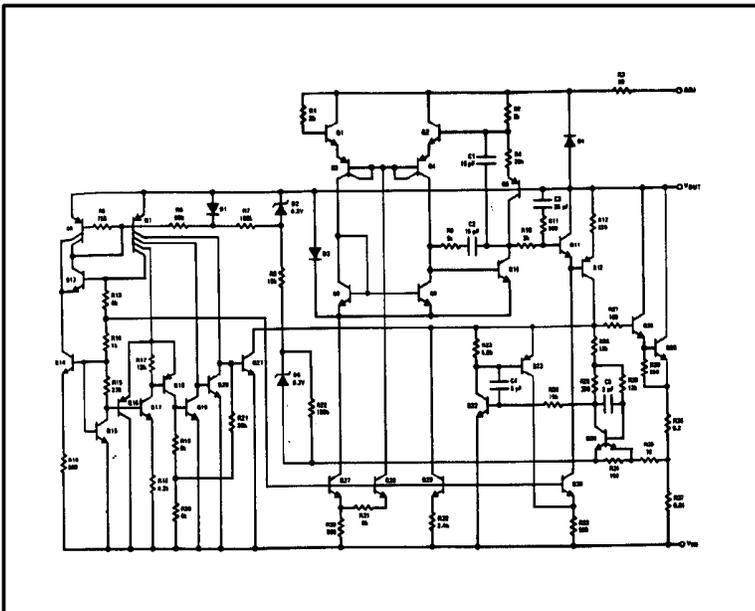
Absolute Maximum Ratings

Power Dissipation..... Internally Limited
 Input-Output Voltage Differential..... 40V
 Operating Junction Temperature Range
 NSG2585..... -55°C to +150°C
 Storage Temperature..... -65°C to +150°C
 Lead Temperature (Soldering, 10 sec.)..... 300°C
 ESD Rating..... 2k Volts

MECHANICAL OUTLINE



Schematic Diagram



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6 Lake Street Lawrence, MA 01841
 1-800-446-1158 / (978) 794-1666 / FAX: (978) 689-0803

T4-4.8-860-006 REV: --



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NSG2585

Electrical Characteristics

Parameter	Conditions	NSG2585			Units
		Min	Typ	Max	
Line Regulation	$T_j = 25^\circ\text{C}$, $3\text{V} \leq V_{\text{IN}} - V_{\text{OUT}} \leq 40\text{V}$ $I_L = 10\text{ mA}$		0.01	0.02	%/V
Load Regulation	$T_j = 25^\circ\text{C}$, $10\text{ mA} \leq I_{\text{OUT}} \leq I_{\text{MAX}}$		0.3	0.5	%
Thermal Regulation	$T_j = 25^\circ\text{C}$, 10ms Pulse		0.002	0.02	%/W
Adjustment Pin Current Charge	$10\text{ mA} \leq I_L \leq I_{\text{MAX}}$ $3.0\text{V} \leq V_{\text{IN}} - V_{\text{OUT}} \leq 40\text{V}$, $T_A = 25^\circ\text{C}$		2	5	μA
Adjustment Pin Current			65	100	μA
Reference Voltage	$T_j = 25^\circ\text{C}$ $3\text{V} \leq V_{\text{IN}} - V_{\text{OUT}} \leq 40\text{V}$, $10\text{ mA} \leq I_{\text{OUT}} \leq I_{\text{MAX}}$, $P \leq P_{\text{MAX}}$	-1.225 -1.200	-1.250 -1.250	-1.275 -1.300	V V
Line Regulation	$3\text{V} \leq V_{\text{IN}} - V_{\text{OUT}} \leq 40\text{V}$		0.02	0.05	%/V
Load Regulation	$10\text{ mA} \leq I_{\text{OUT}} \leq I_{\text{MAX}}$		0.3	1	%
Temperature Stability	$T_{\text{MIN}} \leq T_j \leq T_{\text{MAX}}$		0.6		%
Minimum Load Current	$ V_{\text{IN}} - V_{\text{OUT}} \leq 40\text{V}$ $ V_{\text{IN}} - V_{\text{OUT}} \leq 10\text{V}$		2.5 1.2	5 3	mA mA
Current Limit	$ V_{\text{IN}} - V_{\text{OUT}} \leq 15\text{V}$ K and T Package H and P Package $ V_{\text{IN}} - V_{\text{OUT}} = 40\text{V}$, $T_j = 25^\circ\text{C}$ K and T Package H and P Package	1.5 0.5 0.24 0.15	2.2 0.8 0.4 0.17	3.5 1.8	A A A A
RMS Output Noise, % of V_{OUT}	$T_j = 25^\circ\text{C}$, $10\text{ Hz} \leq f \leq 10\text{ kHz}$		0.003		%
Ripple Rejection Ratio	$V_{\text{OUT}} = -10\text{V}$, $f = 120\text{ Hz}$ $C_{\text{ADJ}} = 10\text{ }\mu\text{F}$	66	60 77		dB dB
Long-Term Stability	$T_j = 125^\circ\text{C}$, 1000 Hours		0.3	1	%

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