



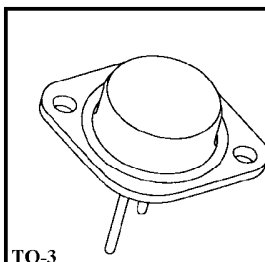
NES

NEW ENGLAND SEMICONDUCTOR

NES150

POWER MOSFET N CHANNEL

- REPETITIVE AVALANCHE RATINGS
- LOW $R_{DS(ON)}$
- LOW DRIVE REQUIREMENT
- DYNAMIC dv/dt RATING



TO-3

30 AMPERE

100 VOLTS

0.055 Ω

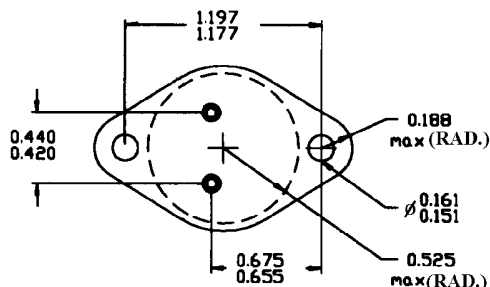
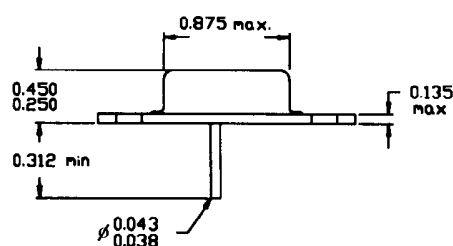
ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$ unless otherwise noted)

PARAMETERS / TEST CONDITIONS		SYMBOL	NES150	UNITS
Drain-Source Voltage		V_{DS}	100	V
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current	$T_C = 25^\circ\text{C}$	I_D	30	A
Power Dissipation	$T_C = 25^\circ\text{C}$	P_D	150	W
Operating Junction & Storage Temperature Range		T_J, T_{stg}	-55 to + 150	$^\circ\text{C}$
Lead Temperature (1/16" from case for 10 secs.)		T_L	300	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYP.	MAX.	UNITS
Junction-to-Case	R_{thJC}		0.83	K/W
Junction-to-Ambient	R_{thJA}		30	K/W
Case-to-Sink	R_{thCS}	0.12		K/W

MECHANICAL OUTLINE



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T4-4.8-860-919 REV: --



NES

NEW ENGLAND SEMICONDUCTOR

NES150

ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}\text{C}$ unless otherwise noted)

PARAMETERS / TEST CONDITIONS		SYMBOL	MIN.	TYP.	MAX.	UNITS
Drain-Source Breakdown Voltage $V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$		$V_{(BR)DSS}$	100			V
Gate Threshold Voltage $V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$		$V_{GS(th)}$	2.0		4.0	V
Gate-Body Leakage $V_{GS} = \text{At Rated } V_{GS}$		I_{GSS}			± 100	nA
Zero Gate Voltage Drain Current $V_{DS} = 0.8 \text{ max Rating}, V_{GS} = 0\text{ V}$		I_{DSS}			250	μA
Zero Gate Voltage Drain Current $V_{DS} = 80\% V_{(BR)DSS}, V_{GS} = 0\text{ V}, T_J = 125^{\circ}\text{C}$		I_{DSS}			1000	μA
Drain-Source On-State Resistance (2) $V_{GS} = 10\text{ V}, I_D = 24\text{ A}$		$r_{DS(on)}$			0.055	Ω
Forward Transconductance (2) $V_{DS} = 15\text{ V}, I_D = 20\text{ A}, V_{DS} \geq I_{D(ON)} \times R_{DS(ON)} \text{ max}$		g_{fs}	9.0			S(Ω)
Input Capacitance	$V_{GS} = 0\text{ V}$	C_{iss}		2000	3000	pF
Output Capacitance	$V_{DS} = 25\text{ V}$	C_{oss}		1000	1500	
Reverse Transfer Capacitance	$f = 1\text{ MHz}$	C_{rss}		350	500	
Total Gate Charge	$V_{DS} = 80\% V_{(BR)DSS}$ $V_{GS} = 10\text{ V}, I_D = 30\text{ A}$ (Gate charge is essentially independent of operating temperature.)	Q_g			120	nC
Gate-Source Charge		Q_{gs}			-	
Gate -Drain Charge		Q_{gd}			-	
Turn-On Delay Time	$V_{dd} = 24\text{ V},$ $I_D = 20\text{ A},$ $R_G = 4.7\text{ }\Omega$ (Switching time is essentially independent of operating temperature.)	$t_{d(on)}$			35	ns
Rise Time		t_r			100	
Turn-Off Delay Time		$t_{d(off)}$			125	
Fall Time		t_f			100	

SOURCE-DRAIN DIODE RATINGS & CHARACTERISTICS ($T_J = 25^{\circ}\text{C}$ unless otherwise noted)

PARAMETERS / TEST CONDITIONS		SYMBOL	MIN.	TYP.	MAX	UNITS
Continuous Current		I_S			38	A
Pulsed Current (1)		I_{SM}			152	A
Forward Voltage (2) $I_F = I_S, V_{GS} = 0\text{ V}$		V_{SD}			2.0	V
Reverse Recovery Time $I_F = I_S, dI/dt = 100\text{ A}/\mu\text{S}$		t_{rr}			500	ns
Reverse Recovered Charge $I_F = I_S, dI/dt = 100\text{ A}/\mu\text{S}$		Q_{rr}			2.9	μC

(1) Pulsed width limited by maximum junction temperature.

(2) Pulse Test: Pulse width < 300 μsec . Duty cycle $\leq 2\%$.

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