Power MOSFET 5.1 Amps, 20 Volts

N-Channel TSOP-6

Features

- Ultra Low R_{DS(on)}
- Higher Efficiency Extending Battery Life
- Logic Level Gate Drive
- Diode Exhibits High Speed, Soft Recovery
- Avalanche Energy Specified
- I_{DSS} Specified at Elevated Temperature

Applications

- Power Management in portable and battery-powered products, i.e. computers, printers, PCMCIA cards, cellular and cordless
- Lithium Ion Battery Applications
- Notebook PC

MAXIMUM RATINGS ($T_C = 25^{\circ}C$ unless otherwise noted)

Rating	Symbol	Value	Unit
Thermal Resistance Junction-to-Ambient (Note 1) Total Power Dissipation @ T _A = 25°C Drain Current	R _{θJA}	244	°C/W
	P _d	0.5	Watts
- Continuous @ T_A = 25°C - Pulsed Drain Current ($t_p <$ 10 μs)	I _D	2.5	Amps
	I _{DM}	10	Amps
Thermal Resistance Junction-to-Ambient (Note 2) Total Power Dissipation @ T _A = 25°C Drain Current	R _{θJA}	128	°C/W
	P _d	1.0	Watts
- Continuous @ T _A = 25°C	I _D	3.6	Amps
- Pulsed Drain Current (t _p < 10 μs)	I _{DM}	14	Amps
Thermal Resistance Junction-to-Ambient (Note 3) Total Power Dissipation @ T _A = 25°C Drain Current	$egin{array}{c} R_{ hetaJA} \ P_{d} \end{array}$	62.5 2.0	°C/W Watts
- Continuous @ T_A = 25°C - Pulsed Drain Current ($t_p < 10 \ \mu s$)	I _D	5.1	Amps
	I _{DM}	2.0	Amps
Operating and Storage Temperature Range	T _J , T _{stg}	- 55 to 150	°C
Maximum Lead Temperature for Soldering Purposes for 10 seconds	TL	260	°C

- 1. Minimum FR-4 or G-10PCB, operating to steady state.
- Mounted onto a 2" square FR- 4 board (1" sq. 2 oz. cu. 0.06" thick single-sided), operating to steady state.
- Mounted onto a 2" square FR- 4 board (1" sq. 2 oz. cu. 0.06" thick single-sided), t < 5.0 seconds.



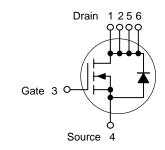
ON Semiconductor®

http://onsemi.com

5.1 AMPERES 20 VOLTS

 $R_{DS(on)} = 45 \text{ m}\Omega$

N-Channel



MARKING DIAGRAM

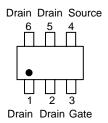


TSOP-6 CASE 318G STYLE 1



446 W Device CodeWork Week

PIN ASSIGNMENT



ORDERING INFORMATION

Device	Package	Shipping	
NTGS3446T1	TSOP-6	3000 Tape & Reel	

ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted)

CI	Symbol	Min	Тур	Max	Unit	
OFF CHARACTERISTICS						
Drain-to-Source Breakdown Voltage (V _{GS} = 0 Vdc, I _D = 0.25 mAdc) Temperature Coefficient (Positive)		V _{(BR)DSS}	20	- 22	-	Vdc mV/°C
Zero Gate Voltage Collector Current (V _{DS} = 20 Vdc, V _{GS} = 0 Vdc) (V _{DS} = 20 Vdc, V _{GS} = 0 Vdc, T _J = 85°C)		I _{DSS}	- -	- -	1.0 25	μAdc
Gate-Body Leakage Current (Vo	I _{GSS(f)}	- -	- -	100 -100	nAdc	
ON CHARACTERISTICS (Note	4)					
Gate Threshold Voltage I_D = 0.25 mA, V_{DS} = V_{GS} Temperature Coefficient (Negative)		V _{GS(th)}	0.6	0.85 -2.5	1.2 -	Vdc mV/°C
Static Drain-to-Source On-Res $(V_{GS} = 4.5 \text{ Vdc}, I_D = 5.1 \text{ Adc} $ $(V_{GS} = 2.5 \text{ Vdc}, I_D = 4.4 \text{ Adc} $	R _{DS(on)}	- -	36 44	45 55	mΩ	
Forward Transconductance (V _{DS}	S = 10 Vdc, I _D = 5.1 Adc)	9FS	-	12	-	mhos
DYNAMIC CHARACTERISTICS	3					
Input Capacitance		C _{iss}	-	510	750	pF
Output Capacitance	$(V_{DS} = 10 \text{ Vdc}, V_{GS} = 0 \text{ Vdc}, f = 1.0 \text{ MHz})$	C _{oss}	-	200	350	
Transfer Capacitance]	C _{rss}	-	60	100	
SWITCHING CHARACTERISTIC	CS (Note 5)					
Turn-On Delay Time		t _{d(on)}	-	9.0	16	ns
Rise Time	(V _{DD} = 10 Vdc, I _D = 1.0 Adc,	t _r	-	12	20	
Turn-Off Delay Time	$V_{GS} = 4.5 \text{ Vdc}, R_{G} = 6.0 \Omega$	t _{d(off)}	-	35	60	
Fall Time		t _f	-	20	35	
Gate Charge		Q _T	-	8.0	15	nC
	$(V_{DS} = 10 \text{ Vdc}, I_D = 5.1 \text{ Adc}, V_{GS} = 4.5 \text{ Vdc})$	Q _{gs}	-	2.0	-	
	l dg ne res,	Q _{gd}	-	2.0	-	
SOURCE-DRAIN DIODE CHAR	RACTERISTICS	•	•	•	•	•
Forward On-Voltage (Note 4)	$(I_S = 1.7 \text{ Adc}, V_{GS} = 0 \text{ Vdc})$ $(I_S = 1.7 \text{ Adc}, V_{GS} = 0 \text{ Vdc}, T_J = 85^{\circ}\text{C})$	V _{SD}		0.74 0.66	1.1 -	Vdc
Reverse Recovery Time		t _{rr}	-	20	-	ns
	// 4744 V 0V/	t _a	-	11	-	1
	$(I_S = 1.7 \text{ Adc}, V_{GS} = 0 \text{ Vdc}, \\ di_S/dt = 100 \text{ A/}\mu\text{s})$	t _b	-	9.0	-	
Reverse Recovery Stored Charge		Q _{RR}	-	0.01	-	μС

Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2%.
 Switching characteristics are independent of operating junction temperature.

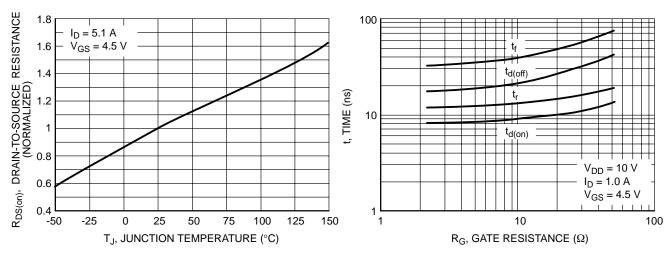
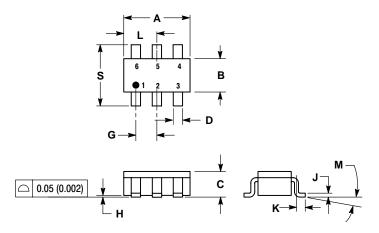


Figure 1. On-Resistance Variation with Temperature

Figure 2. Resistive Switching Time Variation vs. Gate Resistance

PACKAGE DIMENSIONS

TSOP-6 CASE 318G-02 **ISSUE H**



NOTES

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: MILLIMETER.
 MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.

	MILLIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
Α	2.90	3.10	0.1142	0.1220
В	1.30	1.70	0.0512	0.0669
C	0.90	1.10	0.0354	0.0433
D	0.25	0.50	0.0098	0.0197
G	0.85	1.05	0.0335	0.0413
Н	0.013	0.100	0.0005	0.0040
J	0.10	0.26	0.0040	0.0102
K	0.20	0.60	0.0079	0.0236
L	1.25	1.55	0.0493	0.0610
M	0 °	10°	0 °	10°
S	2.50	3.00	0.0985	0.1181

STYLE 1:

PIN 1. DRAIN

- DRAIN
- 3. GATE 4. SOURCE
- 5. DRAIN

ON Semiconductor and War registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer.

PUBLICATION ORDERING INFORMATION

Literature Fulfillment:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA

Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada

Email: ONlit@hibbertco.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

JAPAN: ON Semiconductor, Japan Customer Focus Center 2-9-1 Kamimeguro, Meguro-ku, Tokyo, Japan 153-0051

Phone: 81-3-5773-3850 Email: r14525@onsemi.com

ON Semiconductor Website: http://onsemi.com

For additional information, please contact your local

Sales Representative.