

MOS Field Effect Power Transistor

NP10N45CHB,NP10N45DHB,NP10N45EHB

SWITCHING N-CHANNEL POWER MOS FET INDUSTRIAL USE

DESCRIPTION

This product is N-Channel MOS Field Effect Transistor designed for high current switching applications.

FEATURES

- Channel temperature 175 degree rated
- Super Low On-State Resistance
 RDS(on)1 = 0.5Ω Max. (VGS=10V,ID=5A)
- Low Ciss Ciss = 1600pF Typ.
- · Built-in Gate Protection Diode

ORDERING INFORMATION

PART NUMBER	PACKAGE
NP10N45CHB	TO-220AB
NP10N45DHB	TO-262AA
NP10N45EHB	TO-220SMD

ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

Drain to Source Voltage	Voss	450	٧
Gate to Source Voltage	Vgss	± 30	V
Drain Current(DC)	ID(DC)	± 10	A
Drain Current(pulse)*	D(pulse)	± 40	Α
Total Power Dissipation(Ta=25°C)	PT	1.5	W
Total Power Dissipation(Tch=25°C)	Рт	184	W
Single Avalanche Current	las	10	Α
Single Avalanche Energy	Eas	143	mJ
Channel Temperature	Tch	175	°C
Storage Temperature	T_{stg}	- 55 to + 175	°C

^{*} PW≤10μs,Duty Cycle≤1%

THERMAL RESISTANCE

Channel to Case	Rth(ch-c)	0.82	°C/W
Channel to Ambient	Rth(ch-a)	100	°C/W

The diode connected between the gate and source of the transistor serves as a protector against ESD. When this deveice acutally used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

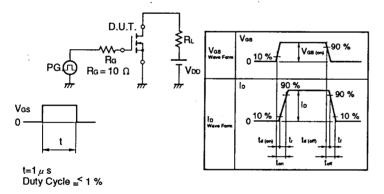
This information in this document is being issued in advance of the production cycle for the device. The parameter for the device may change before final production or NEC Corporation, at its own discretion, may withdraw the device prior to its production.

^{**}Starting Tch=25°C,RG=25Ω,TGS20V→0

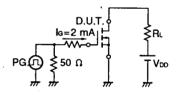
ELECTRICAL CHARACTERISTICS(Ta=25°C)

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CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Drain to Source	RDS(on)	Vgs=10V,lp= 10A		0.4	0.5	Ω
On-state Resistance						
Gate to Source Cutoff Voltage	V _{GS(off)}	Vos=10V,lo=1mA	2.5		3.5	٧
Forward Transfer Admittance	y _{fs}	Vos=10V,lo=5A	4.0			S
Drain Leakage Current	loss	Vps=450V,Vgs=0			100	μΑ
Gate to Source Leakage Current	lgss	Vgs=±30V,Vps=0			±100	nA
Input Capacitance	Ciss	Vps=10V		1600	3600	pF
Output Capacitance	Coss	V _G s=0		310	470	pF
Reverse Transfer Capacitance	Crss	f=1MHz		30	60	pF
Turn-On Delay Time	td(on)	lo=5A		30	66	nS
Rise Time	tr	V _{GS(on)} =10V		20	50	_nS
Turn-Off Delay Time	td(off)	V _{DD} =150V	`	80	160	nS
Fall Time	tr	Rg=10 Ω Rt=30 Ω		20	50	nS
Total Gate Charge	Q _G	lo=10A		42	63	nC
Gate to Source Charge	Qgs	V _{DD} =400V		10		nC
Gate to Drain Charge	Q _{GD}	V _G s=12V		20		nC
Body Diode Forward Voltage	V _{F(S-D)}	I _F =10A,V _{GS} =0		1.0		V
Reverse Recovery Time	trr	Ir=10A,Vgs=0		350		ns
Reverse Recovery Charge	Qrr	di/dt=50A/μs		1.5		μC

Test Circuit 1 Switching Time

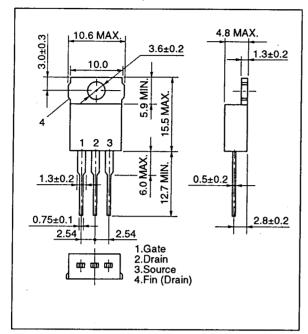


Test Circuit 2 Gate Charge

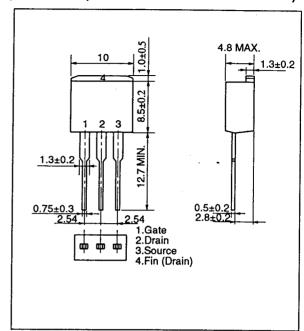


Package Dimensions (in millimeter)

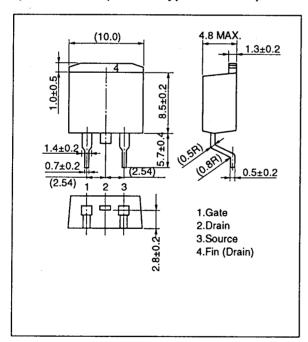
1)TO-220AB(MP-25)



2)TO-262AA(TO-220 Fin Cut:MP-25 Fin Cut)



3)TO-220SMD(JEDEC type:MP-25ZJ)



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Anti-radioactive design is not implemented in this product.