

NB10EP143

(9-bit Hold Register)

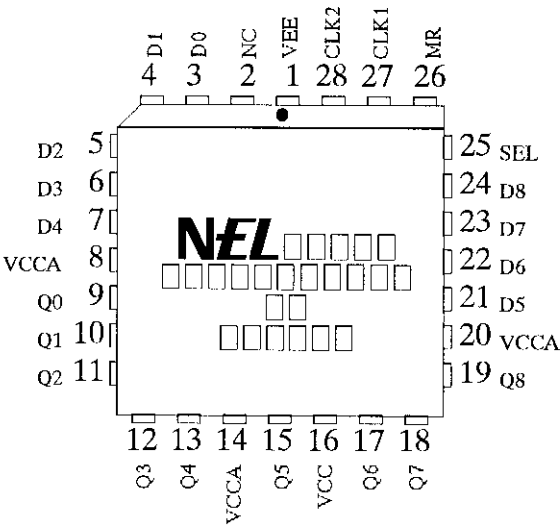
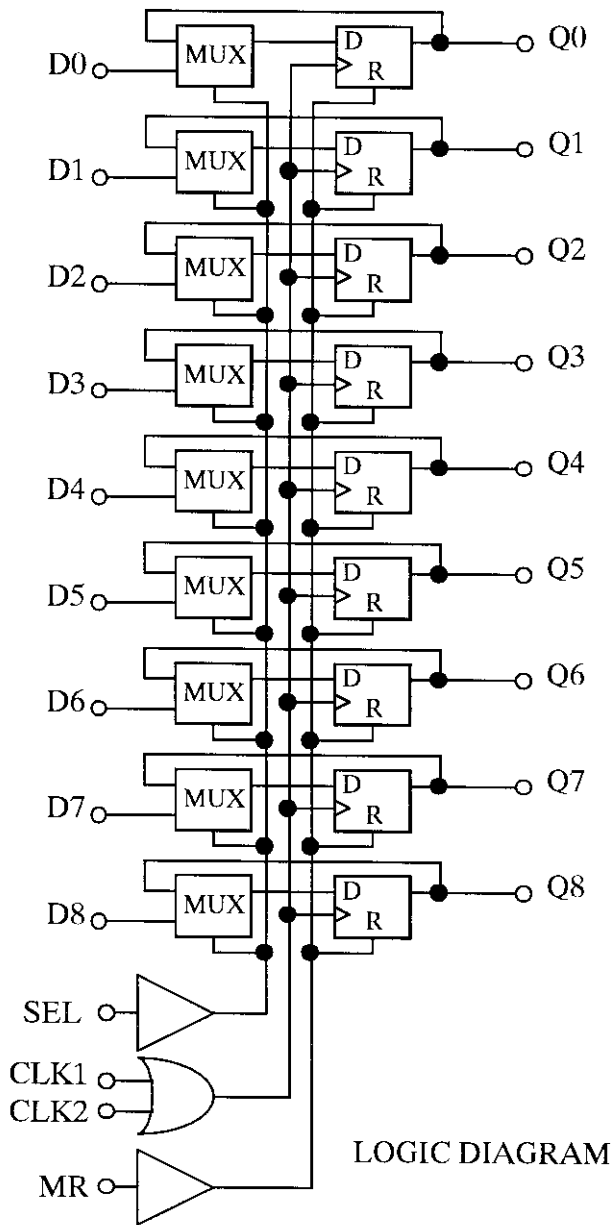
PINACLE

PIN NAME

Pin	Function
D0 - D8	Data Inputs
SEL	Mode Select Input
CLK1, CLK2	Clock Inputs
MR	Master Reset
Q0 - Q8	Data Outputs
NC	No Connection

FUNCTIONS

SEL	Mode
L	Load
H	Hold



*All VCC and VCCA pins are tied together on the die.



NB10EP143

PINACLE

(9-bit Hold Register)

DC Characteristics VEE=-5.2V±5% Ta=25°C

	NB10EP143J		
	MIN	TYP	MAX
I _{IH} (μA)			150
I _{EE} (mA)		43	60

AC Characteristics VEE=-5.2V±5% Ta=25°C

	NB10EP143J		
	MIN	TYP	MAX
f _{MAX} (MHz)	1100	1400	
t _{pLH} /t _{pHl} (ps) CLK	475	650	800
MR	475	650	850
t _s (ps) Setup Time	0	-175	
t _H (ps) Hold Time	350	175	
t _{RR} (ps) Reset Recovery Time	750	550	
t _{PW} (ps) Min. Pulse Width	400		
t _{skew} (ps)		65	
t _r /t _f (ps) :20-80%	300	450	700