

NEL
SELIC

Pin diagram of the NLB6203 integrated circuit. The chip is a square package with pins numbered 1 through 24. The top pins (13-18) are labeled D2A, D1C, D1B, D1A, Q1, and $\overline{Q1}$. The bottom pins (6-11) are labeled D3C, D4A, D4B, D4C, $\overline{Q4}$, and Q4. The left pins (12-7) are labeled D2B, D2C, V_{EE} , V_{CCA} , D3A, and D3B. The right pins (19-24) are labeled Q2, $\overline{Q2}$, V_{CCA} , V_{CC} , Q3, and $\overline{Q3}$. The center of the chip is labeled **NLB6203**. A PKG INDEX pin is located at the bottom left corner.

NLB6203

Quad 3 input AND/NAND

DC Characteristics

$V_{EE} = -4.5V \pm 0.3V$, $V_{CC} = V_{CCA} = GND$, $V_{TT} = -2V$, $T_C = 0^{\circ}C$ to $+85^{\circ}C$, $R_T = 50\Omega$ to V_{TT}

Characteristics	Symbol	Min.	Typ.	Max.	Unit
Power supply current	I_{EE}	80	150	200	mA

AC Characteristics

$V_{EE} = -4.5V \pm 0.3V$, $V_{CC} = V_{CCA} = GND$, $V_{TT} = -2V$, $T_C = 0^{\circ}C$ to $+85^{\circ}C$, $R_T = 50\Omega$ to V_{TT}

Characteristics	Symbol	Min.	Typ.	Max.	Unit
Propagation delay to output	t_{PLH}		430	570	ps
	t_{PHL}		440	590	
Rise time 20% to 80%	t_r		130	170	
Fall time 80% to 20%	t_f		120	160	

Typical Output Voltage Swing

$V_{EE} = -4.5V$, $V_{CC} = V_{CCA} = GND$, $V_{TT} = -2V$, $R_T = 50\Omega$ to V_{TT}

