



Dolby Pro Logic Surround Decoder With I²C BUS I/F

NJW1106

DATA SHEET

■ GENERAL DESCRIPTION

The **NJW1106** is a surround processor including all the necessary circuits for Dolby Pro Logic Surround decoding and digital delay.

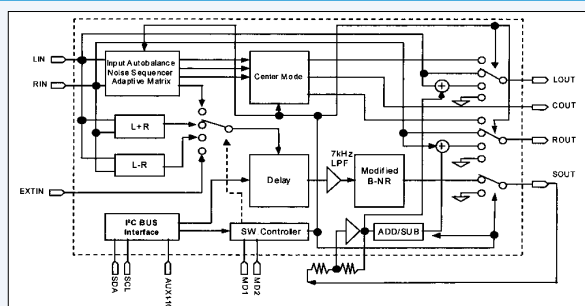
The **NJW1106** all of internal statuses are controlled by I²C BUS.

In addition to the Dolby Pro Logic Surround mode, the **NJW1106** easily performs other surround functions such as Hall, Live, Disco and other similar applications.

■ FEATURES

- Dolby Pro Logic Surround Decoding
- Digital Delay on chip
- Other Surround Mode
- I²C BUS Interface (SDA, SCL)
- Bi-CMOS Technology
- Package Outline QFP80 QFP100

■ BLOCK DIAGRAM



Note: The word "DOLBY", "Pro Logic" and double D mark are registered trademarks of Dolby Laboratories.

Purchase of I²C components of New Japan Radio Co., Ltd or one of its sublicensed Associated Companies conveys a license under the Philips I²C Patent Rights to use these components in an I²C system, provided that the system conforms to the I²C Standard specification as defined by Philips.