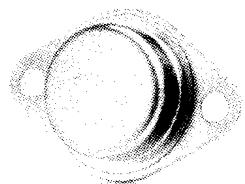


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TEL:805-498-2111 FAX:805-498-3804 WEB:<http://www.semtech.com>

FEATURES

- Guaranteed Power Dissipation
50 Watts @ 80°C Case
- Guaranteed input-output differential:
+ 2.6 Volts
- Low noise, band gap reference
- Remote sense capability
- Sample power cycled burn-in
- Guaranteed thermal resistance junction to case: 0.9°C/W
- TO-3 package available
- Grounded case

DESCRIPTION

The LAS 1900 Series voltage regulators are monolithic integrated circuits designed for use in applications requiring a well regulated positive output voltage. Outstanding features include full power usage up to 5.0 amperes of load current, internal current limiting, thermal shutdown, and safe area protection on the chip, providing protection of the series pass Darlington, under most operating conditions.

A low-noise, temperature stable band gap reference is the key design factor insuring excellent temperature regulation of the LAS 1900 Series. This, coupled to a very low output impedance, insures superior load regulation.

The LAS 19U, a four terminal, adjustable regulator is available with an output range from + 4 to + 30 Volts, providing remote sense capability with a single potentiometer.

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	MINIMUM	MAXIMUM	UNITS
Input Voltage	V_{IN}		30(35) ⁽¹⁾⁽²⁾	Volts
Power Dissipation	P_D		Internally Limited ⁽³⁾	
Thermal Resistance Junction To Case	θ_{JC}		0.9	°C/Watt
Operating Junction Temperature Range LAS 1900 LAS 19U	T_J	- 55	150	°C
Storage Temperature Range	T_{STG}	- 65	150	°C
Lead Temperature (Soldering, 60 Seconds Time Limit)	T_{LEAD}		300	°C

⁽¹⁾ Short circuit protection is only assured to V_{IN} max. Value of 30V applies to V_O of + 5V to + 12V. Value of 35V applies to V_O of 15V and LAS 19U.

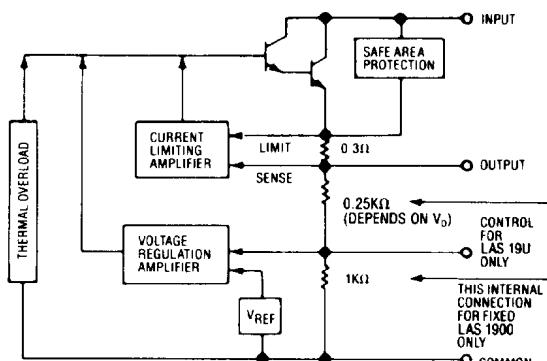
⁽²⁾ In case of short circuit with input-output voltages approaching V_{IN} max. regulator may require the removal of the input voltage to restart.

⁽³⁾ For operation above 80°C T_{CASE} , derate @ 1.11 watt °C.

DEVICE SELECTION GUIDE

V_{OUT}	V_{OUT} TOLERANCE		
	+ 5%	+ 5%, - 3%	+ 2%
5	LAS 1905	LAS 1905B	LAS 19A05
12	LAS 1912	LAS 1912B	LAS 19A12
15	LAS 1915	LAS 1915B	LAS 19A15
4 to 30	LAS 19U (Adjustable/Remote Sense)		

BLOCK DIAGRAM



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ELECTRICAL CHARACTERISTICS

Unless otherwise specified: $V_1 = V_0 + 3$ Volts, $V_2 = V_0 + 10$ Volts,
 $V_3 = V_0 + 15$ Volts, or the maximum input, whichever is less.

		Test Conditions			Test Limits		
Parameter	Symbol	V_{IN}	I_o	T_J	Min	Max	Units
Output Voltage ² LAS 1900 ¹ LAS 1900B ¹ LAS 19A00 ¹ LAS 19U ⁵	V_0	V_1 to V_2	10mA to 5.0A	25°C	0.95 V_0 0.97 V_0 0.98 V_0 4.0	1.05 V_0 1.05 V_0 1.02 V_0 30.0	Volts
Input-Output Differential	$V_{IN}-V_0$		5.0A	0-125°C	2.6		Volts
Line Regulation ²	REG _(LINE)	V_1 to V_3	3.0A	25°C		1.0	% V_0
Load Regulation ²	REG _(LOAD)	V_1	10mA to 5.0A	25°C		0.6	% V_0
Quiescent Current	I_Q	V_1	10mA	25°C		25.0	mA
Quiescent Current Line	I_Q (LINE)	V_1 to V_2	10mA	25°C		5.0	mA
Quiescent Current Load	I_Q (LOAD)	V_1	10mA to 5.0A	25°C		5.0	mA
Current Limit ²	I_{LIM}	$V_0 + 5V$		25°C		15	Amps
Temperature Coefficient	T_C	V_1	0.1A	0-125°C		0.03	% V_0 /°C
Output Noise ³ Voltage	V_N	V_1	0.1A	0-125°C		10	$\mu V_{rms}/V$
Ripple Attenuation ⁴	R_A	$V_0 + 5V$	2.0A	0-125°C	60		dB
Control Voltage LAS 19U	V_C	V_1 to V_2	10mA	25°C	3.6	4.0	Volts
Power Dissipation	P_D	$V_{IN}-V_{OUT}$ 2.6V to 10.0V	10mA to 5.0A	0-125°C	50		Watts

⁽¹⁾ Nominal output voltages are specified under Device Selection Guide.⁽²⁾ Low duty cycle pulse testing with Kelvin connections required. Die temperature changes must be accounted for separately.⁽³⁾ BW = 10Hz - 100KHz⁽⁴⁾ Ripple attenuation is specified for a 1VRMS, 120Hz, input ripple.

Ripple attenuation is minimum of 60 dB at 5V output and is 1 dB less for each volt increase in the output voltage.

⁽⁵⁾ $V_0 = V_C (1 + R1/R2)$

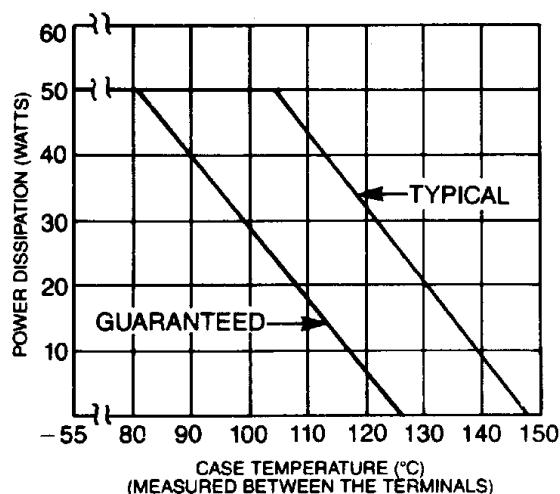
R1 = Resistance from output to control

R2 = Resistance from control to common

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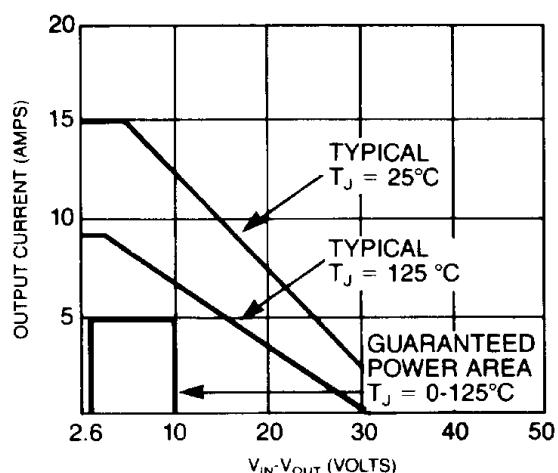
OPERATIONAL DATA

POWER DERATING

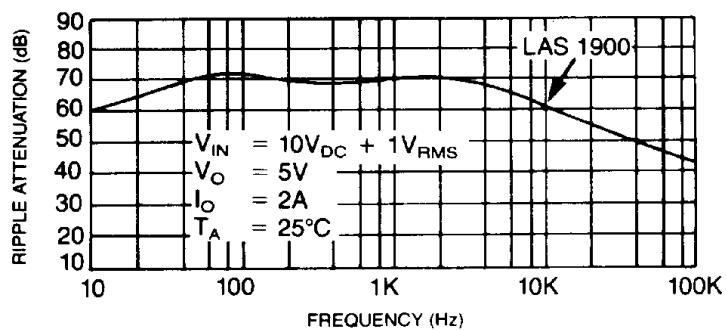
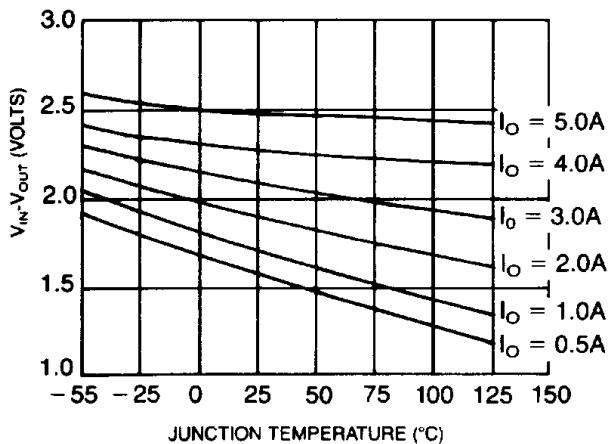
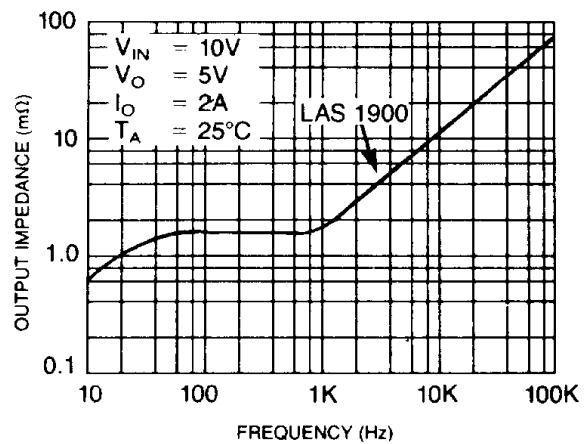


(MEASURED BETWEEN THE TERMINALS)

CURRENT LIMIT



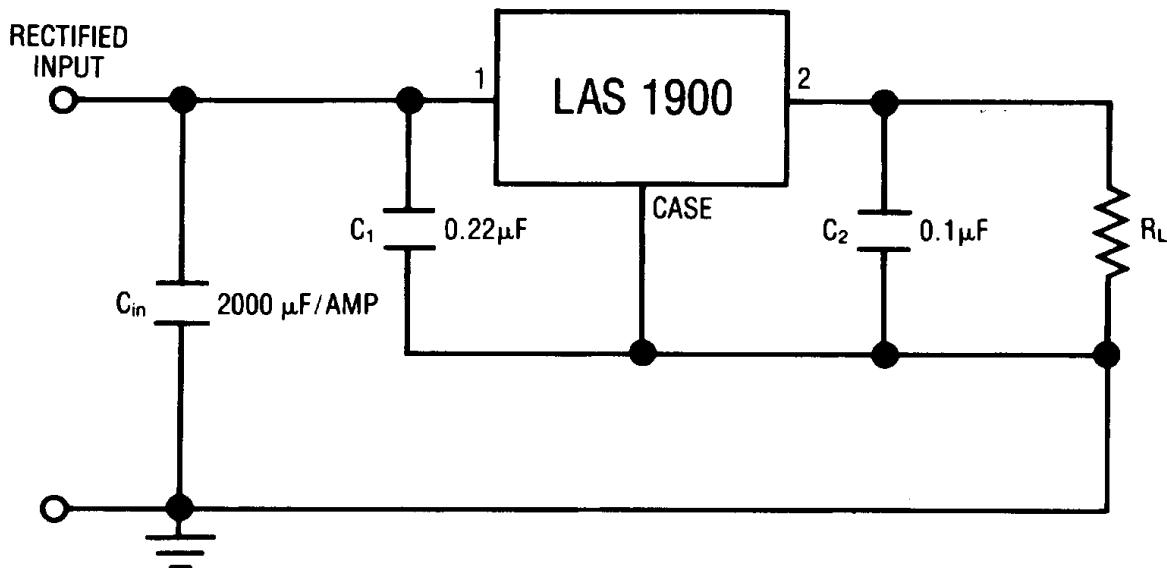
TYPICAL RIPPLE ATTENUATION VS FREQUENCY

TYPICAL INPUT-OUTPUT VOLTAGE DIFFERENTIAL
VS JUNCTION TEMPERATURETYPICAL OUTPUT
IMPEDANCE VS FREQUENCY

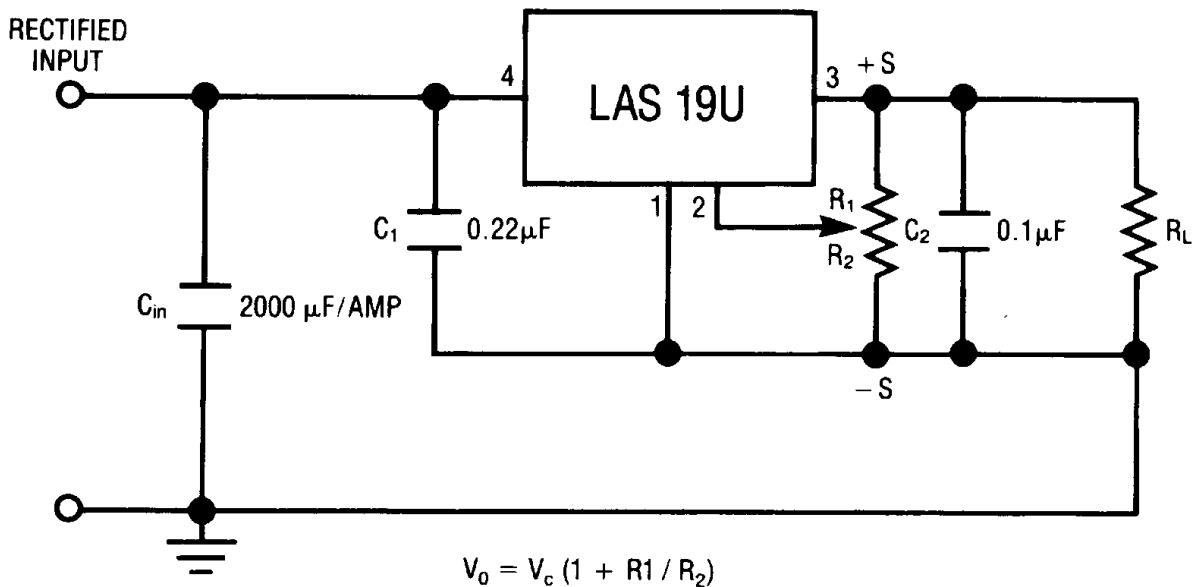
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TYPICAL APPLICATIONS

FIXED VOLTAGE REGULATOR¹



ADJUSTABLE VOLTAGE REGULATOR^{1,2}

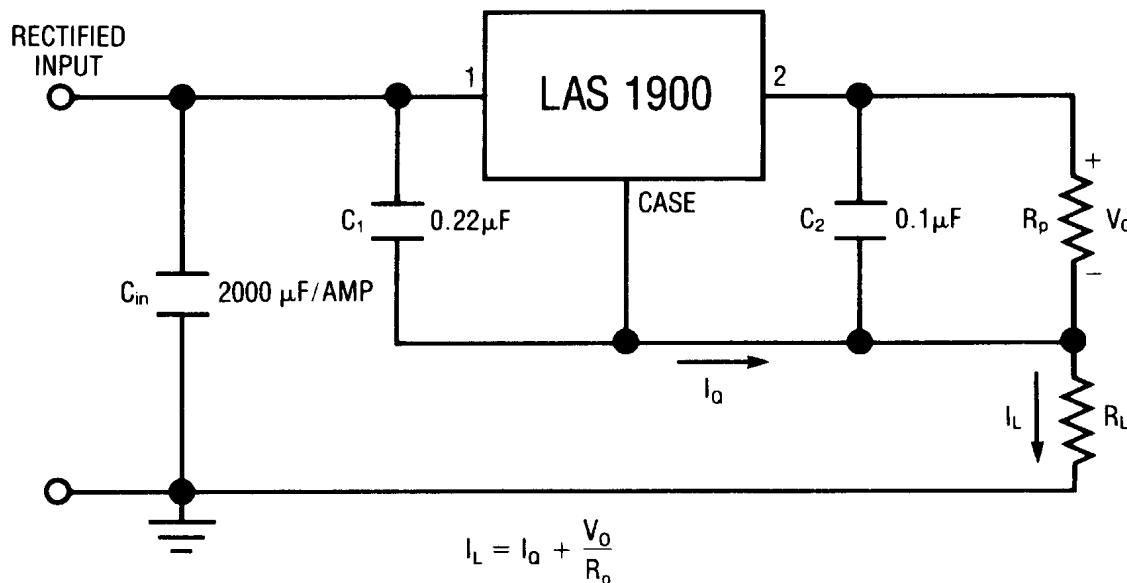
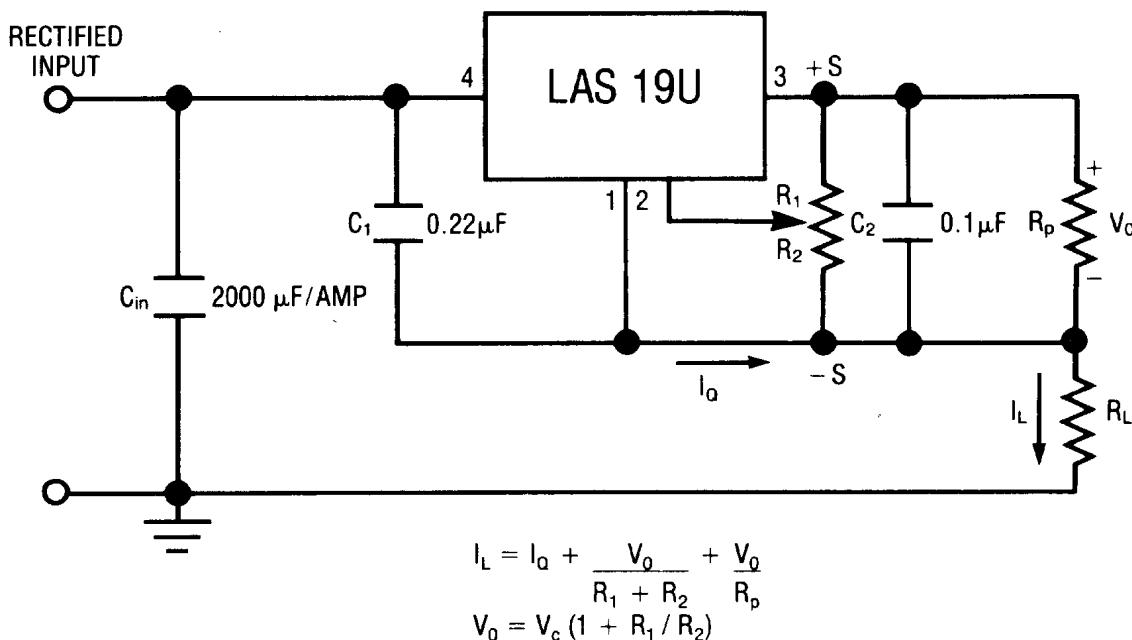


¹ C₁ and C₂ should be placed as close as possible to the regulator.

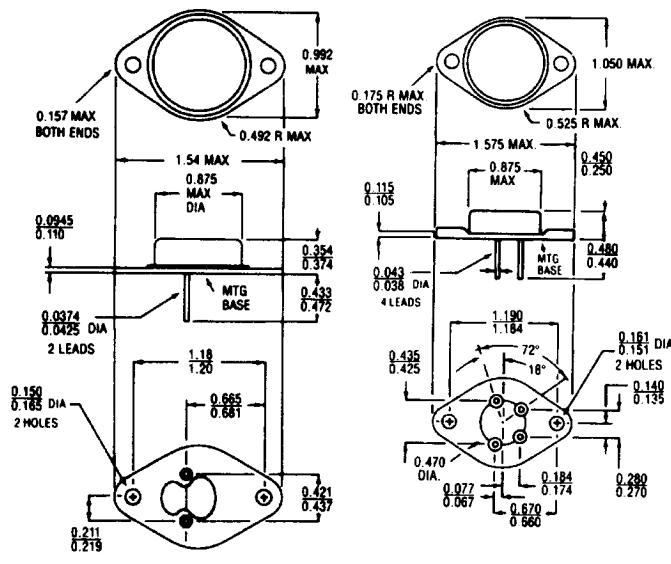
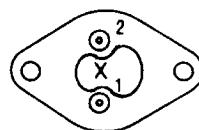
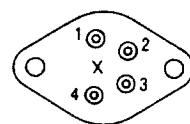
² $\frac{V_0}{R_1 + R_2} \geq 10 \text{ mA}$

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TYPICAL APPLICATIONS

FIXED CURRENT REGULATOR¹ADJUSTABLE CURRENT REGULATOR^{1,2}¹ C_1 and C_2 should be placed as close as possible to the regulator.² $\frac{V_0}{R_1 + R_2} \geq 10$ mA

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DEVICE OUTLINE**TO-3****Bottom View****Bottom View****LAS 19XX**

- 1 – Input
- 2 – Output
- Case is common

LAS 19U

- 1 – Common
- 2 – Control
- 3 – Output
- 4 – Input
- Case is common

NOTE: Case temperature measured at point X.
All dimensions are in inches.