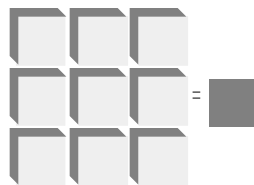


LSI/CSI



LS7314-7315



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AC POWER CONTROLLERS

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FEATURES:

- 10 I/Os to Select/Indicate up to ten Power Levels
- Inputs activated by Touch or Pushbutton Switch
- Output switches pure and precise AC Power to Load
- Operates with 50Hz/60Hz line frequency
- Rugged, latchup-free process technology
- +10V to +14V DC power supply (Vss-VDD)
- 16-Pin Plastic DIP, 16-Pin Wide-Body SOIC (See Figure 1)

APPLICATIONS:

- Universal and shaded-pole motor speed control for modern appliance designs. Eliminates awkward mechanical switch assemblies and multi-tapped motor windings. (See Fig. 4C)
- Multi-level light switches. (See Fig. 4D)

DESCRIPTION:

The LS7314-LS7315 are MOS integrated circuits specifically designed for appliance motor speed control, lighting control, etc. I/Os (PLs) are provided for selecting/indicating from one to ten power levels. The LS7315 is designed for pushbutton control. The LS7314 is designed for touch control. (See Figures 4A and 4B)

INPUT/OUTPUT DESCRIPTION:

PL1-PL10 (Pins 1-8, 15, 16)

Ten inputs/outputs for selecting ten output phase angles (power levels). When no power level is selected (such as after system power-up) PL1-PL10 all act as inputs. When a power level is selected by applying a logic zero at one of these inputs for T_H time (See Dynamic Characteristics), the output (TRIG) is turned on at the phase angle selected and the PL input switches status to become an output in order to drive a display, such as an LED. It switches back to the input state when another PL input is activated or when OFF is selected. (See Note 1)

OFF (Pin 10)

If TRIG is on, a logic 0 applied to the OFF input for T_H time turns TRIG output off and switches the selected PL back to the input state. If TRIG is off, activating OFF leaves the circuit unaffected. Following an OFF activation, TRIG can be turned on by applying any PL input. (See Note 1)

SYNC (Pin 12)

Input for PLL reference frequency (50Hz/60Hz). All internal clock frequencies are synchronized with the SYNC input.

CAP (Pin 11)

Input for component connection for the PLL filter capacitor.

CONNECTION DIAGRAM - TOP VIEW

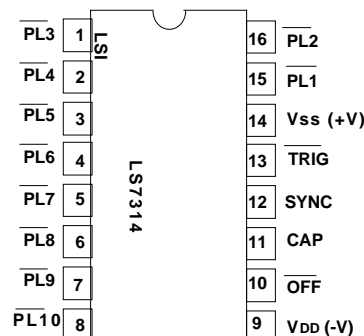


FIGURE 1

TRIG (Pin 13)

This output is designed to drive a triac in series with the load and control its firing angle with respect to the AC line. A 1ms output pulse width is provided to enable the triac to fire even with inductive loads which cause significant phase delays between voltage and current.

Vss (Pin 14)

Supply voltage positive terminal.

VDD (Pin 9)

Supply voltage negative terminal (ground).

NOTE 1: LS7315 has an internal pullup resistor on this input and LS7314 does not. (See DC Electrical Characteristics.)

FIGURE 2. TRIG PHASE ANGLE θ

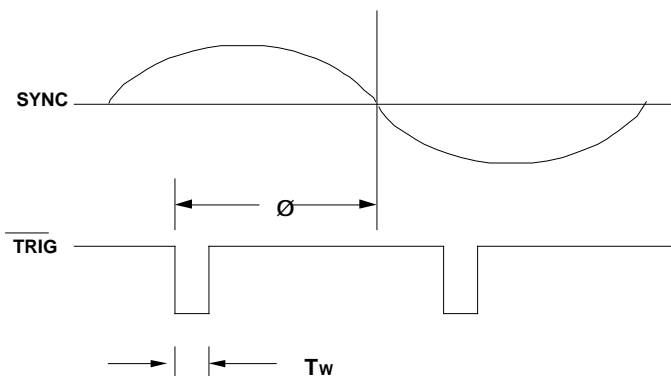


FIGURE 3. LS7314-LS7315 BLOCK DIAGRAM

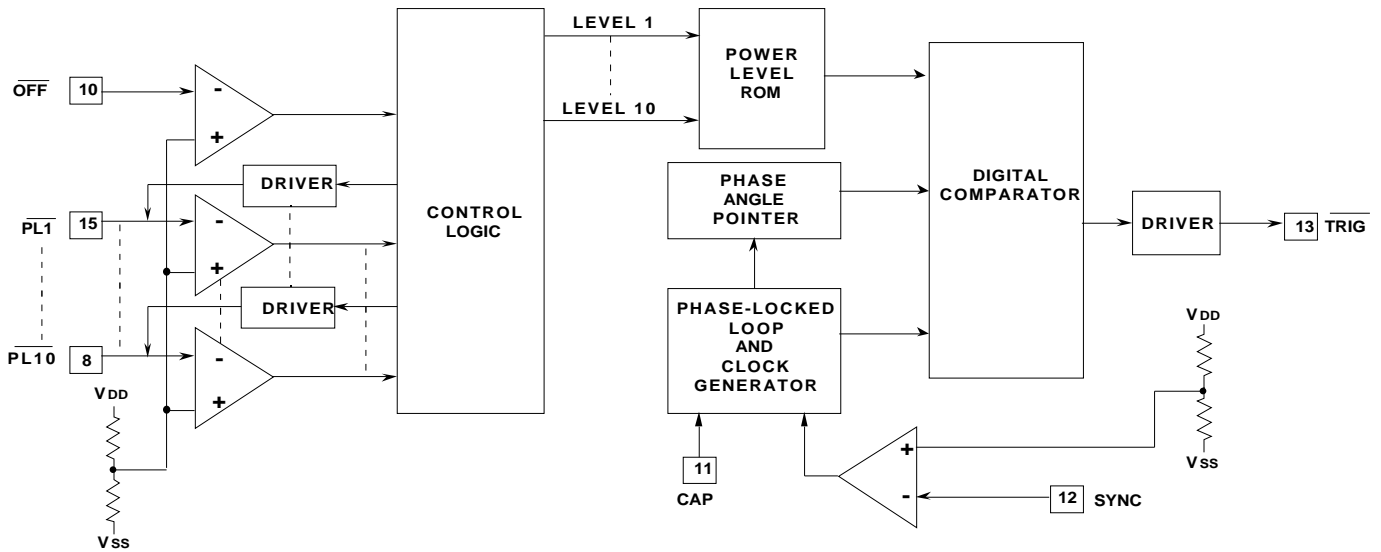


TABLE 1. TRIG PHASE ANGLES, \emptyset (1)

PL	\emptyset *	% PWR (2)	\emptyset **	%PWR (2)
1	43°	8	81°	40
2	55°	16	105°	67
3	68°	26	116°	77
4	81°	40	120°	80
5	95°	55	123°	83
6	108°	69	129°	87
7	122°	82	134°	90
8	134°	90	160°	99
9	147°	96	167°	99
10	160°	99	175°	99

*LS7314 & LS7315

**LS7315-51

- (1) The TRIG Phase Angles shown in TABLE 1 are referenced to the SYNC input. \emptyset values are mask programmable.
- (2) The percentage of Full Power delivered to a resistive load by the Triac Switch.

FIGURE 4C and 4D NOTES

- NOTE 1:** Value for 5mA LEDs.
- NOTE 2:** Use zener which produces 6.2V \pm 5% at 500 μ A or less, i. e. P/N MZ4627.
- NOTE 3:** R6-C6 Snubber Network may be required for large inductive loads.
- NOTE 4:** Tie all unused touch inputs together and return to Vss through 100K Ohm, 1/4W resistor.
- NOTE 5:** The AC Mains Polarity shown is required for Touch Control applications.

FIGURE 4C VALUE TABLE

CONDITION	C2(μ F)
(1), (3)	0.47
(1), (4)	0.33
(2), (3)	0.68
(2), (4)	0.47

(1) No LEDs (2) 5mA LEDs
(3) 115VAC, C2 = 200V (4) 220VAC, C2 = 400V

FIGURE 4A. PUSHBUTTON INTERFACE FOR LS7315

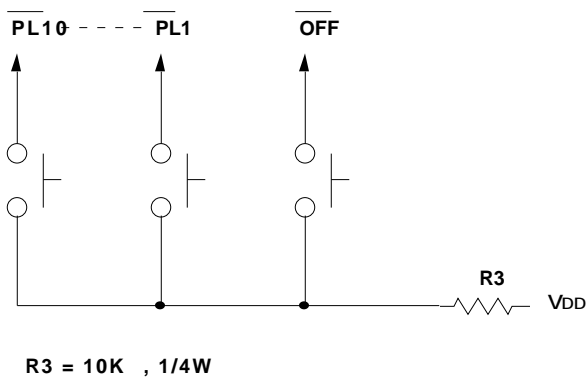
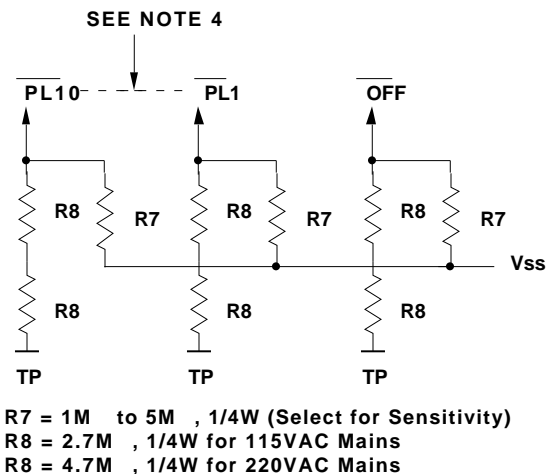


FIGURE 4B. TOUCH INTERFACE FOR LS7314



[illegible]

R1 = 82
R2 = 1.5M
R4 = 560 (See Note 1)
R5 = 10K
R6 = 1.8K , 1W (2W) *
R9 = 100
Z1 = 13V \pm 5%, 1W
Z2 = 6.2V, 1/4W (See Note 2)

6.2V LED DRIVER

115V AC MAINS

200V 0.15μF

100μH

INCANDESCENT LAMP LOAD

13V 1W

1N4148

1.5M

2W 200V 0.68μF

Q40044L4

MT1

MT2

220μF

.047μF

470pF

.047μF

9 V_{DD}

12 SYNC

13 TRIG

11 CAP

14 V_{SS}

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PL10

PL1

OFF

8

15

10

Use Fig. 4A or Fig. 4B Interface Circuits

1K

10K

6.2V, 1/4W (See Note 2)

MAXIMUM RATINGS:

PARAMETER	SYMBOL	VALUE	UNIT
Storage Temperature	T _{STG}	-65 to +150	°C
Operating Temperature	T _A	0 to +80	°C
DC Supply Voltage	V _{SS} -V _{DD}	+20	V
Any Input Voltage	V _{IN}	V _{SS} -20 to V _{SS} +0.5	V

DC ELECTRICAL CHARACTERISTICS:(T_A = 25°C, all voltages referenced to V_{DD})

	SYMBOL	MIN	TYP	MAX	UNIT	CONDITION
Supply Voltage	V _{SS}	+10	+12	+14	V	-
Supply Current	I _{DD}	-	1.2	2	mA	V _{SS} =12V, outputs off

Input Voltage:

SYNC , LO	V _{ISL}	0	-	1/3V _{SS}	V	-
SYNC , HI	V _{ISH}	2/3V _{SS}	-	V _{SS}	V	-
All other inputs , LO	V _{IL}	0	-	1/4V _{SS}	V	-
All other inputs , HI	V _{IH}	1/2V _{SS}	-	V _{SS}	V	-

Input Current:

SYNC Input	I _H	-	-	110	μA	With Series 1.5M Resistor to 115VAC
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Input Pull-up Resistance:

For LS7315

PL , OFF	R _{IN}	50	100	200	K	-
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Output Voltage:

TRIG , HI	V _{OH}	V _{SS}	-	-	V	-
TRIG , LO	V _{OL}	-	V _{SS} -8	-	V	-

Output Current:

TRIG , Sink	I _{OS}	25	-	-	mA	V _{SS} =+12V V _{OL} =V _{SS} -4V
PL Source	I _{OPL}	5	-	-	mA	V _{OPL} =V _{SS} -1V

DYNAMIC CHARACTERISTICS:

	SYMBOL	MIN	TYP	MAX	UNIT	CONDITION
SYNC frequency	f _s	40	-	70	Hz	-
PL , OFF hold time	T _H	50	-	infinite	ms	60Hz SYNC
	T _H	60	-	infinite	ms	50Hz SYNC
TRIG Pulse Width	T _w	-	1.0	-	ms	60Hz SYNC
	T _w	-	1.2	-	ms	50Hz SYNC

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