D961, OCTOBER 1979 - REVISED SEPTEMBER 1990

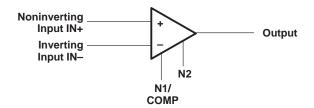
- Low Input Currents
- Low Input Offset Parameters
- Frequency and Transient Response Characteristics Adjustable
- Short-Circuit Protection
- Offset-Voltage Null Capability
- No Latch-Up
- Wide Common-Mode and Differential Voltage Ranges
- Same Pin Assignments as uA709
- Designed to be Interchangeable with National Semiconductor LM101A and LM301A

## description

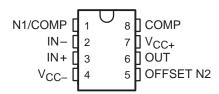
The LM101A, LM201A, and LM301A are highperformance operational amplifiers featuring very low input bias current and input offset voltage and current to improve the accuracy of highimpedance circuits using these devices. The high common-mode input voltage range and the absence of latch-up make these amplifiers ideal for voltage-follower applications. The devices are protected to withstand short circuits at the output. The external compensation of these amplifiers allows the changing of the frequency response (when the closed-loop gain is greater then unity) for applications requiring wider bandwidth or higher slew rate. A potentiometer may be connected between the offset-null inputs (N1 and N2), as shown in Figure 7, to null out the offset voltage.

The LM101A is characterized for operation over the full military temperature range of  $-55^{\circ}$ C to  $125^{\circ}$ C, the LM201A is characterized for operation from  $-25^{\circ}$ C to  $85^{\circ}$ C, and the LM301A is characterized for operation from  $0^{\circ}$ C to  $70^{\circ}$ C.

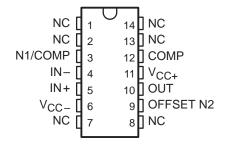
### symbol



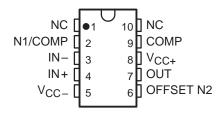
# D, JG, OR P PACKAGE (TOP VIEW)



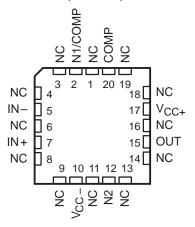
## LM101A W FLAT PACKAGE (T0P VIEW)



## LM101A U FLAT PACKAGE (T0P VIEW)



## LM101A FK CHIP-CARRIER PACKAGE (T0P VIEW)



NC - No internal connection



#### **AVAILABLE OPTIONS**

	.,,	PACKAGE							
TA	V <sub>IO</sub> MAX at 25°C	SMALL OUTLINE	CHIP CARRIER	CERAMIC DIP	PLASTIC DIP	FLAT PACK	FLAT PACK		
		(D)	(FK)	(JG)	(P)	(U)	(W)		
0°C to 70°C	7.5 mV	LM301AD	_	_	LM301AP	_	_		
−25 °C to 85 °C	2 mV	LM201AD	-	_	LM201AP	_	_		
−55°C to 125°C	2 mV	LM101AD	LM101AFK	LM101AJG	LM101AP	LM101AU	LM101AW		

The D package is available taped and reeled. Add the suffix R to the device type, (i.e., LM301ADR).

## absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

		LM101A	LM201A	LM301A	UNIT	
Supply voltage V <sub>CC+</sub> (see Note 1)	22	22	18	V		
Supply voltage V <sub>CC</sub> – (see Note 1)		-22	-22	-18	V	
Differential input voltage (see Note 2)	ut voltage (see Note 2) ±30 ±30					
Input voltage (either input, see Notes 1 and 3)		±15	±15	±15	V	
Voltage between either offset null terminal (N1/N2) and V <sub>CC</sub>	-0.5 to 2	-0.5 to 2	-0.5 to 2	V		
Duration of output short-circuit (see Note 4)		unlimited	unlimited	unlimited		
Continuous total power dissipation		See Dissipation Rating Table				
Operating free-air temperature range	ting free-air temperature range -55 to					
Storage temperature range	-65 to 150	-65 to 150	-65 to 150	°C		
Case temperature for 60 seconds: FK package					°C	
Lead temperature 1,6 mm (1/16 inch) from case for 60 seconds	JG, U, or W package	300			°C	
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds	D or P package	260	260	260	°C	

- NOTES: 1. All voltage values, unless otherwise noted, are with respect to the midpoint between V<sub>CC+</sub> and V<sub>CC-</sub>
  - 2. Differential voltages are at the noninverting input terminal with respect to the inverting input terminal.
  - 3. The magnitude of the input voltage must never exceed the magnitude of the supply voltage or 15 V, whichever is less.
  - 4. The output may be shorted to ground or either power supply. For the LM101A only, the unlimited duration of the short-circuit applies at (or above) 125°C case temperature or 75°C free-air temperature. For the LM201A only, the unlimited duration of the short-circuit applies at (or below) 85°C case temperature or 75°C free-air temperature.

### **DISSIPATION RATING TABLE**

PACKAGE	$T_{\mbox{\scriptsize A}} \leq 25^{\circ} \mbox{\scriptsize C}$	DERATING	DERATE	T <sub>A</sub> = 70°C	T <sub>A</sub> = 85°C	T <sub>A</sub> = 125°C
	POWER RATING	FACTOR	ABOVE TA	POWER RATING	POWER RATING	POWER RATING
D	500 mW	5.8 mW/°C	64°C	464 mW	377 mW	145 mW
FK	500 mW	11.0 mW/°C	105°C	500 mW	500 mW	275 mW
JG	500 mW	8.4 mW/°C	90°C	500 mW	500 mW	210 mW
Р	500 mW	8.0 mW/°C	87°C	500 mW	500 mW	200 mW
U	500 mW	5.4 mW/°C	57°C	432 mW	351 mW	135 mW
W	500 mW	8.0 mW/°C	87°C	500 mW	500 mW	200 mW

## recommended operating conditions

	MIN	MAX	UNIT
Supply voltage, V <sub>CC+</sub>	5	18	V
Supply voltage, V <sub>CC</sub> _	-5	-18	



## electrical characteristics at specified free-air temperature, C<sub>C</sub> = 30 pF (see Note 5)

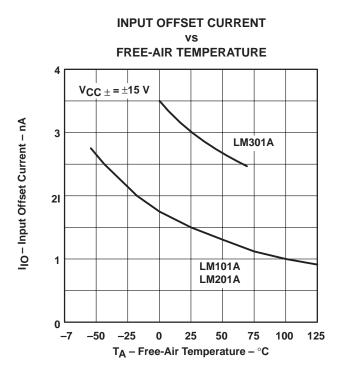
PARAMETER		TEST CONDITIONS†		LM101A, LM201A			LM301A			UNIT	
	PARAMETER	TEST CONDITIONS		MIN	TYP	MAX	MIN	TYP	MAX	UNIT	
VIO	Input offset voltage	V <sub>O</sub> = 0	25°C		0.6	2		2	7.5	mV	
۷IO	input onset voltage	VO = 0	Full range			3			10	IIIV	
ανιο	Average temperature coefficient of input offset voltage	V <sub>O</sub> = 0	Full range		3	15		6	30	μV/°C	
ادما	Input offset current		25°C		1.5	10		3	50		
ΙΟ	input onset current		Full range			20			70	nA	
		$T_A = -55^{\circ}C \text{ to } 25^{\circ}$	°C		0.02	0.2					
αΙΙΟ	Average temperature coefficient of	$T_A = 25^{\circ}C$ to MAX	(		0.01	0.1				- A /0C	
<sup>α</sup> IIO	input offset current	$T_A = 0$ °C to 25°C						0.02	0.6	nA/°C	
		T <sub>A</sub> = 25°C to 70°C						0.01	0.3		
lin	Input bias current		25°C		30	75		70	250	nA	
IIB			Full range			100			300		
VICR	Common-mode input voltage range	See Note 6	Full range	±15			±12			V	
	Maximum peak-to-peak output voltage swing	$V_{CC\pm} = \pm 15 \text{ V},$	25°C	24	28		24	28		V	
.,		$R_L = 10 \text{ k}\Omega$	Full range	24			24				
VOPP		$V_{CC\pm} = \pm 15 \text{ V},$	25°C	20	26		20	26			
		$R_L = 2 k\Omega$	Full range	20			20				
AVD	Large-signal differential voltage	$V_{CC\pm} = \pm 15 \text{ V},$ $V_{O} = \pm 10 \text{ V},$	25°C	50	200		25	200		V/mV	
/ ·VD	amplification	$R_L \ge 2 k\Omega$	Full range	25			15			V/111V	
rį	Input resistance		25°C	1.5	4		0.5	2		ΜΩ	
CMRR	Common-mode rejection ratio	V <sub>IC</sub> = V <sub>ICR</sub> min	25°C	80	98		70	90		dB	
			Full range	80			70				
le = 1 -=	Supply voltage rejection ratio		25°C	80	98		70	96		·ID	
ksvr	$(\Delta V_{CC}/\Delta V_{IO})$		Full range	80			70			dB	
ICC	Supply current	No load, $V_O = 0$ ,	25°C		1.8	3		1.8	3	^	
.00	Зирріу сипепі	See Note 6	MAX		1.2	2.5				mA	

<sup>†</sup> All characteristics are measured under open-loop conditions with zero common-mode input voltage unless otherwise specified. Full range for LM101A is -55°C to 125°C, for LM201A is -25°C to 85°C, and for LM301A is 0°C to 70°C.

NOTES: 5. Unless otherwise noted,  $V_{CC\pm}$  =  $\pm 5$  V to  $\pm 20$  V for LM101A and LM201A, and  $V_{CC\pm}$  =  $\pm 5$  V to  $\pm 15$  V for LM301A. All typical values are at  $V_{CC\pm}$  =  $\pm 15$  V.

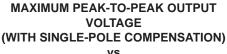
<sup>6.</sup> For LM101A and LM201A,  $V_{CC\pm} = \pm 20$  V. For LM301A,  $V_{CC\pm} = \pm 15$  V.

### TYPICAL CHARACTERISTICS



**INPUT BIAS CURRENT** FREE-AIR TEMPERATURE 100  $V_{CC\pm} = \pm 15 V$ 80 IB - Input Bias Current - nA LM301A 60 40 LM101A 20 LM201A 0 └ -75 -50 -25 0 25 100 125 50 75 T<sub>A</sub> - Free-Air Temperature - °C

Figure 1



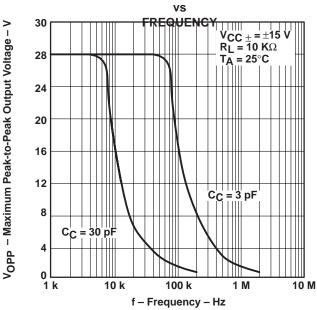


Figure 2

# OPEN-LOOP LARGE-SIGNAL DIFFERENTIAL VOLTAGE AMPLIFICATION

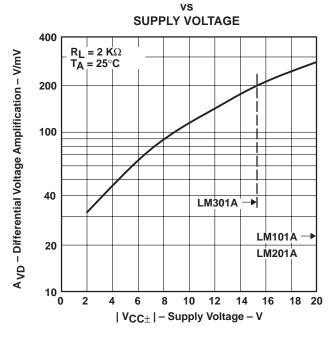
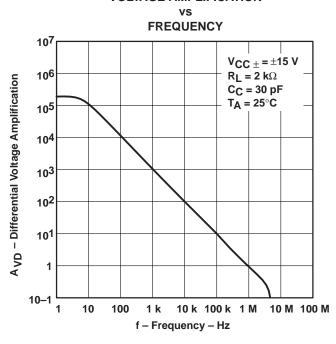


Figure 3 Figure 4

### TYPICAL CHARACTERISTICS

## OPEN-LOOP LARGE-SIGNAL DIFFERENTIAL VOLTAGE AMPLIFICATION



VOLTAGE-FOLLOWER LARGE-SIGNAL PULSE RESPONSE

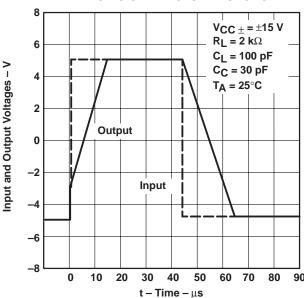
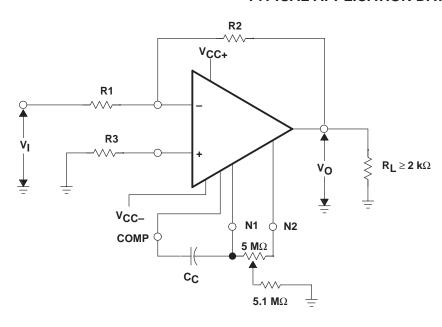


Figure 5

Figure 6

## TYPICAL APPLICATION DATA



$$\frac{V_O}{V_I} = - \frac{R2}{R1}$$

$$C_{C} \geq \frac{R1 \cdot 30 \text{ pF}}{R1 + R2}$$

$$R3 = \frac{R1 \cdot R2}{R1 + R2}$$

Figure 7. Inverting Circuit with Adjustable Gain, Single-Pole Compensation, and Offset Adjustment



#### **IMPORTANT NOTICE**

Texas Instruments and its subsidiaries (TI) reserve the right to make changes to their products or to discontinue any product or service without notice, and advise customers to obtain the latest version of relevant information to verify, before placing orders, that information being relied on is current and complete. All products are sold subject to the terms and conditions of sale supplied at the time of order acknowledgement, including those pertaining to warranty, patent infringement, and limitation of liability.

TI warrants performance of its semiconductor products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are utilized to the extent TI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.

CERTAIN APPLICATIONS USING SEMICONDUCTOR PRODUCTS MAY INVOLVE POTENTIAL RISKS OF DEATH, PERSONAL INJURY, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE ("CRITICAL APPLICATIONS"). TI SEMICONDUCTOR PRODUCTS ARE NOT DESIGNED, AUTHORIZED, OR WARRANTED TO BE SUITABLE FOR USE IN LIFE-SUPPORT DEVICES OR SYSTEMS OR OTHER CRITICAL APPLICATIONS. INCLUSION OF TI PRODUCTS IN SUCH APPLICATIONS IS UNDERSTOOD TO BE FULLY AT THE CUSTOMER'S RISK.

In order to minimize risks associated with the customer's applications, adequate design and operating safeguards must be provided by the customer to minimize inherent or procedural hazards.

TI assumes no liability for applications assistance or customer product design. TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of TI covering or relating to any combination, machine, or process in which such semiconductor products or services might be or are used. TI's publication of information regarding any third party's products or services does not constitute TI's approval, warranty or endorsement thereof.

Copyright © 1998, Texas Instruments Incorporated