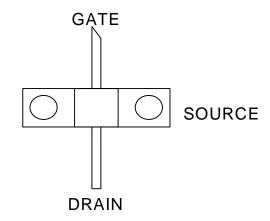
# Solid State

# **PACKAGED 1W POWER PHEMT**

#### **FEATURES**

- +31 dBm Typical Power at 15 GHz
- 12.5 dB Typical Power Gain at 15 GHz
- Low Intermodulation Distortion
- 50% Power-Added-Efficiency
- Color-coded by I<sub>DSS</sub> range



#### **DESCRIPTION AND APPLICATIONS**

The LP1500P100 is a packaged Aluminum Gallium Arsenide / Indium Gallium Arsenide (AlGaAs/InGaAs) Pseudomorphic High Electron Mobility Transistor (PHEMT), utilizing an Electron-Beam direct-write 0.25  $\mu$ m by 1500  $\mu$ m Schottky barrier gate. The recessed "mushroom" gate structure minimizes parasitic gate-source and gate resistances. The epitaxial structure and processing have been optimized for reliable high-power applications. The LP1500 also features Si<sub>3</sub>N<sub>4</sub> passivation and is available in die form or in a low cost plastic surface-mount packages, SOT-223 or SOT-89. Packages are color-coded by I<sub>DSS</sub> range.

Typical applications include commercial and military high-performance power amplifiers, including SATCOM uplink transmitters, and medium-haul digital radio transmitters.

The LP 1500P100 may be procured in a variety of grades, depending upon specific user requirements. Standard lot screening is patterned after MIL-STD-19500, JANC grade. Space level screening to LSSD JANS grade is also available.

## PERFORMANCE SPECIFICATIONS ( $T_A = 25^{\circ}C$ )

SYMBOLS	PARAMETERS		MIN	TYP	MAX	UNITS
I <sub>DSS</sub>	Saturated Drain-Source Current	LP1500-P100-1 BLUE	375	410	450	mΑ
	$V_{DS} = 2V  V_{GS} = 0V$	LP1500-P100-2 GREEN	451	490	526	
	0 / 1 / 0 / 1 / 1 / 0 / 0	LP1500-P100-3 RED	527	565	600	
P <sub>1dB</sub>	Output Power at 1dB Gain Compression					
	$V_{DS} = 8.0V, I_{DS} = 50\% I_{DSS}$	f = 15 GHz	29.5	31.0		dBm
G <sub>1dB</sub>	Power Gain at 1dB Gain Compress					
	$V_{DS} = 8.0 \text{V}, I_{DS} = 50\% I_{DSS}$	f = 15 GHz	8.0	9.0		dB
$\eta_{ADD}$	Power-Added Efficiency			50		%
I <sub>MAX</sub>	Maximum Drain-Source Current	$V_{DS} = 2V V_{GS} = +1V$		925		mΑ
G <sub>M</sub>	Transconductance	$V_{DS} = 2V V_{GS} = 0V$	300	400		mS
$V_{P}$	Pinch-Off Voltage	$V_{DS} = 2V I_{DS} = 5mA$	-0.25	-1.2	-2.0	V
I <sub>GSO</sub>	Gate-Source Leakage Current	$V_{GS} = -5V$		10	100	μΑ
BV <sub>GS</sub>	Gate-Source Breakdown Voltage	$I_{GS} = 8mA$	-12	-15		V
$BV_GD$	Gate-Drain Breakdown Voltage	$I_{GD} = 8mA$	-12	-16		V

**DSS-025 WD** 

# Solid State

### PACKAGED 1W POWER PHEMT

ABSOLUTE MAXIMUM RATINGS (25°C)						
SYMBOL	PARAMETER	RATING <sup>1</sup>				
V <sub>DS</sub>	Drain-Source Voltage	+12V				
V <sub>GS</sub>	Gate-Source Voltage	-4V				
I <sub>DS</sub>	Drain-Source Current	2 x I <sub>DSS</sub>				
I <sub>G</sub>	Gate Current	70 mA				
P <sub>IN</sub>	RF Input Power	700 mW				
Тсн	Channel Temperature	+175°C				
T <sub>STG</sub>	Storage Temperature	-65/175°C				
P <sub>T</sub>	Power Dissipation	3.0W <sup>3,4</sup>				

RECOMMENDED CONTINUOUS OPERATING LIMITS					
SYMBOL	PARAMETER	RATING <sup>2</sup>			
V <sub>DS</sub>	Drain-Source Voltage	+8V			
V <sub>GS</sub>	Gate-Source Voltage	-1V			
I <sub>DS</sub>	Drain-Source Current	0.8 x I <sub>DSS</sub>			
I <sub>G</sub>	Gate Current	15 mA			
P <sub>IN</sub>	RF Input Power	300 mW			
Тсн	Channel Temperature	+150°C			
T <sub>STG</sub>	Storage Temperature	-20/50°C			
P <sub>T</sub>	Power Dissipation	2.5 W <sup>3,4</sup>			
G <sub>XdB</sub>	Gain Compression	8 dB			

#### NOTES:

- 1. Operating conditions that exceed the Absolute Maximum Ratings could result in permanent damage to the device.
- 2. Recommended Continuous Operating Limits should be observed for reliable device operation.
- 3. Power Dissipation defined as:  $P_T \equiv (P_{DC} + P_{IN}) P_{OUT}$ , where:  $P_{DC} = DC$  bias power,  $P_{OUT} = RF$  output power, and  $P_{IN} = RF$  input power.
- 4. Power Dissipation to be de-rated as follows above 25°C:

Absolute Maximum:

 $P_T = 3.0W - (20mW/^{\circ}C) \times T_{HS}$ 

Recommended Continuous Operating: P<sub>T</sub> = 2.5W - (20mW/°C) x T<sub>HS</sub>

where  $T_{HS}$  = heatsink or ambient temperature.

5. Specifications subject to change without notice.

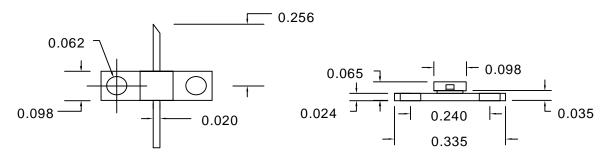
### **HANDLING PRECAUTIONS:**

Care should be exercised during handling to avoid damage to the devices. Proper Electrostatic Discharge (ESD) precautions should be observed at all stages of storage, handling, assembly, and testing. These devices should be treated as Class 1A (0-500V), and further information on ESD control measures can be found in MIL-STD-1686 and MIL-HDBK-263.

#### **APPLICATIONS NOTES AND DESIGN DATA:**

Applications Notes are available from your local LSSD Sales Representative, or directly from the factory. Complete design data, including S-parameters, Noise data, and Large-Signal models, is available on 3.5" diskette, or may be downloaded from our Web Page.

#### PACKAGE OUTLINE: (DIMENSIONS IN INCHES)



## **DSS-025 WD**

**Phone:** (408) 988-1845 **Internet:** http://www.Filtronicsolidstate.com **FAX:** (408) 970-9950