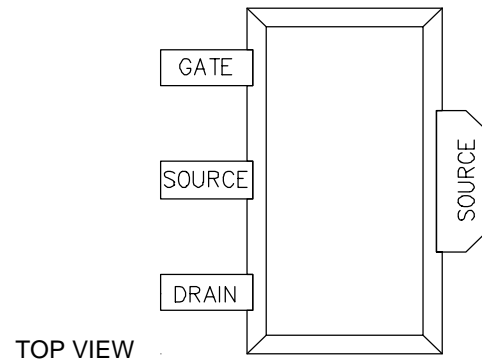


FEATURES

- **+28 dBm Typical Power at 1800 MHz**
- **16 dB Typical Power Gain at 1800 MHz**
- **1.2 dB Typical Noise Figure**
- **+44 dBm Typical Intercept Point**
- **50% Power-Added-Efficiency**
- **Color-coded by I_{DSS} range**



DESCRIPTION AND APPLICATIONS

The LP1500-SOT89 is a packaged Aluminum Gallium Arsenide / Indium Gallium Arsenide (AlGaAs/InGaAs) Pseudomorphic High Electron Mobility Transistor (PHEMT), utilizing an Electron-Beam direct-write 0.25 μm by 1500 μm Schottky barrier gate. The recessed “mushroom” gate structure minimizes parasitic gate-source and gate resistances. The epitaxial structure and processing have been optimized for reliable high-power applications. The LP1500 also features Si_3N_4 passivation and is available in die form or various other packages. Packages are color-coded by the I_{DSS} range (see below).

Typical applications include PCS/Cellular low-voltage high-efficiency output amplifiers, and general purpose power amplifiers. Standard LP1500 lot screening is patterned after MIL-STD-19500, JANC grade.

PERFORMANCE SPECIFICATIONS ($T_A = 25^\circ\text{C}$)

SYMBOLS	PARAMETERS		MIN	TYP	MAX	UNITS
I _{DSS}	Saturated Drain-Source Current V _{DS} = 2V V _{GS} = 0V	LP1500-SOT89-1 BLUE LP1500-SOT89-2 GREEN LP1500-SOT89-3 RED	375 451 527	420 490 560	450 526 600	mA
P _{1dB}	Output Power at 1dB Gain Compression V _{DS} = 5.0V, I _{DS} = 50% I _{DSS} f = 1800 Mhz		26.0	27.5		dBm
G _{1dB}	Power Gain at 1dB Gain Compression V _{DS} = 5.0V, I _{DS} = 50% I _{DSS} f = 1800 MHz		13.5	16.0		dB
η _{ADD}	Power-Added Efficiency (5V/50%)			40		%
NF _{MIN}	Min. Noise Figure V _{DS} = 5V, I _{DS} = 50% I _{DSS} , f = 1800 MHz			1.2		dB
IP3	Output Intercept Point V _{DS} = 5V, I _{DS} = 50% I _{DSS} , f = 1800 MHz			44		dBm
I _{MAX}	Maximum Drain-Source Current	V _{DS} = 2V V _{GS} = +1V		925		mA
G _M	Transconductance	V _{DS} = 2V V _{GS} = 0V	300	400		mS
V _P	Pinch-Off Voltage	V _{DS} = 2V I _{DS} = 5mA	-0.25	-1.2	-2.0	V
I _{GSO}	Gate-Source Leakage Current	V _{GS} = -5V		10	100	μA
BV _{GS}	Gate-Source Breakdown Voltage	I _{GS} = 8mA	-10	-12		V
BV _{GD}	Gate-Drain Breakdown Voltage	I _{GD} = 8mA	-10	-13		V

ABSOLUTE MAXIMUM RATINGS (25°C)		
SYMBOL	PARAMETER	RATING ¹
V _{DS}	Drain-Source Voltage	7V
V _{GS}	Gate-Source Voltage	-3V
I _{DS}	Drain-Source Current	I _{DSS}
I _G	Gate Current	50 mA
P _{IN}	RF Input Power	350 mW
T _{CH}	Channel Temperature	175°C
T _{STG}	Storage Temperature	-65/175°C
P _T	Power Dissipation	1.7W ^{3,4}

RECOMMENDED CONTINUOUS OPERATING LIMITS		
SYMBOL	PARAMETER	RATING ²
V _{DS}	Drain-Source Voltage	6V
V _{GS}	Gate-Source Voltage	-1V
I _{DS}	Drain-Source Current	0.65 x I _{DSS}
I _G	Gate Current	15 mA
P _{IN}	RF Input Power	250 mW
T _{CH}	Channel Temperature	150°C
T _{STG}	Storage Temperature	-20/50°C
P _T	Power Dissipation	1.4 W ^{3,4}
G _{XdB}	Gain Compression	8 dB

NOTES:

- Operating conditions that exceed the Absolute Maximum Ratings could result in permanent damage to the device.
- Recommended Continuous Operating Limits should be observed for reliable device operation.
- Power Dissipation defined as: $P_T \equiv (P_{DC} + P_{IN}) - P_{OUT}$, where: P_{DC} = DC bias power, P_{OUT} = RF output power, and P_{IN} = RF input power. **Provide for adequate thermal heatsinking at large source lead.**
- Power Dissipation to be de-rated as follows above 25°C:
 Absolute Maximum: $P_T = 1.7W - (11mW/°C) \times T_{HS}$
 Recommended Continuous Operating: $P_T = 1.4W - (11mW/°C) \times T_{HS}$
 where T_{HS} = heatsink or ambient temperature.
- Specifications subject to change without notice.

HANDLING PRECAUTIONS:

Care should be exercised during handling to avoid damage to the devices. Proper Electrostatic Discharge (ESD) precautions should be observed at all stages of storage, handling, assembly, and testing. These devices should be treated as Class 1A (0-500V), and further information on ESD control measures can be found in MIL-STD-1686 and MIL-HDBK-263.

APPLICATIONS NOTES AND DESIGN DATA:

Applications Notes are available from your local FSS Sales Representative, or directly from the factory. Complete design data, including S-parameters, Noise data, and Large-Signal models, is available on 3.5" diskette, or may be down-loaded from our Web Page.

PACKAGE OUTLINE: (dim. in mils; 1 mil = 0.001 in.)

