

LXM1623-12-4x

12V Dual 4W CCFL Programmable Inverter Module

PRELIMINARY DATASHEET

DESCRIPTION

The LXM1623-12-4x is a Dual 4W Output Direct DriveTM CCFL (Cold energizes the lamp was designed Cathode Fluorescent Lamp) Inverter specifically to ensure that no premature Module specifically designed for driving lamp degradation occurs, while allowing LCD backlight lamps. It is ideal for significant power savings at lower dim driving typical 6.4" to 10.4" TFT panels.

LXM1623 modules provide designer with a vastly superior display the system battery or AC adapter directly brightness range. This brightness range is to high frequency, high-voltage waves achievable with virtually any LCD display. required to ignite and operate CCFL

dimming input that permits brightness available (LXM1623-05-4x), as well as control from either a DC voltage source or 6W versions (LXM1623-xx-6x) for a PWM signal or external Potentiometer. driving larger higher voltage panels. The maximum output current is externally programmable over a range of 5 to 6.5mA Microsemi's new LX1689 backlight in 0.5mA steps to allow the inverter to controller, which provides a number of properly match to a wide array of LCD cost and performance advantages due to panel lamp current specifications.

Digital RangeMAX Dimming Technique provides flicker-free brightness are stable fixed-frequency operation. control in any wide range typically (50:1+) dimming application.

The resultant "burst drive" levels.

The modules convert DC voltage from The modules are available with a lamps. A 5V input inverter also is

> The modules design is based on the controller's high level of integration.

> Other benefits of this new topology secondary-side strike-voltage regulation and both open/shorted lamp protection with fault timeout.

KEY FEATURES

- Externally Programmable Maximum Output Current
- Easy to Use Brightness Control
- RangeMAX Wide Range Dimming
- Output Open & Short-Circuit Protection and Automatic Strike-Voltage Regulation and Timeout
- Fixed Frequency Operation
- Rated From -20 to 70°C
- UL60950 E175910

APPLICATIONS

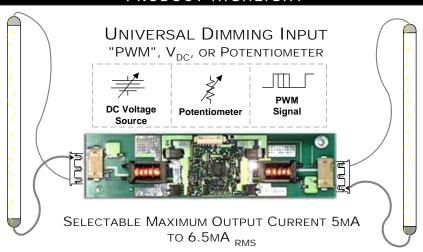
- High Brightness Displays
- Portable Instrumentation
- **Desktop Displays**
- Industrial Display Controls

BENEFITS

- Smooth, Flicker Free 2%-100% Full-Range Brightness Control
- Programmable output current allows inverter to mate with a wide variety of LCD panel's specifications
- Output Open Circuit Voltage Regulation Minimizes Corona Discharge For High Reliability

IMPORTANT: For the most current data, consult MICROSEMI's website: http://www.microsemi.com

PRODUCT HIGHLIGHT



| PACKAGE ORDER INFO | | | | | |
|--------------------|-------------------------|---|--|--|--|
| PART NUMBER | OUTPUT CONNECTOR | INVERTER MATES DIRECTLY TO PANEL CONNECTORS | | | |
| LXM1623-12-41 | JST SM02(8.0)B-BHS-1-TB | JST BHR-03VS-1 | | | |
| LXM1623-12-42 | JST SM02B-BHSS-1-TB | JST BHSR-02VS-1 | | | |
| LXM1623-12-43 | Honda QZ-19-A3MYL #02 | HONDA QZ-19-3F01 | | | |



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Note 1: Exceeding these ratings could cause damage to the device. All voltages are with respect to Ground. Currents are positive into, negative out of specified terminal

RECOMMENDED OPERATING CONDITIONS (R.C.)

This module has been designed to operate over a wide range of input and output conditions. However, best efficiency and performance will be obtained if the module is operated under the condition listed in the 'R.C.' column. Min. and Max. columns indicate values beyond which the inverter, although operational, will not function optimally.

| Parameter | Symbol | Recommended Operating Conditions | | | Units |
|---|--------------------|----------------------------------|------|------|-------------------|
| i diametei | Gyllibol | Min | R.C. | Max | Offics |
| Input Supply Voltage Range (Fully Regulated Lamp Current) | V _{IN1} | 10.8 | 12 | 13.2 | V |
| Input Supply Voltage Range (Functional) | | 10.2 | 12 | 13.8 | |
| Output Power (each output) | Po | | 3.5 | 4.0 | W |
| Linear BRITE Control Input Voltage Range | $V_{BRT\ ADJ}$ | 0.5 | | 2.0 | V |
| Lamp Operating Voltage | V_{LAMP} | 350 | 440 | 530 | V_{RMS} |
| Lamp Current (Full Brightness) | I _{OLAMP} | 5 | | 6.5 | mA _{RMS} |
| Operating Ambient Temperature Range | T _A | -20 | | 70 | °C |

ELECTRICAL CHARACTERISTICS

Unless otherwise specified, the following specifications apply over the recommended operating condition and ambient temperature of 25°C except where otherwise noted.

| Parameter | Symbol Test Conditions | | LXM1623-12-4x | | | Units |
|---|------------------------|--|---------------|------|-----|-------------------|
| Faranietei | Syllibol | rest Conditions | Min | Тур | Max | Ullits |
| OUTPUT PIN CHARACTERISTICS | | | | | | |
| Full Bright Lamp Current (each output) | I _{L(MAX)} | $V_{BRT_ADJ} \ge 2.0V_{DC}$, $\overline{SLEEP} \ge 2.0V$, $V_{IN1} = 12V_{DC}$ $I_{SET1} = Ground$, $I_{SET2} = Ground$ | 4.5 | 5 | 5.5 | mA _{RMS} |
| Full Bright Lamp Current (each output) | $I_{L(MAX)}$ | $V_{BRT_ADJ} \ge 2.0V_{DC}$, $\overline{SLEEP} \ge 2.0V$, $V_{IN1} = 12V_{DC}$ $I_{SET1} = Ground$, $I_{SET2} = Open$ | 5.0 | 5.5 | 6.0 | mA _{RMS} |
| Full Bright Lamp Current (each output) | $I_{L(MAX)}$ | $V_{BRT_ADJ} \ge 2.0V_{DC}$, $\overline{SLEEP} \ge 2.0V$, $V_{IN1} = 12V_{DC}$ $I_{SET1} = Open$, $I_{SET2} = Ground$ | 5.5 | 6 | 6.5 | mA _{RMS} |
| Full Bright Lamp Current (each output) | $I_{L(MAX)}$ | $V_{BRT_ADJ} \ge 2.0V_{DC}$, $\overline{SLEEP} \ge 2.0V$, $V_{IN1} = 12V_{DC}$ $I_{SET1} = Open$, $I_{SET2} = Open$ | 6.0 | 6.5 | 7.0 | mA _{RMS} |
| Output Current Lamp to Lamp Deviation | I _{LL%DEV} | $V_{BRT_ADJ} \ge 2.0V_{DC}$, $\overline{SLEEP} \ge 2.0V$, $V_{IN1} = 12V_{DC}$ $I_{SET1} = Open$, $I_{SET2} = Open$ | | 3 | 10 | % |
| Min. Average Lamp Current (each output) | I _{L(MIN)} | $V_{BRT_ADJ} \le 0.5 V_{DC}$, $\overline{SLEEP} \ge 2.0 V$, $V_{IN1} = 12 V_{DC}$ $I_{SET1} = I_{SET2} = Ground$ | | 0.30 | | mA _{RMS} |
| Lamp Start Voltage | V_{LS} | -20°C < T _A < 70°C, V _{IN1} > 10.8V _{DC} | 1250 | 1400 | | V_{RMS} |
| Operating Frequency | f _O | $V_{BRT_ADJ} = 2.5V_{DC}, \overline{SLEEP} \ge 2.0V, V_{IN1} = 12V$ | 76 | 80 | 83 | kHz |
| Burst Frequency | f _{BURST} | Output Burst Frequency | 148 | 156 | 163 | Hz |



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ELECTRICAL CHARACTERISTICS (CONTINUED)

Unless otherwise specified, the following specifications apply over the recommended operating condition and ambient temperature of 25° C except where otherwise noted.

| | Parameter | Symbol | vmbol Test Conditions | | LXM1623-12-4x | | | | |
|---|-------------------------------------|----------------------|---|------|---------------|------------------|------------------|--|--|
| | Farameter | Syllibol | rest Conditions | Min | Тур | Max | Units | | |
| • | BRITE INPUT | | | | | | | | |
| | Input Current | I _{BRT} | $V_{BRT_ADJ} = 0V_{DC}$ | | -300 | | μA _{DC} | | |
| | | IDICI | $V_{BRT_ADJ} = 3V_{DC}$ | | 50 | | μA _{DC} | | |
| | Minimum Input for Max. Lamp Current | V_{BRT_ADJ} | I _{O(LAMP)} = Maximum Lamp Current | | 2.0 | 2.05 | V_{DC} | | |
| | Maximum Input for Min. Lamp Current | V_{BRT_ADJ} | I _{O(LAMP)} = Minimum Lamp Current | 0.4 | 0.5 | | V_{DC} | | |
| • | SLEEP INPUT | | | | | | | | |
| | RUN Mode | V _{SLEEP} | | 2.0 | | V _{IN1} | V_{DC} | | |
| | SLEEP Mode | V _{SLEEP} | | -0.3 | | 0.8 | V _{DC} | | |
| • | SET _{1,2} INPUT | | | | | | | | |
| | SET _{1,2} Low Threshold | V_L | | | | 0.4 | V | | |
| | Input Current | I _{SET} | V _{SET} ≤ 0.4V | | -300 | | μA | | |
| • | POWER CHARACTERISTICS | | | | | | | | |
| | Sleep Current | I _{IN(MIN)} | $V_{IN1} = 12V_{DC}, \overline{SLEEP} \le 0.8V$ | 0.0 | 10 | 50 | μA _{DC} | | |
| | Run Current | I _{IN(RUN)} | V_{IN1} = 12 V_{DC} , $\overline{SLEEP} \ge 2.0V$, I_{SET1} = Open I_{SET2} = Ground, V_{LAMP} = 440 V_{RMS} | | 530 | | mA _{DC} | | |
| | Efficiency | η | V_{IN1} = 12 V_{DC} , $\overline{SLEEP} \ge 2.0V$, I_{SET1} = Open I_{SET2} = Ground, V_{LAMP} = 440 V_{RMS} | | 85 | | % | | |

| | | FUNCTIONAL PIN DESCRIPTION | | | |
|------------------------|---|--|--|--|--|
| Conn | PIN | DESCRIPTION | | | |
| CN1 (Molex | 53261-0890) | Mates with 51021-0800 housing, 50079-8100 pins. Mates with LX9501 input cable assembly | | | |
| CN1-1 | V _{IN1} | Main Input Power Supply (10.8V ≤ V _{IN1} ≤ 13.2V) | | | |
| CN1-2 | V IN1 | Main input I ower Supply (10.07 \leq 7 N1 \leq 13.27) | | | |
| CN1-3 | GND | Power Supply Return | | | |
| CN1-4 | OND | rowei Suppiy Netuiti | | | |
| CN1-5 | SLEEP | ON/OFF Control. (0V < $\overline{\text{SLEEP}}$ < 0.8 = OFF, $\overline{\text{SLEEP}}$ >= 2.0V = ON | | | |
| CN1-6 | BRITE | Brightness Control (0.5V to 2.0V _{DC}). 2.0V _{DC} gives maximum lamp current. | | | |
| CN1-7 SET ₁ | | SET ₁ MSB Connecting this pin to ground decreases the output current (see Table 1) | | | |
| CN1-8 | SET ₂ LSB Connecting this pin to ground decreases the output current (see Table 1) | | | | |
| CN2, CN3 f | or LXM1623-1 | 2-41 and -42 (JST SM02(8.0)B-BHS-1-TB or SM02B-BHSS-1-TB) | | | |
| CN2-1 CN3-1 | | High voltage connection to high Side of lamp. Connect to lamp terminal with shortest lead length. DO NOT connect to Ground. | | | |
| CN2-2 CN3-2 | V_{LO} | Connection to low side of lamp. Connect to lamp terminal with longer lead length. DO NOT connect to Ground | | | |
| CN2, CN3 f | or LXM1623-1 | 2-43 (Honda QZ-19-A3MYL #02) | | | |
| CN2-3 CN3-3 | V _{HI} | High voltage connection to high side of lamp. Connect to lamp terminal with shortest lead length. DO NOT connect to Ground. | | | |
| CN2-1 CN3-1 | V_{LO} | Connection to low side of lamp. Connect to lamp terminal with longer lead length. DO NOT connect to Ground | | | |



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PRELIMINARY DATASHEET

TABLE 1

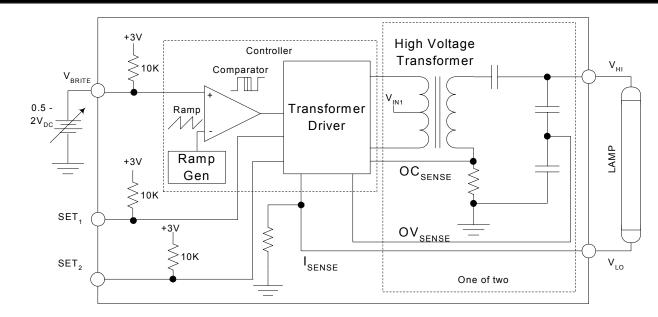
OUTPUT CURRENT SETTINGS

| SET₁ (Pin 7) | SET ₂ (Pin 8) | Nominal Output Current | | |
|-----------------|-----------------------------|------------------------|--|--|
| Open* | Open* | 6.5mA | | |
| Open* | Ground | 6.0mA | | |
| Ground | Open* | 5.5mA | | |
| Ground | Ground | 5.0mA | | |

^{*} If driven by a logic signal it should be open collector or open drain only, not a voltage source.

PHYSICAL DIMENSIONS LXM1623-12-4X 4.52in 115mm 4.29in GROUNDED MOUNTING HOLE 3MM X 2 DIA. ±0.08 6MM SCREW HEAD CLEARANCE -01 -02 CN-2 1.18in. 1.02in. 30mm 26mm 0.15in. \bigcirc Warning 0.23in **High Voltage** 0.0392in. Present at high 0.256in. 6.5mm Max side of transformer Weight: (15g) typ and Output PCB tolerances ± 0.5mm Connector

SIMPLIFIED BLOCK DIAGRAM





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TYPICAL APPLICATION

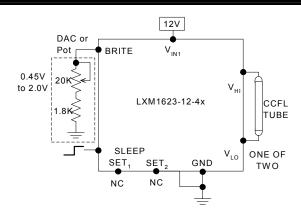


Figure 1 – Brightness Control (Output current set to maximum)

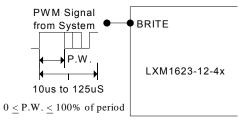


Figure 1A - PWM Brightness Control

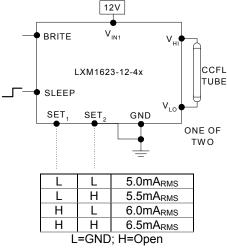


Figure 2 – Max Output Current (SET₁ and SET₂ Inputs)

- The brightness control may be a voltage output DAC or other voltage source, a digital pot or 20K manual pot. The inverter contains an internal 10K pull-up to 3V to bias the pot, add a 1.8K resistor to set the lower threshold voltage. A 3.3V Logic Level PWM signal from a micro-controller may also be used as shown in Figure 1A.
- If you need to turn the inverter ON/OFF remotely, connect to TTL logic signal to the SLEEP input.
- Connect V_{HI} to high voltage wire from the lamp. Connect V_{LO} to the low voltage wire (wire with thinner insulation). Never connect V_{LO} to circuit ground as this will defeat lamp current regulation. If both lamp wires have heavy high voltage insulation, connect the longest wire to V_{LO}. This wire is typically white.
- Use the SET₁ and SET₂ (see Figure 2) inputs to select the desired maximum output current. Using these two pins in combination allows the inverter to match a wide variety of panels from different manufactures. Generally the best lamp lifetime correlates with driving the CCFL at the manufactures nominal current setting. However the SET₁ and SET₂ inputs allow the user the flexibility to adjust the current to the maximum allowable output current to increase panel brightness at the expense of some reduced lamp life.
- Although the SET pins are designed such that just leaving them open or grounding them is all that is needed to set the output current, they can also be actively set. Using a open collector or open drain logic signal will allow you to reduce the lamp current for situations where greater dim range is required, as an example in nighttime situations. In conjunction with a light sensor or other timer the panel could be set to higher brightness (maximum output current) for daytime illumination and lower brightness (minimum or typical output current) at nighttime. Since the dim ratio is a factor of both the burst duty cycle and the peak output current, using this technique the effective dim ratio can be increased greater than the burst duty cycle alone. Conversely the SET inputs could be used to overdrive the lamp temporarily to facilitate faster lamp warm up at initial lamp turn on. Of course any possible degradation on lamp life from such practices is the users responsibility since not all lamps are designed to be overdriven.
- The inverter has a built in fault timeout function. If the output is open (lamp disconnected or broken) or shorted the inverter will attempt to strike the lamp for several seconds. After about 2 seconds without success the inverter will shutdown. In order to restart the inverter it is necessary to toggle the sleep input or cycle the V_{IN1} input supply



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|---|---|---|---|---|

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