



## **L8575 Dual-Resistive, Low-Cost Subscriber Line Interface Circuit (SLIC)**

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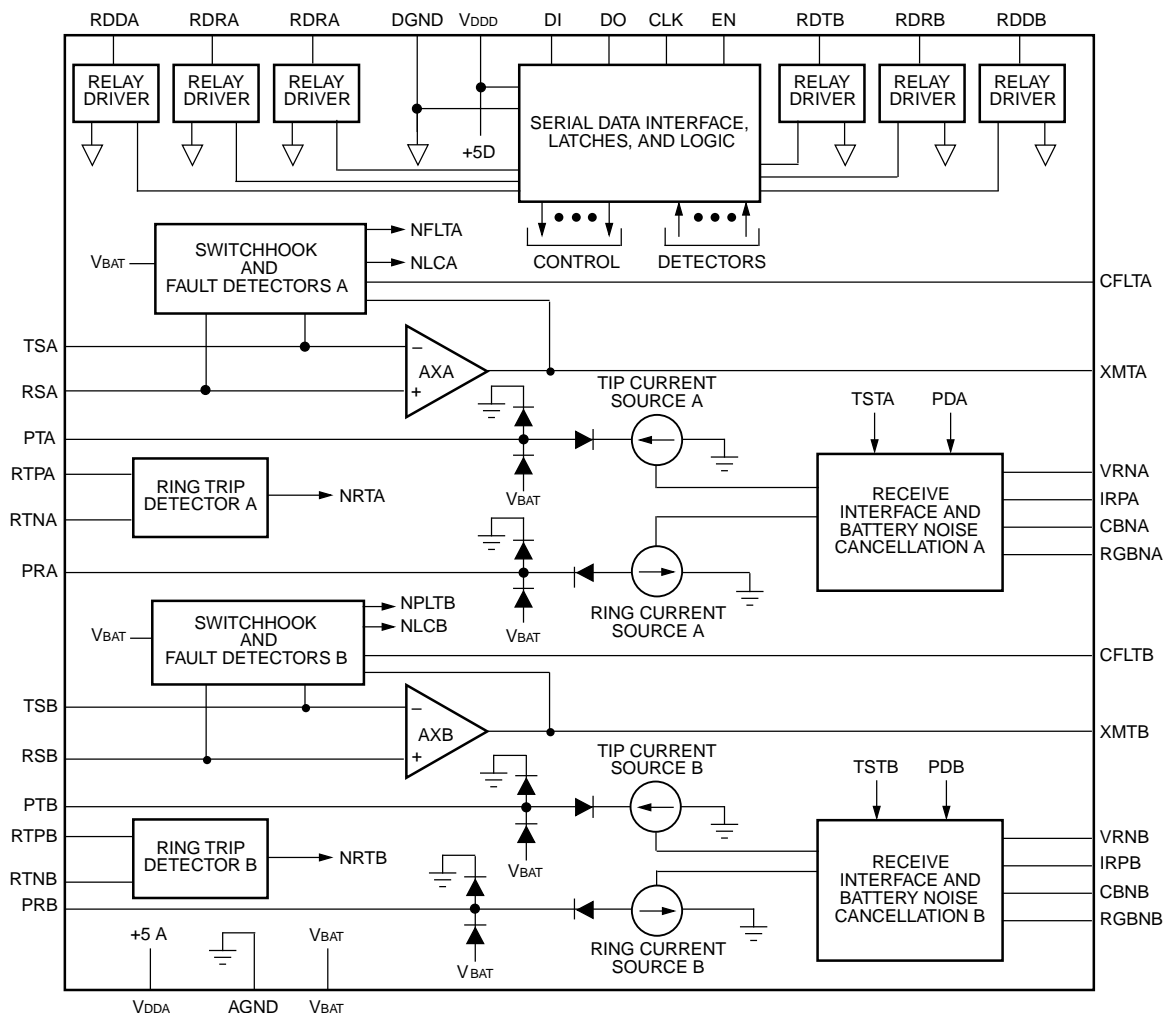
### **Features**

- Two channels in a single package
- Serial data interface
- Per-channel powerdown
- Low standby power ( $\leq 65$  mW per channel)
- Integrated protection
- No external protection device required
- Battery noise cancellation
- Switchhook detector
- Ring trip detector
- Switchhook and ring trip detector self-test
- Fault detector
- Zero ring voltage cross detection
- Three relay drivers per channel
- 44-pin, surface-mount, plastic package (PLCC)

### **Description**

The L8575 is a dual-resistive, low-cost subscriber line interface circuit (SLIC) that is optimized to meet both ITU-T recommendations and LSSGR requirements for 600  $\Omega$ /900  $\Omega$  resistive and complex impedance termination applications. It interfaces the low-voltage circuits on an analog line card to the Tip and Ring of two subscriber loops. The L8575 does not supply dc current to the subscriber loops—external resistors are used for this purpose. The device is built using a 90 V complementary bipolar (CBIC) process and is available in a 44-pin PLCC package.

## Functional Diagram



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