
Olive

LES105 Five-port Switch Application Note

1. The note of SGAM configuration
8M (256K x 32) & 16M (512K x 32) chip can be layout on the same location just by grounding the pin30, but only 8M memory is used.
2. The PHY' s ID address is suggested starting from 04h~0Bh (8 ports). And the default starting value is 04h.
3. If fiber media is used. The chosen port' s auto-negotiation function should be disabled and fix the wanted speed & duplex mode. All these settings are required to change by EEPROM.
4. Pin150 (EEDO/NA16#) should be pulled down
if don' t want to drop the packet for 16 consecutive collisions.
5. BP[1:0] (Pin 148, Pin149) = "10" are suggested.
6. LES105 support combination as following:
 - (1) Pin [11:9] = 000, & RMII# = Low, then support all 5 ports RMII .
 - (2) Pin [11:9] = 000, & RMII# = Hi, then support all 5 ports MII .
 - (2) Pin [11:9] = 100, & RMII# = Low, then support
Port0 = MII, the default address of PHY is 07h
Port1~ 4 = RMII, the default starting address of PHY is 08h
7. Provide carrier mode of back pressure
if bit[11:8] of 02h in EEPROM be set to "0101".
8. Pin146 (QFLED#) should be pulled high.