



THE INFINITE POWER OF INNOVATION

9-Line Low Capacitance, µPower SCSI Terminator

PRODUCTION DATA SHEET

DESCRIPTION

The LX5211 is a nine-line active terminator for the SCSI parallel bus. The SCSI standard recommends active termination at both ends of the SCSI bus.

During disconnect mode, the LX5211 requires a meager 500nA of supply current while offering only 2.5pF of output capacitance. To enter this low-power mode, the disconnect pin can be left open (floating) or driven low, thereby disconnecting the terminating resistors and placing the internal low dropout regulator into low-power mode. In disconnect mode, each termination line presents a high impedance to the SCSI bus with the overall effect being to preserve high signal integrity and yield subsequent reliable, error-free communications.

During normal operation, the LX5211

consumes only 600µA of current, which is the lowest enabled supply current of any terminator available on the market today. Linfinity's proprietary BiCMOS low dropout regulator architecture enables this unique and very efficient operating characteristic.

The LX5211 also offers a precisely trimmed channel output current specified to a 5% tolerance. The maximum value of the output current is trimmed as closely as possible to the SCSI standard maximum specification to give the highest possible noise margin for fast SCSI operation. And the LX5211 sinks up to 30mA of current, making it compatible with today's fast active negation drivers.

The LX5211 is a stable, pin-for-pin replacement for a variety of industry products, such as the UCC5606.

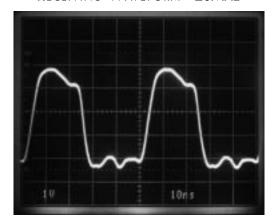
KEY FEATURES

- 2.5pf OUTPUT CAPACITANCE DURING DISCONNECT
- 500nA SUPPLY CURRENT IN DISCONNECT MODE
- 600µA SUPPLY CURRENT DURING NORMAL OPERATION
- 30mA SINK CURRENT FOR ACTIVE NEGATION
- LOGIC COMMAND DISCONNECTS ALL TERMINATION LINES
- CURRENT LIMIT AND THERMAL PROTECTION
- COMPATIBLE WITH SCSI 1, 2, 3, AND ULTRA SCSI STANDARDS
- HOT SWAP COMPATIBLE
- CONSULT FACTORY FOR APPLICATION TEST REPORT: 5211TR
- EVALUATION BOARD AVAILABLE:
 REQUEST LXE9005 EVALUATION KIT

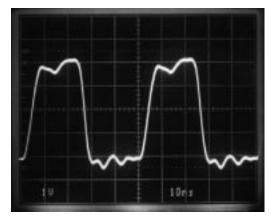
IMPORTANT: For the most current data, consult LinFinity's web site: http://www.linfinity.com.

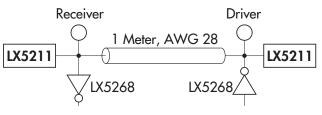
PRODUCT HIGHLIGHT

RECEIVING WAVEFORM - 20MHZ



DRIVING WAVEFORM - 20MHz





For An In-Depth Discussion On Applying SCSI, Request Linfinity Application Note: "Understanding The Single-Ended SCSI Bus"

PACKAGE ORDER INFORMATION



Note: All surface-mount packages are available in Tape & Reel. Append the letter "T" to part number. (i.e. LX5211CDPT)

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ABSOLUTE MAXIMUM RATINGS	(Note 1)
TermPwr Voltage	+7V
Signal Line Voltage	
Regulator Output Current	
Operating Junction Temperature	
Plastic (PWP, DP Packages)	150°C
Storage Temperature Range	65°C to 150°C
Lead Temperature (Soldering, 10 seconds)	300°C
Note 1. Exceeding these ratings could cause damage to the devierespect to Ground. Currents are positive into, negative of terminal.	C

THERMAL DATA

PWP PACKAGE:

THERMAL RESISTANCE-JUNCTION TO LEADS, $\theta_{_{JL}}$	27°C/W
THERMAL RESISTANCE-JUNCTION TO AMBIENT, $\theta_{_{JA}}$	100°C/W
DP PACKAGE:	
THERMAL RESISTANCE-JUNCTION TO LEADS, $\theta_{_{JL}}$	20°C/W
THERMAL RESISTANCE-JUNCTION TO AMBIENT, $\theta_{_{JA}}$	50°C/W

Junction Temperature Calculation: $T_J = T_A + (P_D \ x \ \theta_{JA})$. The θ_{JA} numbers are guidelines for the thermal performance of the device/pc-board system. All of the above assume no ambient airflow.

POWER UP / POWER DOWN FUNCTION TABLE

DISCONNECT	Outputs	Quiescent Current
L	HI Z	0.5µA
Н	Enabled	600µA
Open	HI Z	0.5μΑ

PACKAGE PIN OUTS 24 _____ T6 T8 2 2 23 **T5** 22 REG OUT 21 N.C. **T9** _____3 N.C. 20 HEATSINK/GND GND ____5 HEATSINK/GND _____6 19 HEATSINK/GND HEATSINK/GND _____7 18 HEATSINK/GND HEATSINK/GND = 8 17 HEATSINK/GND HEATSINK/GND -9 16 N.C. 15 V_{TERM} DISCONNECT 10 **TI** 🖂 11 **T2** 12 _13 _____ **T3** PWP PACKAGE (Top View) 77 III O 78 III 2 16 🎞 **T6** 15 III T5 **T9** 🖂 3 14 REG OUT HEATSINK/GND 4 13 HEATSINK/GND 12 HEATSINK/GND 11 W V 10 **T4** T2 🖂 8 9 ____ T3 DP PACKAGE (Top View)



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RECOMMENDED OPERATING CONDITIONS (Note 2)					
Parameter	Symbol	Recommended Operating Conditions			Units
	Sylliooi	Min.	Тур.	Max.	Units
TermPwr Voltage	V _{TERM}	4		5.25	٧
Signal Line Voltage		0		5	٧
DISCONNECT Input Voltage		0		V _{TERM}	٧
Output Capacitor on REGOUT		2.2			μF
Operating Virtual Junction Temperature Range					
LX5211C		0		125	°C

Note 2. Range over which the device is functional.

ELECTRICAL CHARACTERISTICS

(Unless otherwise specified, these specifications apply over the operating ambient temperature range of $0^{\circ}\text{C} \leq T_{A} \leq 70^{\circ}\text{C}$. TemPwr = 4.75V, DISCONNECT = 0V. Low duty cycle pulse testing techniques are used which maintains junction and case temperatures equal to the ambient temperature.)

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Parameter	Symbol	Test Conditions		LX5211		
Parameter	Syllion		Min.	Тур.	Max.	Units
Supply Current Section						
TermPwr Supply Current		All term lines = Open		0.6	1.2	mA
		All term lines = 0.5V		194	210	mA
Power Down Mode		DISCONNECT = Open		0.5	1	μA
Output Section (Terminator Lin	nes)					
Terminator Impedance		$I_{TERM} = -5$ mA to -15 mA, $T_A = 25$ °C	104	110	116	Ω
		I _{TERM} = -5mA to -15mA	100	110	120	Ω
Terminator Output High Voltage			2.7	2.9		٧
Max. Output Current		$V_{OUT} = 0.5V, T_A = 25^{\circ}C$	-20.3	-21.8	-23	mA
		$V_{OUT} = 0.5V, 0^{\circ}C \le T_{A} \le 70^{\circ}C$	-19.0	-21.8	-23	mA
		$V_{OUT} = 0.5V, V_{TERM} = 4V, T_A = 25^{\circ}C$	-19.5	-21.8	-23	mA
		$V_{OUT} = 0.5V, V_{TERM} = 4V, 0^{\circ}C \le T_{A} \le 70^{\circ}C$	-18.0	-21.8	-23	mA
Output Leakage		DISCONNECT = Open, V _{TERM} = 0V to 5.25V		10	400	nA
Output Capacitance		DISCONNECT = Open		2.5		рF
Sink Current		$V_{OUT} = 4V$	20	30		mA
Regulator Section	·					
Regulator Output Voltage				3.6		٧
Line Regulation		V _{TERM} = 4V to 6V		10	20	m۷
Load Regulation		I _{REG} = 0 to -50mA		20	50	m۷
Drop Out Voltage		$I_{REG} = -50 \text{mA}$		0.7	1.0	٧
Short Circuit Current		$V_{REG} = OV$		-200	-350	mA
Thermal Shutdown				150		°C
Disconnect Section			-			
DISCONNECT Threshold			0.8		2.0	٧
Input Current		DISCONNECT = 0V			40	μA
		1				<u> </u>

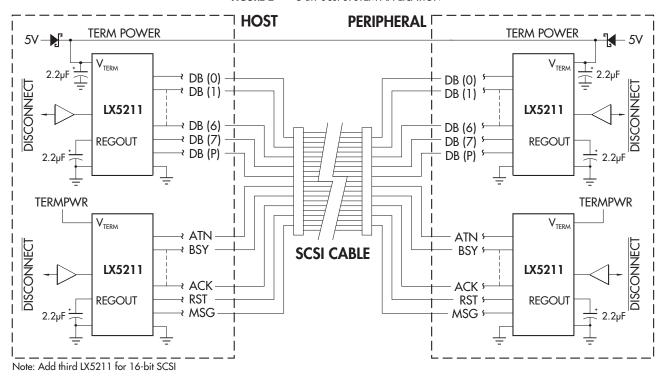


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APPLICATION SCHEMATIC

FIGURE 2 — 8-BIT SCSI SYSTEM APPLICATION



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