SHARP®

Integrated Circuits Group

APPLICATIONS:
Pager
PDA
Set Top Box
Cellular Phone

LH51V1016JS
1M SRAM

FEATURES

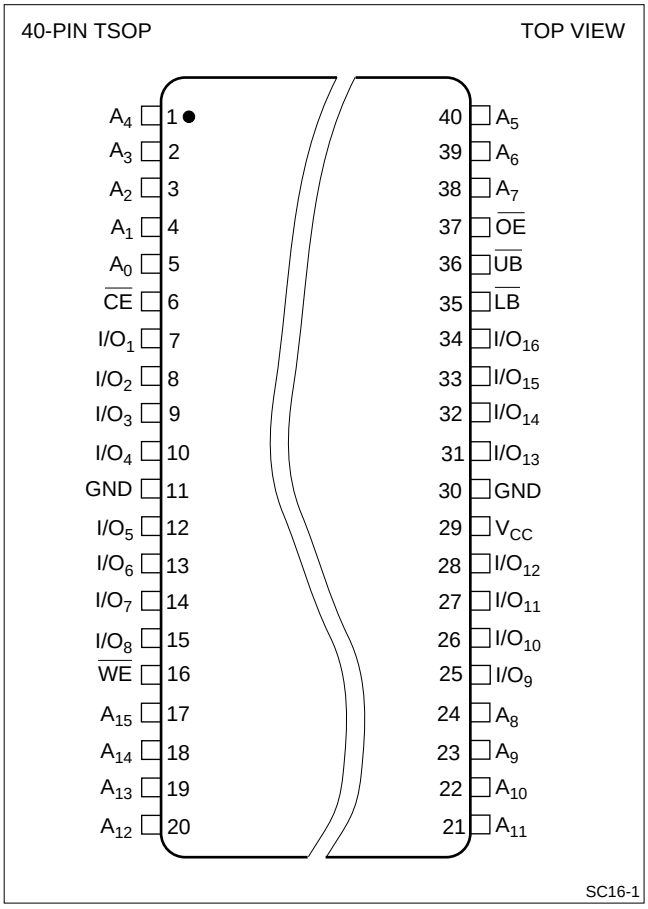
- Access time 70 ns (MAX.)
- Operating current
  - 45 mA (MAX.)
  - 5 mA (MAX. t_{RC}, t_{WC} = 1 µs)
- Standby current 25 µA (MAX.)
- Data retention current 0.5 µA (MAX.) V_{CCDR} = 3 V, T_A = 25°C)
- Single power supply 2.7 V to 3.6 V
- Operating temperature -25°C to +85°C
- Fully static operation
- Three-state output
- Not designed or rated as radiation hardened
- 40-pin TSOP (TSOP40-P-1014) plastic package
- P-type bulk silicon

DESCRIPTION

The LH51V1016JS is a static RAM organized as 65,536 × 16 bit which provides low-power standby mode.

It is fabricated using silicon-gate CMOS process technology.

40-PIN TSOP PINOUT



PIN DESCRIPTION

PIN	DESCRIPTION
A_0 to A_{15}	Address Input
I/O_1 to I/O_{16}	Data Input/Output
$\overline{CE}$	Chip Enable
$\overline{LB}$	Byte Enable Input (I/O_1 – I/O_8)
$\overline{UB}$	Byte Enable Input (I/O_9 – I/O_{16})
$\overline{WE}$	Write Enable
$\overline{OE}$	Output Enable
V_{CC}	Power Supply
GND	Ground

This Product Information is from Specification: MS-09X01; Issue Date: October 8, 1997.