

===== PRODUCT DATA =====

Micro International, Inc

PART NUMBER

LDZ752A and LDZ752AT

Micro-LID Zener Diode



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## Micro-LID Zener Diodes LDZ752A and LDZ752AT

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### Description:

The LDZ752A (untinned) and LDZ752AT (tinned) are zener diodes in very small, rugged, surface mount, 4-post ceramic packages (Micro International manufactured package p/n 4-075-1). The LDZ752A and LDZ752AT meet the general specifications of the 1N752A zener diode. The 4-075-1 Micro-LID package is a 4-post, leadless ceramic carrier which can be provided with gold metallized or pre-tinned lands, and is approved for military, medical implant, sensor, and high reliability applications. Other 5.6 volt zener diodes with different tolerance and current characteristics are available upon request.

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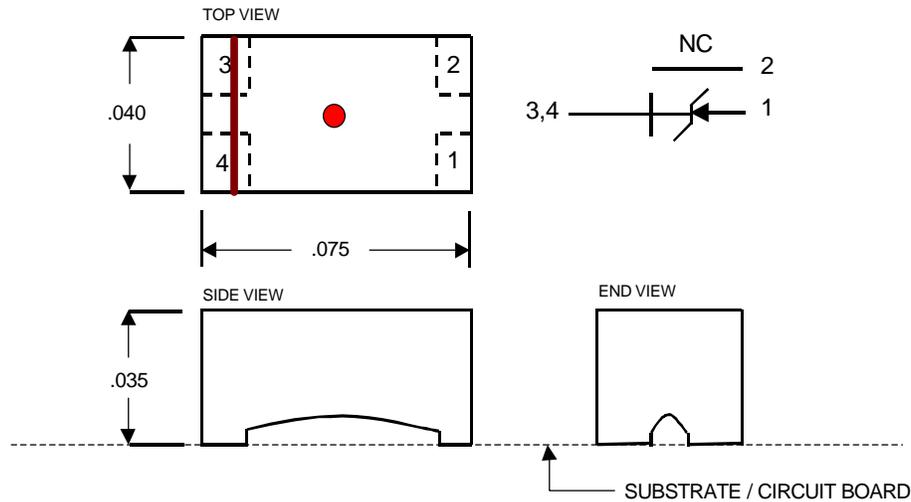
### Maximum Ratings:

Parameter	Symbol	Rating
Zener Voltage	$V_z$	5.6 V
Forward Current	$I_f$	200 mA
Zener Current	$I_z$	50 mA
Total Dissipation	$P_t$	350 mW
Operating Junction Temperature	$T_j$	150°C
Storage Temperature	$T_{stg}$	-65°C to 150°C
Operating Temperature	$T_{oper}$	-55°C to 125°C

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### Outline / Schematic:



### Dimensions / Marking:

Length	.075" $\pm$ .003"	Post 1 (Anode)	.015" x .010" typ
Width	.040" $\pm$ .003"	Post 2 (NC)	.015" x .010" typ
Height	.035" $\pm$ .003"	Post 3,4 (Cathode)	.015" x .012" typ

Marking on back of package : Brown Stripe over Cathode and Red Dot  
(post down configuration) in Center

### Standard In-Process Screening Requirements:

- Semiconductor die and Micro-LID package visual inspection
- Wire pull test
- 24 hour stabilization bake at 150°C
- 10 temperature cycles from -55°C to 125°C
- 100% electrical test of dc characteristics at 25°C
- Final visual inspection

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**Electrical Characteristics (25°C Ambient)**

<b>Parameter</b>	<b>Symbol</b>	<b>Min</b>	<b>Typ</b>	<b>Max</b>	<b>Units</b>
Zener Voltage I <sub>z</sub> = 20 mA	V <sub>z</sub>	5.32	5.6	5.88	V
Reverse Current V <sub>r</sub> = 1 V	I <sub>r</sub>	--	--	1	uA
Zener Impedance I <sub>zt</sub> = 20 mA	Z <sub>zt</sub>	--	--	11	Ohms

\* Pulse test, pulse width ≤ 300 usec, duty cycle ≤ 2%

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