REVISIONS

REV.	DESCRIPTION	DATE	APPROVED
-	RELEASED ON ECN #E0210	11/1/95	MA

1. **Specification subject to change without notice.**
2. **All dimensions and specifications apply to standard modules. This information may vary for modules with optional features.**
3. **All dimensions are in millimeters.**
4. **Precautions:These precautions apply equally to modules from all makers, not just Densitron. Violation of these guidelines may void the warranty and can cause problems ranging from erratic operation to catastrophic display failure.**

Handling precautions:

- This device is susceptible to Electro-Static Discharge (ESD) damage. Observe Anti-Static precautions.

Power supply precautions:

- Identify and, at all times, observe absolute maximum ratings for both logic and LC drivers. Note that there is some variance between models.
- Prevent the application of reverse polarity to V_{DD} and V_{SS}, however briefly.
- Use a clean power source free from transients. Power up conditions are occasionally “jolting” and may exceed the maximum ratings of the module.
- The +5V power of the module should also supply the power to all devices which may access the display. Don’t allow the data bus to be driven when the logic supply to the module is turned off.
- DO NOT install a capacitor between the V_O (contrast) pin and ground. V_{DD} must, at all times, exceed the V_O voltage level. The capacitor combines with the contrast potentiometer to form an R-C network which “holds-up” V_O, at power-down, possibly damaging the module.

Operating precautions:

- DO NOT plug or unplug the module when the system is powered up.
- Minimize the cable length between the module and host MPU. (Recommended max. length 30 cm).
- For models with EL or CCFL backlights, do not disable the backlight by interrupting the HV line. Unloaded inverters produce voltage extremes which may arc within a cable or at the display.
- Operate the module within the limits of the modules temperature specifications.

Mechanical / Environmental precautions:

- Improper soldering is the major cause of module difficulty. Use of flux cleaner is not recommended as they may seep under the elastomeric connection and cause display failure. Densitron recommends the use of Kester “245” no-clean solder.
- Mount the module so that it is free from torque and mechanical stress.
- Surface of LCD panel should not be touched or scratched. The display front surface is an easily scratched, plastic polarizer. Avoid contact and clean only when necessary with soft, absorbent cotton dampened with petroleum benzene.
- ALWAYS employ anti-static procedure while handling the module.
- Prevent moisture build-up upon the module and observe the environmental constraints for storage temperature and humidity.
- DO NOT store in direct sunlight.
- If leakage of the liquid crystal material should occur, avoid contact with this material, particularly ingestion. If the body or clothing becomes contaminated by the liquid crystal material, wash thoroughly with water and soap.

Notes: (unless otherwise specified)

Unless otherwise specified:	APPROVALS	DATE	DENSITRON INTERNATIONAL PLC.	
Dimensions are mm Tolerances are: X = ± 3 .X = ± 0.5 .XX = ± 0.05 FSCM NO. 62483	DRAWN			
	CHECKED		TITLE: 64 X 240 GRAPHICS LCD MODULE	
	ISSUED		DWG. NO. LM3083	SHEET 1 OF 8

1.0 DESCRIPTION

Dot matrix display module consisting of liquid Crystal Display, printed circuit board, metal support frame.

Available LC fluid types are: NTN (supertwisted nematic).

Other options include electroluminescent (EL) white backlighting.

2.0 MECHANICAL CHARACTERISTICS

Item	Specifications	Unit
Package Dimensions	180.0 (W) x 75.0 (H) x 10.1 max (D)	mm
Display format	240 dots (W) x 64 dots (H)	-
Driving method	1/64	duty
Dot size	0.49 (W) x 0.49 (H)	mm
Dot pitch	0.53 (W) x 0.53 (H)	mm
Active display area	127.16 (W) x 33.88 (H)	mm
Viewing area	132.0 (W) x 39.0 (H)	mm
Weight		g

Notes: W-Width; H-Height; D-Depth.

3.0 ABSOLUTE MAXIMUM RATINGS

V_{SS}=0V;T_a=25°C

Item	Symbol	NTN		Unit
		Min.	Max.	
Logic supply voltage	V_{DD}-V_{SS}	0	7	V
LC driver supply voltage	V_{DD}-V_{EE}	0	23	V
Operating temperature Storage temperature (Note 1)	T_{OP} T_{ST}	-10 -20	+50 +60	°C
Humidity: Operating (@40°C) Non-operating (@40°C)	- -	- -	85% 95%	RH (Note 2) RH (Note 2)

Notes: 1: Tested to 100 hrs.
2: Refers to non-condensing conditions.

4.0 ELECTRICAL CHARACTERISTICS

V_{DD}=5±0.25V;T_a=25°C

Item	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Input “High” voltage	V_{IH}	-	0.8	-	V_{DD}	V
Input “Low” voltage	V_{IL}	-	V_{SS}	-	0.2	V
Output “High” voltage	V_{OH}	I_{OH}=0.205mA	2.2	-	-	V
Output “Low” voltage	V_{OL}	I_{OL}=1.2mA	-	-	0.8	V
Power supply current	I_{EE}	V_{EE}=-20V	-	1	-	mA
Power supply current	I_{DD}	V_{DD}=5.0V	-	12	-	mA

5.0 RECOMMENDED LC DRIVE VOLTAGE (V_{DD}-V_O)

V_{DD}=5.0±0.25V

Temperature	NTN
Ta=-20°C	-
Ta=0°C	17.3
Ta= 25°C	15.7
Ta= 50°C	15.0
Ta= 70°C	-

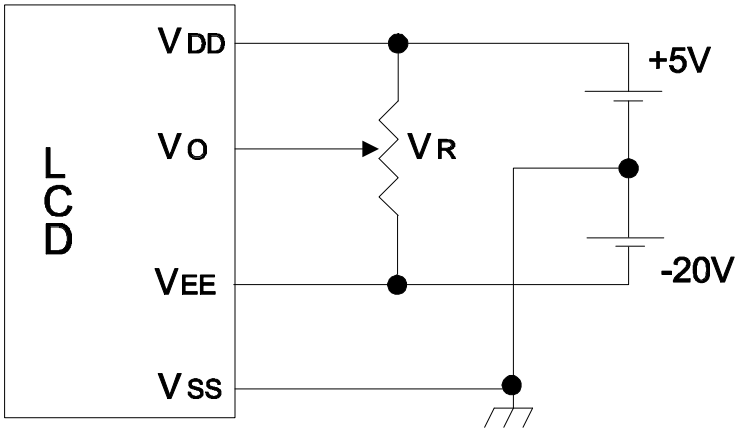
6.0 BACKLIGHT SPECIFICATIONS:

Ta=20°C,60%RH,Darkroom.

Item	Symbol	Typ.	Max.	Unit
EL lamp input voltage	V_{EL}	100	150	Vrms
EL lamp input current	I_{EL}	14.0	20.0	mA
Life to half initial brightness	-	2500	3000	Hours
EL lamp input frequency	F_{EL}	400	800	Hz
Recommended backlight inverter	-	DAS5V14	-	-

7.0 POWER SUPPLY

NTN

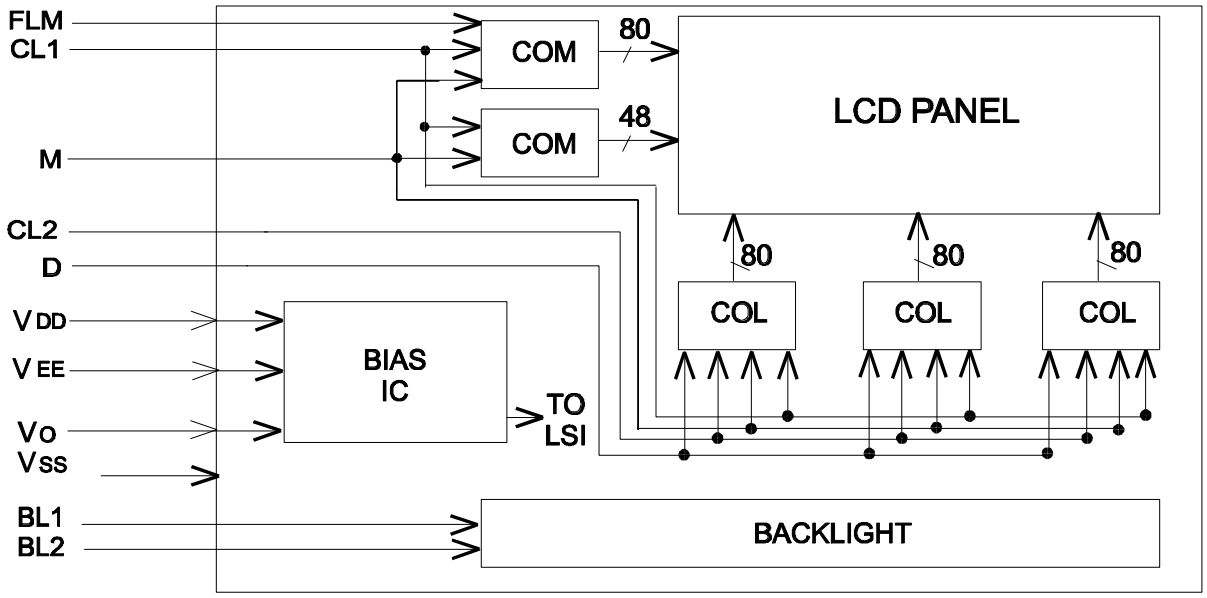


V_R= 10K - 20K ohm

8.0 INTERFACE DESCRIPTION

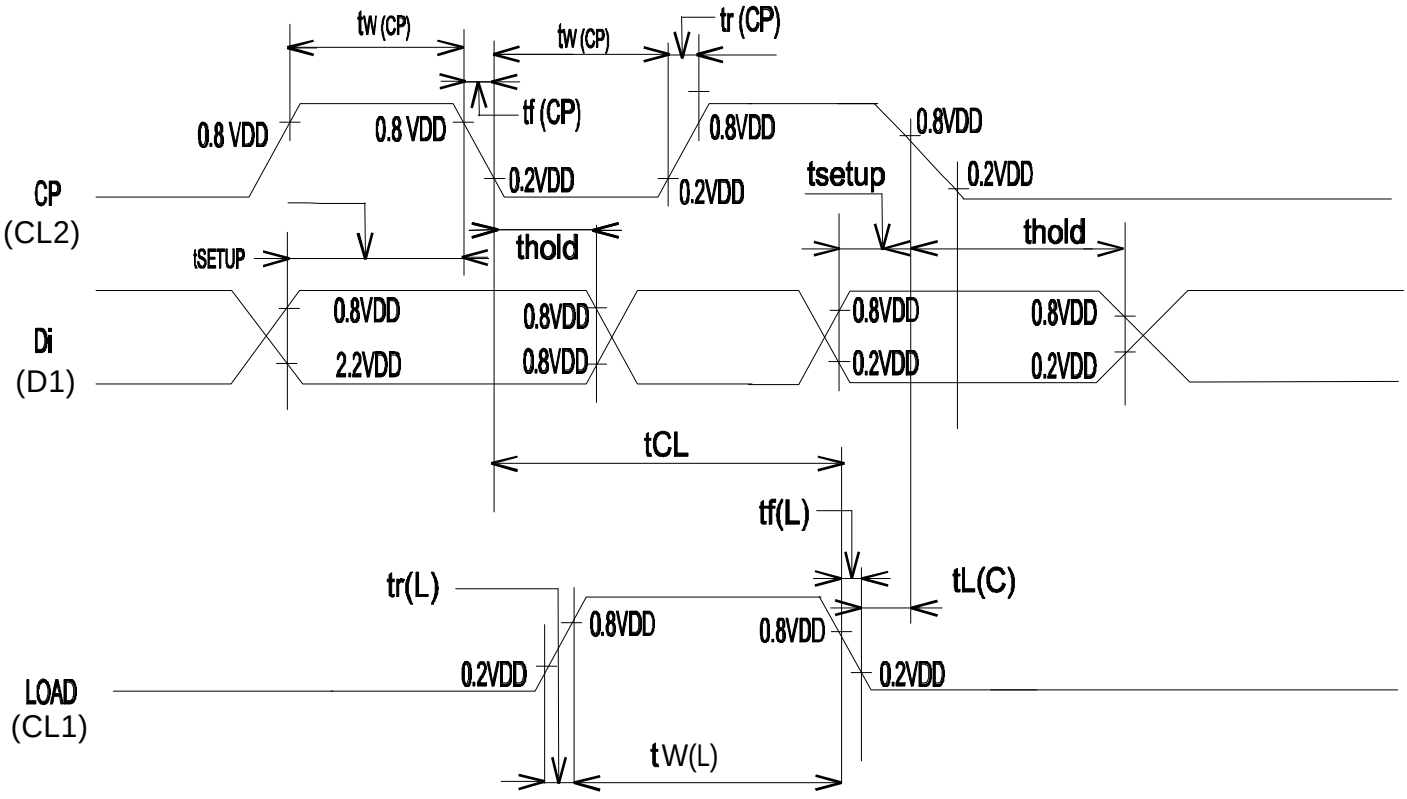
Pin No.	Symbol	I/O	Function
1	D	I	Display data H.. dot ON, L.. dot OFF
2	FLM	I	First line marker indicates the beginning of each display cycle
3	M	I	Control signal for A.C. driving
4	CL1	I	The CL1 latches the serial data in the shift registers
5	CL2	I	Clock signal for shifting the serial data
6	N/C	-	No connection
7	Vdd	-	Power supply for logic circuit
8	Vss	-	Ground
9	Vee	-	Power supply for LCD driving
10	Vo	-	Operating voltage for LCD drivers
BL1	VEL	-	EL backlight input voltage (from output of DC-AC inverter)
BL2	VEL	-	EL backlight input voltage (from output of DC-AC inverter)

9.0 BLOCK DIAGRAM:



10.0 TIMING CHARACTERISTICS

Item	Symbol	Min.	Typ.	Max.	Unit
Max Clock Frequency	f_{cp}	3.3	-	-	MHz
Clock Pulse Width	$t_{w(CP)}$	125	-	-	nS
LOAD Pulse Width	$t_{w(L)}$	125	-	-	nS
Data Set-up Time	t_{setup}	50	-	-	nS
CP→LOAD time	t_{CL}	250	-	-	nS
LOAD→CP time	t_{LC}	0	-	-	nS
CP Rise/Fall time	$t_{r(CP)1}$ $t_{f(CP)}$	-	-	50	nS
LOAD Rise/Fall time	$t_{r(L)1}$ $t_{f(L))}$	-	-	1	µs
Data Hold time	t_{hold}	50	-	-	ns

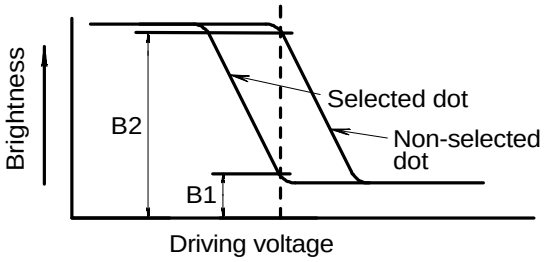


12.0 OPTICAL CHARACTERISTICS

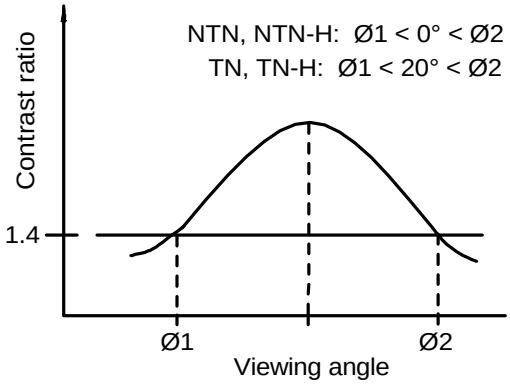
Item	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Contrast ratio	K	Ø=20° θ=0°	4	-	-	-
Viewing angle	Ø2-Ø1	θ=0° K>1.4	40	-	-	Deg.
	θ	Ø=20° K=1.4	±30	-	-	Deg.
Response time Rise	t_r	Ø=20° θ=0°	-	150	250	mS
Fall	t_f	Ø=20° θ=0°	-	150	250	mS

DEFINITION OF CONTRAST RATIO (K)

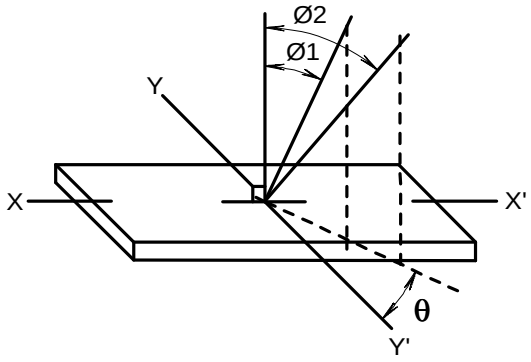
$$K = \frac{\text{Brightness of non-selected dots}}{\text{Brightness of selected dots}}$$



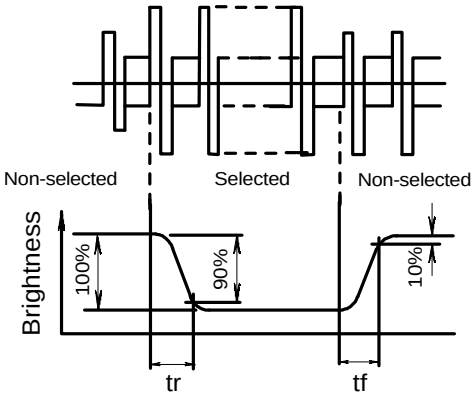
CONTRAST VERSUS VIEWING ANGLE

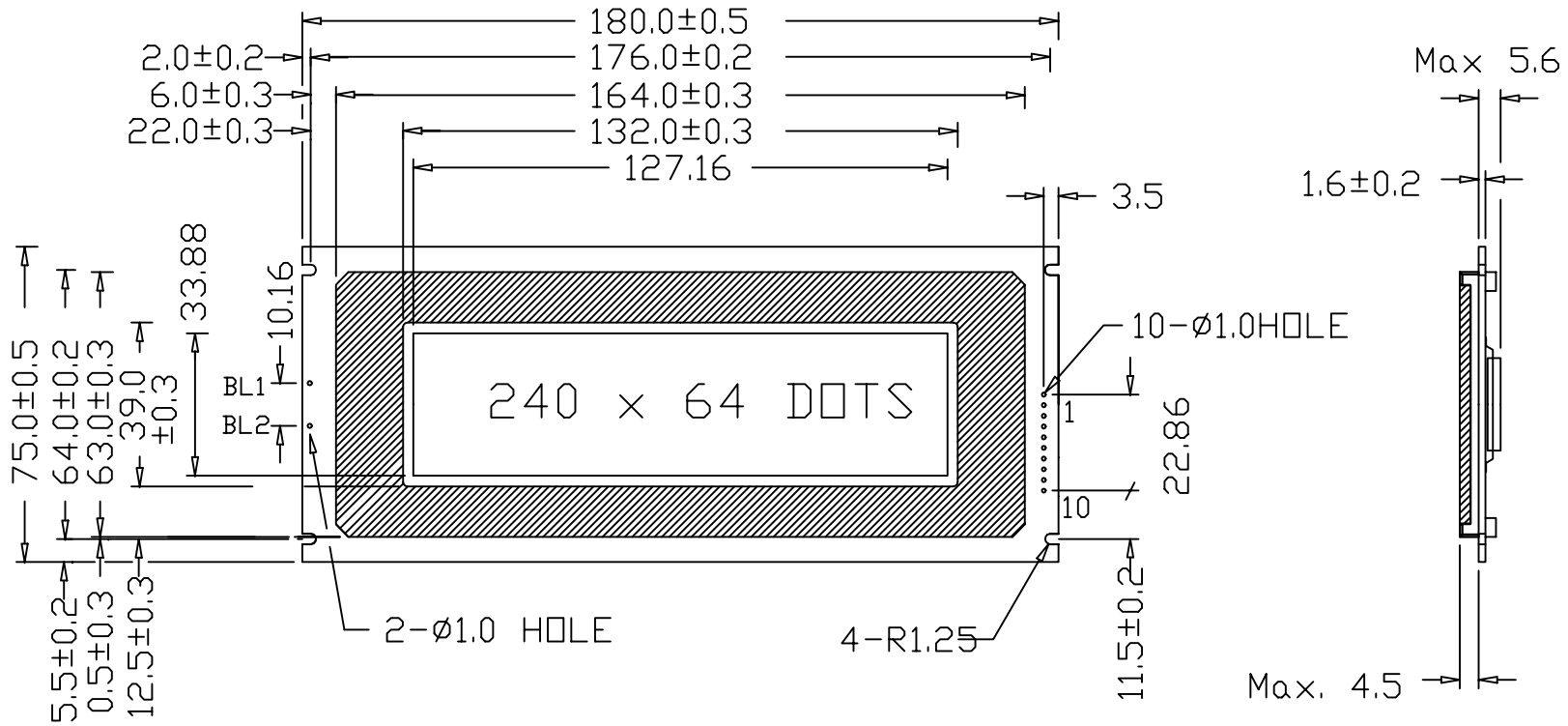


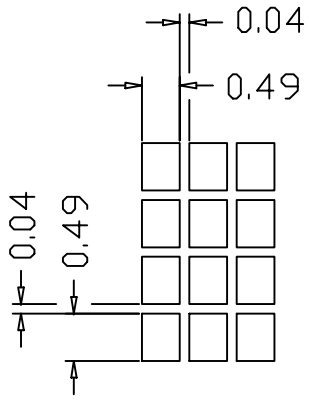
DEFINITION OF ANGLES Ø AND θ



DEFINITION OF OPTICAL RESPONSE







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14.0 PART NUMBER DESCRIPTION FOR AVAILABLE OPTIONS

LM3083①②64G240③④⑤

①

POLARIZER TYPE

A = Reflective: light background, no backlight
B = Transflective: light background with white EL backlight

②

NOT APPLICABLE - LEAVE BLANK

③

FLUID TYPE AND POWER SUPPLY

D = NTN with +5VDC and external negative voltage operation

④

FLUID TYPE AND TEMPERATURE COMPENSATION CIRCUIT

N = NTN, NTN-H

⑤

COLOR FOR NTN FLUID

G = Gray background
Y = Yellow background