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REVISIONS							
REV.	DESCRIPTION	DATE	APPROVED				
-	RELEASED ON ECN #E0737	5/18/98	MA				

- 1. Specification subject to change without notice.
- 2. All dimensions and specifications apply to standard modules. This information may vary for modules with optional features.
- 3. All dimensions are in millimeters.
- 4. Precautions:These precautions apply equally to modules from all makers, not just Densitron. Violation of these guidelines may void the warranty and can cause problems ranging from erratic operation to catastrophic display failure.

#### Handling precautions:

♦ This device is susceptible to Electro-Static Discharge (ESD) damage. Observe Anti-Static precautions.

#### Power supply precautions:

- Identify and, at all times, observe absolute maximum ratings for both logic and LC drivers. Note that there is some variance between models.
- Prevent the application of reverse polarity to VDD and Vss, however briefly.
- Use a clean power source free from transients. Power up conditions are occasionally "jolting" and may exceed the
  maximum ratings of the module.
- ♦ The +5V power of the module should also supply the power to all devices which may access the display. Don't allow the data bus to be driven when the logic supply to the module is turned off.
- ◆ DO NOT install a capacitor between the Vo (contrast) pin and ground. VDD must, at all times, exceed the Vo voltage level. The capacitor combines with the contrast potentiometer to form an R-C network which "holds-up" Vo, at power-down, possibly damaging the module.

#### Operating precautions:

- DO NOT plug or unplug the module when the system is powered up.
- Minimize the cable length between the module and host MPU. (Recommended max. length 30 cm).
- For models with EL backlights, do not disable the backlight by interrupting the HV line. Unloaded inverters produce voltage extremes which may arc within a cable or at the display.
- Operate the module within the limits of the modules temperature specifications.

#### Mechanical / Environmental precautions:

- Improper soldering is the major cause of module difficulty. Use of flux cleaner is not recommended as they may seep under the elastomeric connection and cause display failure. Densitron recommends the use of Kester "245" no-clean solder.
- Mount the module so that it is free from torque and mechanical stress.
- Surface of LCD panel should not be touched or scratched. The display front surface is an easily scratched, plastic
  polarizer. Avoid contact and clean only when necessary with soft, absorbent cotton dampened with petroleum
  benzene
- ALWAYS employ anti-static procedure while handling the module.
- Prevent moisture build-up upon the module and observe the environmental constraints for storage temperature and humidity.
- ♦ DO NOT store in direct sunlight.

• If leakage of the liquid crystal material should occur, avoid contact with this material, particularly ingestion. If the body or clothing becomes contaminated by the liquid crystal material, wash thoroughly with water and soap.

**APPROVALS** Unless otherwise DATE DENSITRON INTERNATIONAL PLC. specified: DRAWN Dimensions are mm TITLE Tolerances are: CHECKED 1 LINE X 24 CHARACTERS  $X = \pm 3$  $.X = \pm 0.5$ ISSUED DWG. NO.  $.XX = \pm 0.05$ LM2303 SHEET 1 OF 8 FSCM NO. 62483

#### 1.0 DESCRIPTION

Dot matrix display module consisting of a Liquid Crystal Display, CMOS driver and controller LSI, printed circuit board, metal support frame.

Available LC fluid type is: NTN (supertwisted nematic).

Options include electroluminescent (EL) backlighting and/or on-board negative voltage generation.

#### 2.0 MECHANICAL CHARACTERISTICS

Item	Specifications	Unit
Package Dimensions	208.0 (W) x 40.0 (H) x 10.6 max (D)	mm
Display format	1 line x 24 characters	-
Character font format	5 (W) x 7 (H) with attached cursor	dots
Driving method	1/16	duty
Dot size	1.152 (W) x 1.765(H)	mm
Dot pitch	1.158(W) x 1.771(H)	mm
Character Size	6.0 (W) x 14.54(H)	mm
Active display area	171.6 (W) x 14.54 (H)	mm
Viewing area	178.0 (W) x 23.0 (H)	mm
Weight		g

Notes:W-Width;H-Height;D-Depth.

## 3.0 ABSOLUTE MAXIMUM RATINGS

Vss=0V;Ta=25°C

Item	Symbol	NTN		Unit
		Min.	Max.	
Logic supply voltage	VDD-VSS	0	7	V
LC driver supply voltage	VDD-VO	0	10	V
Operating temperature	Тор	0	+50	°C
Storage temperature (Note 1)	Tst	-20	+70	
Humidity: Operating (@40°C)	-	-	85%	RH (Note 2)
Non-operating (@40°C)	-	-	95%	RH (Note 2)

Notes: 1: Tested to 100 hrs.

2: Refers to non-condensing conditions.

3. With backlight off.

### 4.0 ELECTRICAL CHARACTERISTICS

VDD=5±0.25V;Ta=25°C

Item	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Input "High" voltage	VIH	-	2.2	•	Vdd	V
Input "Low" voltage	VIL	-	-	-	0.6	V
Output "High" voltage	Voн	Iон=0.205mA	2.4	•	-	V
Output "Low" voltage	Vol	IoL=1.2mA	-	-	0.4	V
Power supply current	IDD	VDD=5.0V	-	3	-	mA

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# 5.0 RECOMMENDED LC DRIVE VOLTAGE (VDD-Vo)

VDD=5.0±0.25V

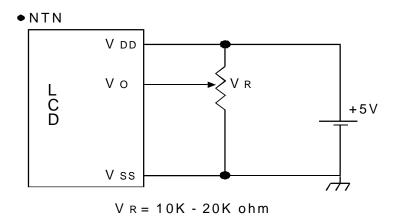
Temperature	NTN
Ta= -20°C	-
Ta= 0°C	4.9
Ta= 25°C	4.5
Ta= 50°C	4.1
Ta=70°C	-

### 6.0 BACKLIGHT SPECIFICATIONS:

Ta=20°C,60%RH,Darkroom.

			·	•
ltem	Symbol	Тур.	Max.	Unit
EL input voltage	VEL	100	150	V
EL input current	lel	13.0	20.0	mA
EL Lamp input frequency	FEL	400	800	Hz
Life to half initial brightness	-	2500	3000	Hours
Recommended backlight inverter	-	DAS5V7A	-	-

## 7.0 POWER SUPPLY

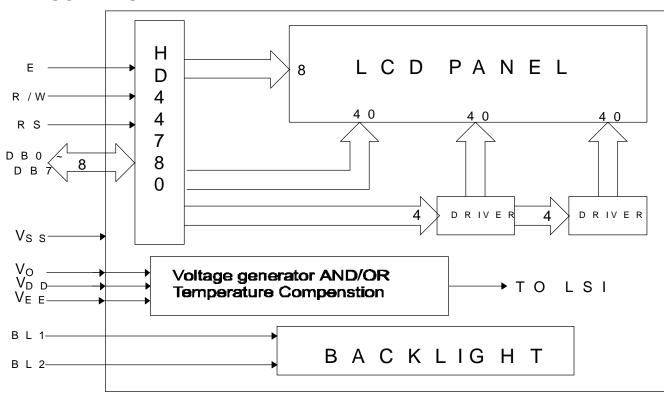


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## **8.0 INTERFACE DESCRIPTION**

Pin No.	Symbol	I/O	Function
1	Vss	-	Ground (0V)
2	Vdd	-	Logic Supply Voltage (+5V)
3	Vo	-	LC Drive voltage for contrast adjustment
4	RS	1	Register Select 0: Instruction Register
			1: Data Register
5	R/W	I	Read / Write 0: Data Write (Module←MPU)
			1: Data Read (Module→MPU)
6	Е	I	Enable Signal Active High (H→L)
7	DB0	I/O	Bi-directional data bus line 0
8	DB1	I/O	Bi-directional data bus line 1
9	DB2	I/O	Bi-directional data bus line 2
10	DB3	I/O	Bi-directional data bus line 3
11	DB4	I/O	Bi-directional data bus line 4
12	DB5	I/O	Bi-directional data bus line 5
13	DB6	I/O	Bi-directional data bus line 6
14	DB7	I/O	Bi-directional data bus line 7
15	N/C(VEE)	- (O)	No connection (Negative voltage output for models with on-
	, ,	, ,	board negative voltage generators)
16	N/C	-	No connection
BL1	VEL	-	EL backlight input voltage (from output of DC-AC inverter)
BL2	VEL	-	EL backlight input voltage (from output of DC-AC inverter)

### 9.0 BLOCK DIAGRAM:



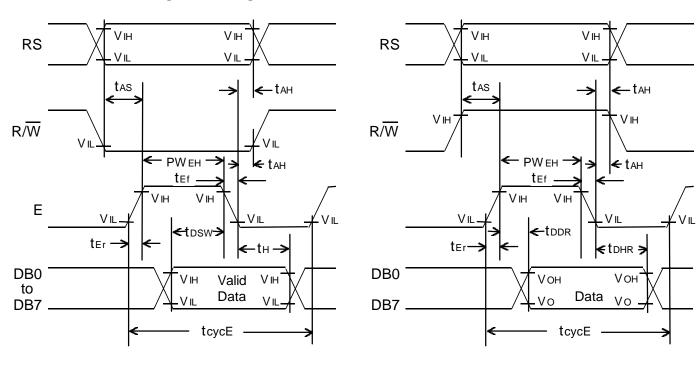
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## 10.0 TIMING CHARACTERISTICS

ltem	Symbol	Min.	Тур.	Max.	Unit
Enable cycle time	ТсусЕ	500	-	-	nS
Enable pulse width	PWEH	230	-	-	nS
Enable rise / fall time	ter/tef	-	-	20	nS
Address set-up time	tas	40	-	-	nS
Address hold time	tah	10	-	-	nS
Data delay time	tddr	-	-	160	nS
Data hold time (Write)	tDHW	10	-	-	nS
Data hold time (Read)	tdhr	5	-	-	nS
Data set-up time	tosw	80	-	-	nS

### WRITE OPERATION

# **READ OPERATION**



## 11.0 DD RAM ADDRESS vs. DISPLAY POSITION

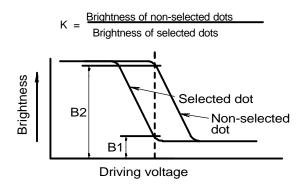
Character	1	2	3	4	5	6	7	8	9	10	11	 22	23	24
Line 1	00	01	02	03	04	05	06	07	08	09	0A	 15	16	17

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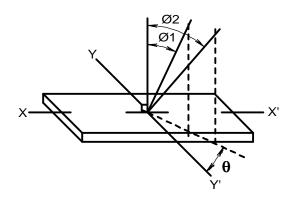
## 12.0 OPTICAL CHARACTERISTICS

Item	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Contrast ratio TN, TN-H	K	Ø=20° θ=0°	3	-	-	-
Contrast ratio NTN	K	Ø=20° θ=0°	4	-	-	-
Contrast ratio NTN-H	K	Ø=20° θ=0°	5	-	-	-
Viewing angle TN, TN-H	Ø2-Ø1	θ=0° K <u>&gt;</u> 1.4	20	-	-	Deg.
	θ	Ø=20° K=1.4	±30	-	-	Deg.
Viewing angle NTN	Ø2-Ø1	θ=0° K <u>&gt;</u> 1.4	40	-	-	Deg.
	θ	Ø=20° K=1.4	±30	-	-	Deg.
Viewing angle NTN-H	Ø2-Ø1	θ=0° K <u>&gt;</u> 1.4	40	-	-	Deg.
	θ	Ø=20° K=1.4	±40	-	-	Deg.
Response time Rise	tr	Ø=20° θ=0°	-	150	250	mS
Fall	tf	Ø=20° θ=0°	-	150	250	mS

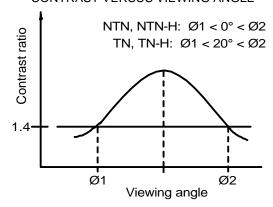
#### **DEFINITION OF CONTRAST RATIO (K)**



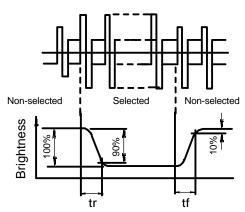
#### DEFINITION OF ANGLES Ø AND $\theta$



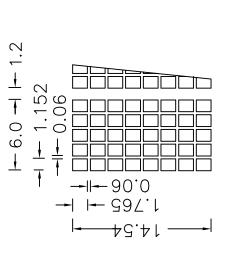
#### CONTRAST VERSUS VIEWING ANGLE



#### **DEFINITION OF OPTICAL RESPONSE**



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DWG. NO.

SHEET

LM2303

## 14.0 PART NUMBER DESCRIPTION FOR AVAILABLE OPTIONS

# LM2303①21C24345

Polarizer Type

A = Reflective: light background with no backlight

B = Transflective: light background, EL or LED backlight

2 NOT Applicable LEAVE BLANK

Fluid Type and Power Supply

S = NTN with +5VDC operation

Fluid Type/TN Viewing Direction

N = NTN

Background Color for NTN

G = Gray background

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