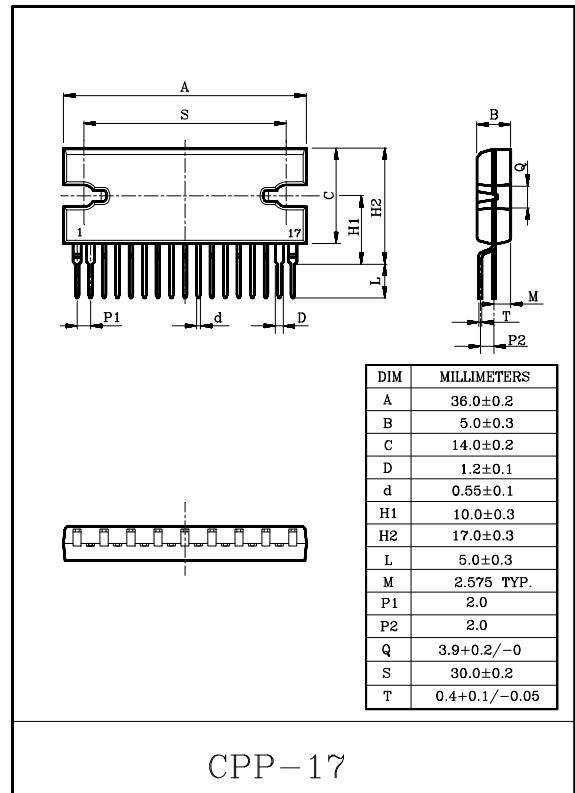


## 22W BTL DUAL AUDIO POWER AMPLIFIER

The thermal resistance  $\theta_{j-T}$  KIA6210AH package CPP-17(Compact Power Package 17pin), designed for low thermal resistance, has high efficiency of heat radiation. The temperature rise of chip can be reduced, and the influence from the degradation of the features due to the temperature rise at the high output can also be reduced. This stereo audio power IC, designed for car audio use, has two built-in channels to reduce the characteristic difference between L and R channels. In addition, the functions of stand-by and muting, and a variety of protection circuits are involved.

## FEATURES:

- Low Thermal Resistance.  
:  $\theta_{j-T}=1.5^{\circ}\text{C}/\text{W}$  (Infinite heat sink)
- High Power.  
:  $P_{\text{OUT}}(1)=22\text{W}(\text{Typ.})/\text{channel}$   
( $V_{\text{cc}}=14.4\text{V}$ ,  $f=1\text{kHz}$ ,  $\text{THD}=10\%$ ,  $R_L=4\Omega$ )  
:  $P_{\text{OUT}}(2)=19\text{W}(\text{Typ.})/\text{channel}$   
( $V_{\text{cc}}=13.2\text{V}$ ,  $f=1\text{kHz}$ ,  $\text{THD}=10\%$ ,  $R_L=4\Omega$ )
- Low Distortion.  
:  $\text{THD}=0.04\%(\text{Typ.})$   
( $V_{\text{cc}}=13.2\text{V}$ ,  $f=1\text{kHz}$ ,  $P_{\text{OUT}}=1\text{W}$ ,  $R_L=4\Omega$ ,  $G_V=50\text{dB}$ )
- Low Noise.  
:  $V_{\text{NO}}=0.30\text{mV}_{\text{rms}}(\text{Typ.})$   
( $V_{\text{cc}}=13.2\text{V}$ ,  $R_L=4\Omega$ ,  $G_V=50\text{dB}$ ,  $R_g=0\Omega$ ,  $BW=20\text{Hz} \sim 20\text{kHz}$ )
- Built-in stand-by function.  
(With ④ pin set at low, power is turned OFF.)
- Built-in muting function.  
(With ① pin set at low,  $I_{\text{SB}}=1\mu\text{A}(\text{Typ.})$  power is turned OFF.)  
:  $V(\text{Mute})=1\text{V}(\text{Typ.})$
- Built-in various protection circuits  
Protection circuits : Thermal shut down, over voltage,  $\text{OUT} \rightarrow V_{\text{cc}}$  short,  
 $\text{OUT-GND}$  short and  $\text{OUT-OUT}$  short.
- Operating supply voltage :  $V_{\text{cc}}=9 \sim 18\text{V}$ .



# KIA6210AH

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## MAXIMUM RATINGS (Ta=25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Peak Supply Voltage (0.2sec)	V <sub>CC</sub> surge	50	V
DC Supply Voltage	V <sub>CC</sub> DC	25	V
Operating Supply Voltage	V <sub>CC</sub> opr	18	V
Output Current (Peak)	I <sub>O</sub> (Peak)	9	A
Power Dissipation	P <sub>D</sub>	50	W
Operating Temperature	T <sub>opr</sub>	-30~85	°C
Storage Temperature	T <sub>stg</sub>	-55~150	°C

## ELECTRICAL CHARACTERISTICS

(Unless otherwise specified, V<sub>CC</sub>=13.2V, R<sub>L</sub>=4Ω, f=1kHz, Ta=25°C)

CHARACTERISTIC	SYMBOL	TEST CIRCUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Quiescent Supply Current	I <sub>CCQ</sub>	-	V <sub>IN</sub> =0	-	120	250	mA
Output Power	P <sub>OUT(1)</sub>	-	V <sub>CC</sub> =14.4V, THD=10%	-	22	-	W
	P <sub>OUT(2)</sub>	-	THD=10%	16	19	-	
Total Harmonic Distortion	THD		P <sub>OUT</sub> =1W	-	0.04	0.4	%
Voltage Gain	G <sub>V</sub>	-	-	48	50	52	dB
Output Noise Voltage	V <sub>NO</sub>	-	R <sub>g</sub> =0Ω, BW=20Hz~20kHz	-	0.30	0.70	mV <sub>rms</sub>
Ripple Rejection Ratio	R.R	-	f <sub>ripple</sub> =100Hz, R <sub>g</sub> =600Ω	40	54	-	dB
Input Resistance	R <sub>IN</sub>	-	-	-	30	-	kΩ
Output Offset Voltage	V <sub>offset</sub>	-	V <sub>IN</sub> =0	-300	0	300	mV
Current at Stand-by State	I <sub>SB</sub>	-	-	-	1	10	µA
Cross Talk	C.T	-	R <sub>g</sub> =600Ω, V <sub>OUT</sub> =0dBm	-	60	-	dB
④ Pin Control Voltage	V <sub>SB</sub>	-	Stand-by→OFF(Power→ON)	2.5	-	V <sub>CC</sub>	V
① Pin Control Voltage	V <sub>(MUTE)</sub>	-	Mute→ON (Power→OFF)	-	1.0	2.0	V

# KIA6210AH

## BLOCK DIAGRAM AND TEST CIRCUIT

KIA6210AH ( $G_V=50\text{dB}$ )

