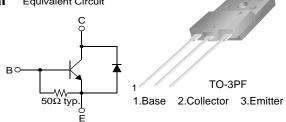


KSD5702

High Voltage Color Display Horizontal Equivalent Circuit **Deflection Output**

(Damper Diode Built In)

- High Collector-Base Voltage : V_{CBO} =1500V High Switching Speed t_F = 0.4 μ s (Max.)
- For Color TV



NPN Triple Diffused Planar Silicon Transistor

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parameter	Value	
V _{CBO}	Collector-Base Voltage	1500	V
V _{CEO}	Collector-Emitter Voltage	800	V
V _{EBO}	Emitter-Base Voltage	6	V
I _C	Collector Current	6	А
I _{CP}	Collector Current (Pulse)	16	А
P _C	Collector Dissipation (T _C =25°C)	60	W
TJ	Junction Temperature	150	°C
T _{STG}	Storage Temperature	- 55 ~ 150	°C

Electrical Characteristics T_C=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
I _{CBO}	Collector Cut-off Current	$V_{CB} = 800V, I_{E} = 0$			10	μΑ
I _{EBO}	Emitter Cut-off Current	$V_{EB} = 4V, I_{C} = 0$	40		200	mA
h _{FE1} h _{FE2}	DC Current Gain	$V_{CE} = 5V, I_{C} = 1A$ $V_{CE} = 5V, I_{C} = 3A$	10 5		30 15	-
V _{CE} (sat)	Collector-Emitter Saturation Voltage	$I_C = 4A, I_B = 0.8A$		2	5	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	$I_C = 4A, I_B = 0.8A$			1.5	V
f _T	Current Gain Bandwidth Product	V _{CE} = 10V, I _C = 1A		3		MHz
V _F	Damper Diode Turn On Voltage	I _F = 6A			2	V
t _F	Fall Time	$V_{CC} = 200V, I_C = 4A$ $I_{B1} = 0.8A, I_{B2} = -1.6A$ $R_L = 50\Omega$			0.4	μs

Typical Characteristics

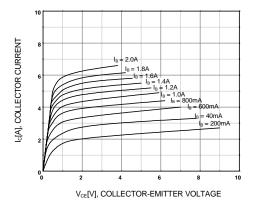


Figure 1. Static Characteristic

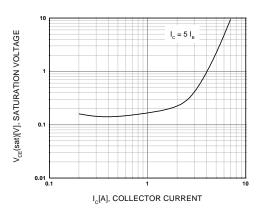


Figure 3. Collector-Emitter Saturation Voltage

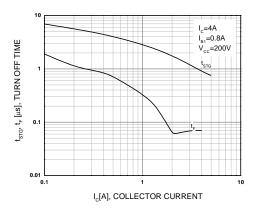


Figure 5. Switching Time

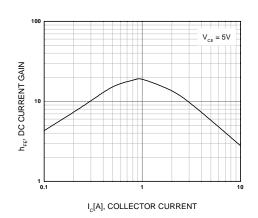


Figure 2. DC current Gain

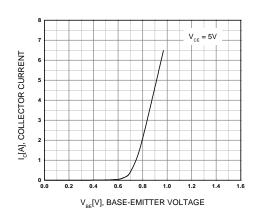


Figure 4. Base-Emitter On Voltage

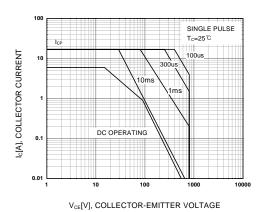


Figure 6. Safe Operating Area

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Typical Characteristics (Continued)

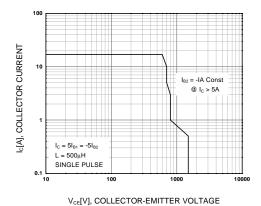


Figure 7. Reverse Bias Safe Operating Area

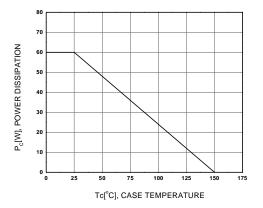
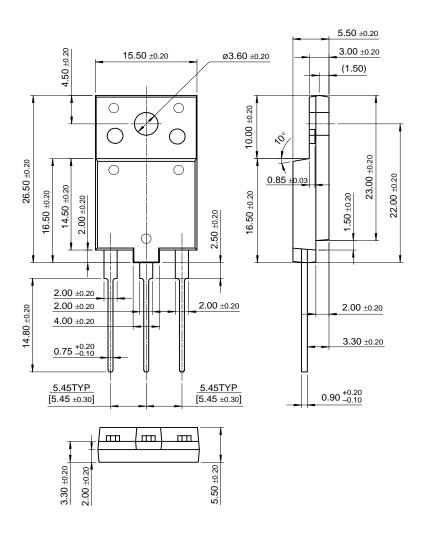


Figure 8. Power Derating

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Package Demensions

TO-3PF



Dimensions in Millimeters

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