

KA7406

Camera Motor Driver and Control IC

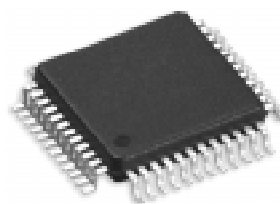
Features

- Output current up to 0.8A (Each channel).
- 4 function mode (CW, CCW, STOP and BRAKE) are controlled by 2 logic signals fed into 2 input terminals.
- Operating voltage range: $V_{CC}=2.5 \sim 7V$. (Exception battery check function)
- Built-in spark arrester diode.
- Low saturation voltage (1.5V max at 600mA).

Description

The KA7406 is a monolithic integrated circuit, suitable for the zoom and reel motor driver for camera, tape deck, any other consumer and industrial applications. The KA7406 has the functions which drive buffer for flash & battery check and auto-focus magnetic control.

48-QFP-1010E



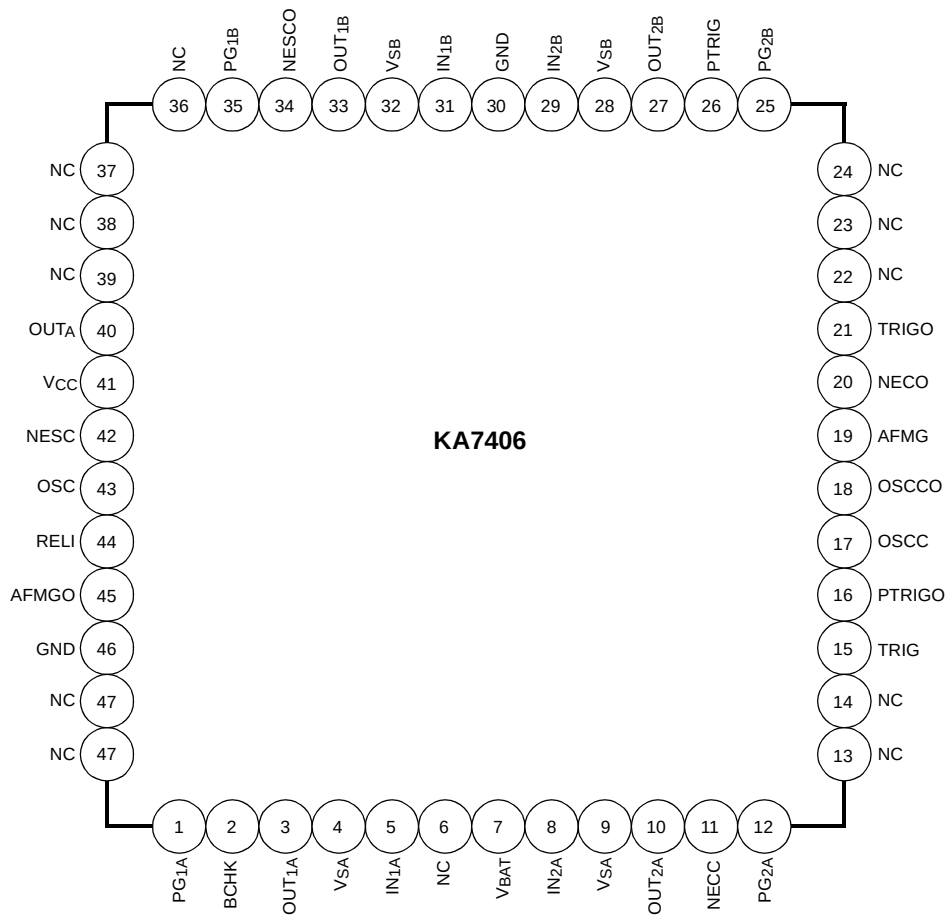
Typical Applications

- Camera system

Ordering Information

Device	Package	Operating Temp.
KA7406	48-QFP-1010E	-25°C to +75°C

Pin Assignments



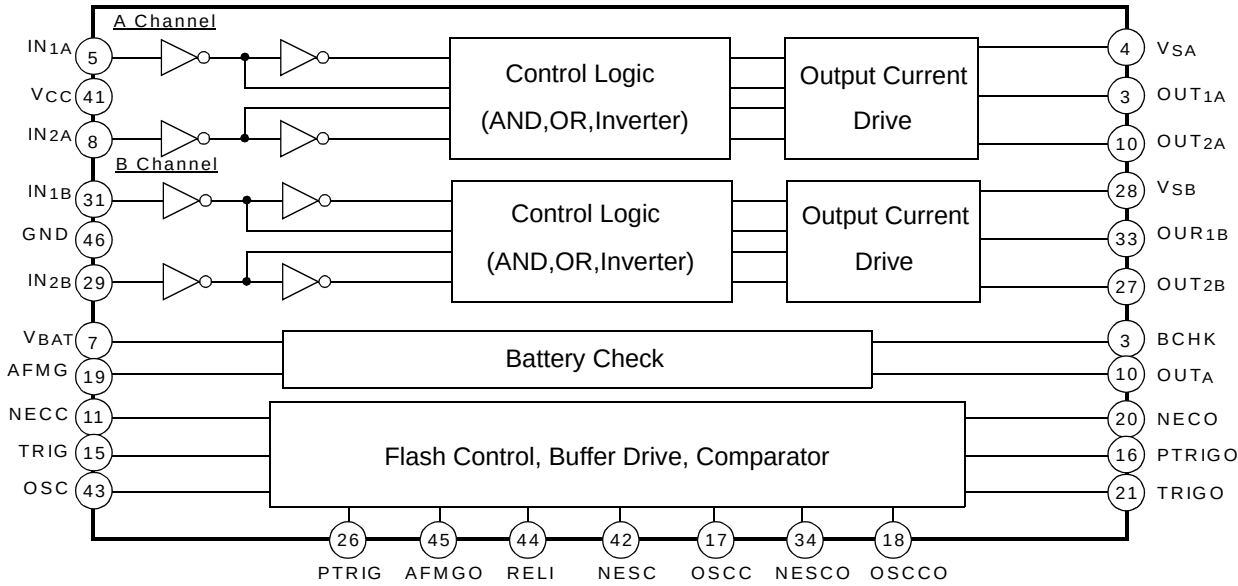
Pin Definitions

Pin Number	Pin Name	I/O	Pin Function Description	Remark
1	PG1A	-	Power ground 1	Channel A
2	BCHK	O	Battery check output	-
3	OUT1A	O	Output 1	Channel A
4	VSA	-	Output supply voltage	Channel A
5	IN1A	I	Input 1	Channel A
6	NC	-	No connection	-
7	VBAT	-	Power supply voltage	Channel A
8	IN2A	I	Input 2	Channel A
9	VSA	-	Output supply voltage	Channel A
10	OUT2A	O	Output 2	-
11	NECC	I	Flash charge control input	-
12	PG2A	-	Power ground 2	-
13	NC	-	No connection	-
14	NC	-	No connection	-
15	TRIG	I	Flash trigger input	-
16	PTRIGO	O	Pretrigger output	-
17	OSCC	I	Flash charge control input	-
18	OSCCO	O	Flash charge control output	-
19	AFMG	I	Auto focus magnetic control input	-
20	NECO	I	Flash charge control input	-
21	TRIGO	O	Flash trigger output	-
22	NC	-	No connection	-
23	NC	-	No connection	-
24	NC	-	No connection	-
25	PG2B	-	Power ground 2	Channel B
26	PTRIG	I	Flash control input	-
27	OUT2B	O	Output 2	Channel B
28	VSB	-	Output supply voltage	Channel B
29	IN2B	I	Input 2	Channel B
30	GND	-	Signal ground	-
31	IN1B	I	Input 2	Channel B
32	VSB	-	Output supply voltage	Channel B

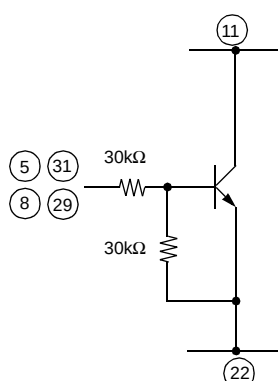
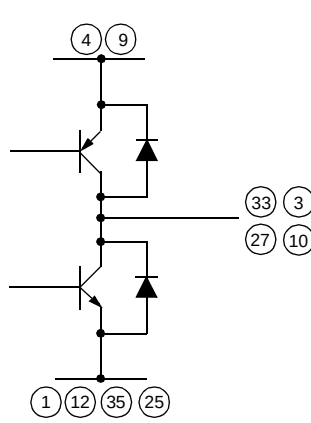
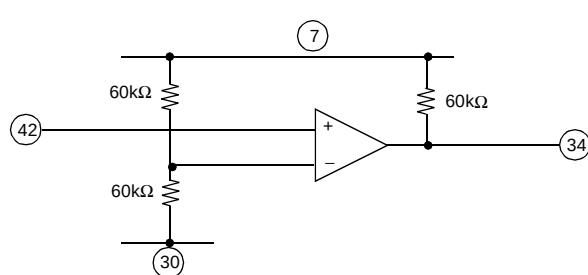
Pin Definitions (Continued)

Pin Number	Pin Name	I/O	Pin Function Description	Remark
33	OUT _{1B}	O	Output 1	Channel B
34	NESCO	I	Battery check output	-
35	PG _{1B}	-	Power supply voltage ground	Channel B
36	NC	-	No connection	-
37	NC	-	No connection	-
38	NC	-	No connection	-
39	NC	-	No connection	-
40	OUT _A	O	Battery check TR driving signal	-
41	VCC	-	Regulator output voltage	Effective RELI=Low Channel
42	NESC	I	Battery check input	-
43	OSC	I	Oscillator input	-
44	RELI	I	Battery check input	-
45	AFMGO	O	Auto focus magnetic control output	-
46	GND	-	AFMG control TR ground	-
47	NC	-	No connection	-
48	NC	-	No connection	-

Internal Block Diagram



Equivalent Circuits

Description	Pin No.	Internal Circuit
DC Motor Control Input		
IN _{1A}	5	
IN _{2A}	8	
IN _{1B}	31	
IN _{2B}	29	
V _{CC}	41	
GND	30	
DC Motor Output		
OUT _{1A}	3	
OUT _{2A}	10	
OUT _{1B}	33	
OUT _{2B}	27	
V _{SA}	4, 9	
V _{SB}	28, 32	
PG _{1A}	1	
PG _{2A}	12	
PG _{1B}	35	
PG _{2B}	25	
Comparator		
NESC	42	
NESCO	34	
V _{BAT}	7	
	30	

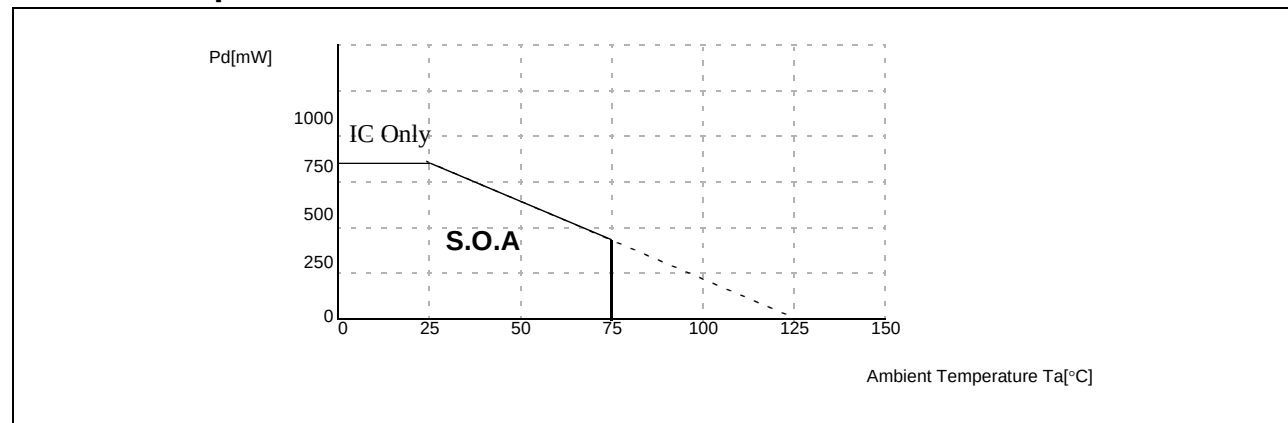
Equivalent Circuits (Continued)

Description	Pin No.	Internal Circuit
Autofocus Circuit		
AFMG	19	
AFMGO	45	
VBAT	7	
GND	46	
Discrete Circuit		
PTRIG	26	
TRIG	15	
NECC	11	
PTRIGO	16	
TRIGO	21	
NECO	20	
VBAT	7	
Regulator Circuit		
RELI	44	
VCC	41	
VBAT	7	
Flash Charge		
OSCC	17	
OSCCO	18	
GND	46	

Absolute Maximum Rating (Ta = 25°C)

Parameter	Symbol	Value	Unit
Power supply voltage	V _{BAT}	10	V
Channel supply voltage	V _S	10	V
Power dissipation	P _D	750	mW
Operating temperature	T _{OPR}	-25 ~ +75	°C
Storage temperature	T _{STG}	-40 ~ +125	°C
Output current	I _O	1	A

Power Dissipation Curve



Recommended Operating Conditions (Ta = 25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit
Operating supply voltage	V _{CC}	2.5	-	7.0	V

Electrical Characteristics

(VCC=5V, Ta=25°C, unless otherwise specified)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Units
Supply current 1	ICC1	V _{IN} (all) = 0V	-	0.1	10	μA
Supply current 2	ICC2	V _{IN1} =3V	-	15	30	mA
Supply current 3	ICC3	V _{IN2} =3V	-	15	30	mA
Supply current 4	ICC4	V _{IN} =3V	-	30	50	mA
Input current	I _{IN}	VCC=5V, V _{IN} =2V	-	45	80	μA
Leakage current	I _{IK}	VCC=7V	-	0.1	10	μA
Upper spark diode forward voltage 1	V _{SF1}	I _O =500mA	-	1.0	1.7	V
Lower spark diode forward voltage 2	V _{SF2}	I _O =500mA	-	1.0	1.7	V
Output saturation voltage 1A	V _{O1A}	I _O A=200mA	-	0.45	0.70	V
Output saturation voltage 1B	V _{O1B}	I _O B=200mA	-	0.45	0.70	V
Output saturation voltage 2A	V _{O2A}	I _O A=400mA	-	1.0	1.5	V
Output saturation voltage 2B	V _{O2B}	I _O B=400mA	-	1.0	1.5	V
Output saturation voltage 3A	V _{O3A}	I _O A=200mA	-	0.45	0.70	V
Output saturation voltage 3B	V _{O3B}	I _O B=200mA	-	0.45	0.70	V
Output saturation voltage 4A	V _{O4A}	I _O A=400mA	-	1.0	1.5	V
Output saturation voltage 4B	V _{O4B}	I _O B=400mA	-	1.0	1.5	V
Output saturation voltage 5	V _{O5}	I _O B=400mA	-	0.6	0.8	V
Output saturation voltage 6	V _{O6}	I _O =400mA	-	0.6	0.8	V
Output saturation voltage 7	V _{O7}	I _O =800mA	-	1.2	1.6	V
Output saturation voltage 8	V _{O8}	I _O =800mA	-	1.2	1.6	V
OUTA terminal output current	I _{OUTA}	V _{OUTA} =0.7A	6	15	24	mA
Battery check time	T _{CHECK}	R _{OSC} =72k, C _{OSC} =0.1μF	8	10	12	ms
Battery check reference voltage	V _{CHECK}	V _{BAT} =4~5V	4.4	4.5	4.6	V
Comparator reference voltage	V _{TH}	V _{nesc} =1~2V	1.15	1.25	1.35	V
RELI terminal input current	I _{RELI}	V _{RELI} =0V	-	5	7	mA
VCC terminal output saturation voltage	V _{VCC}	I _O =150mA	-	0.3	0.5	V
AFMG terminal input current	I _{AFMG}	V _{AFMG} =5V	-	145	190	μA
AFMGO output saturation voltage	V _{AFMGO}	I _O =100mA	-	0.3	0.5	V
PTRIG terminal input current	I _{PTRIG}	V _{PTRIG} =5V	-	145	190	μA
PTRIGO output saturation voltage	V _{PTRIGO}	I _O =10mA	-	0.3	0.5	V

Electrical Characteristics (Continued)

(VCC=5V, Ta=25°C, unless otherwise specified)

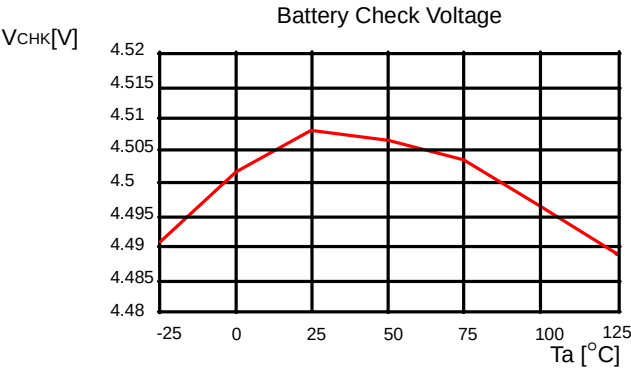
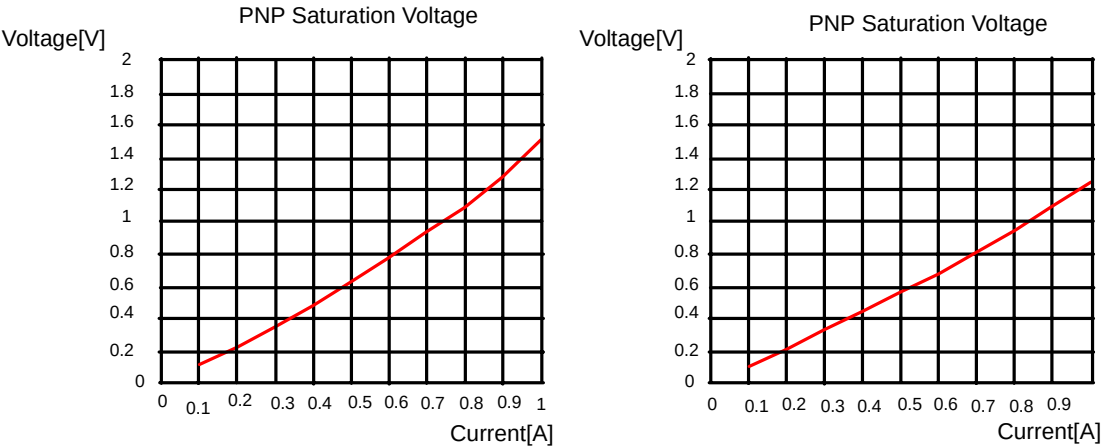
Parameter	Symbol	Condition	Min.	Typ.	Max.	Units
TRIG terminal input current	ITRIG	VTRIG=5V	-	145	190	μA
TRIGO terminal voltage	VTRIGO	VTRIG=5V	0.84	0.94	1.04	V
NECC terminal input current	INECC	VNECC=5V	-	145	190	mA
NECO terminal output current	INECO	VNECO=0V	1.0	1.15	1.3	V
OSCC terminal input current	IOSCC	VOSCC=5V	-	500	700	μA
OSCCO output saturation voltage	IOSCCO	VOSCCO=10mA	-	0.3	0.5	V

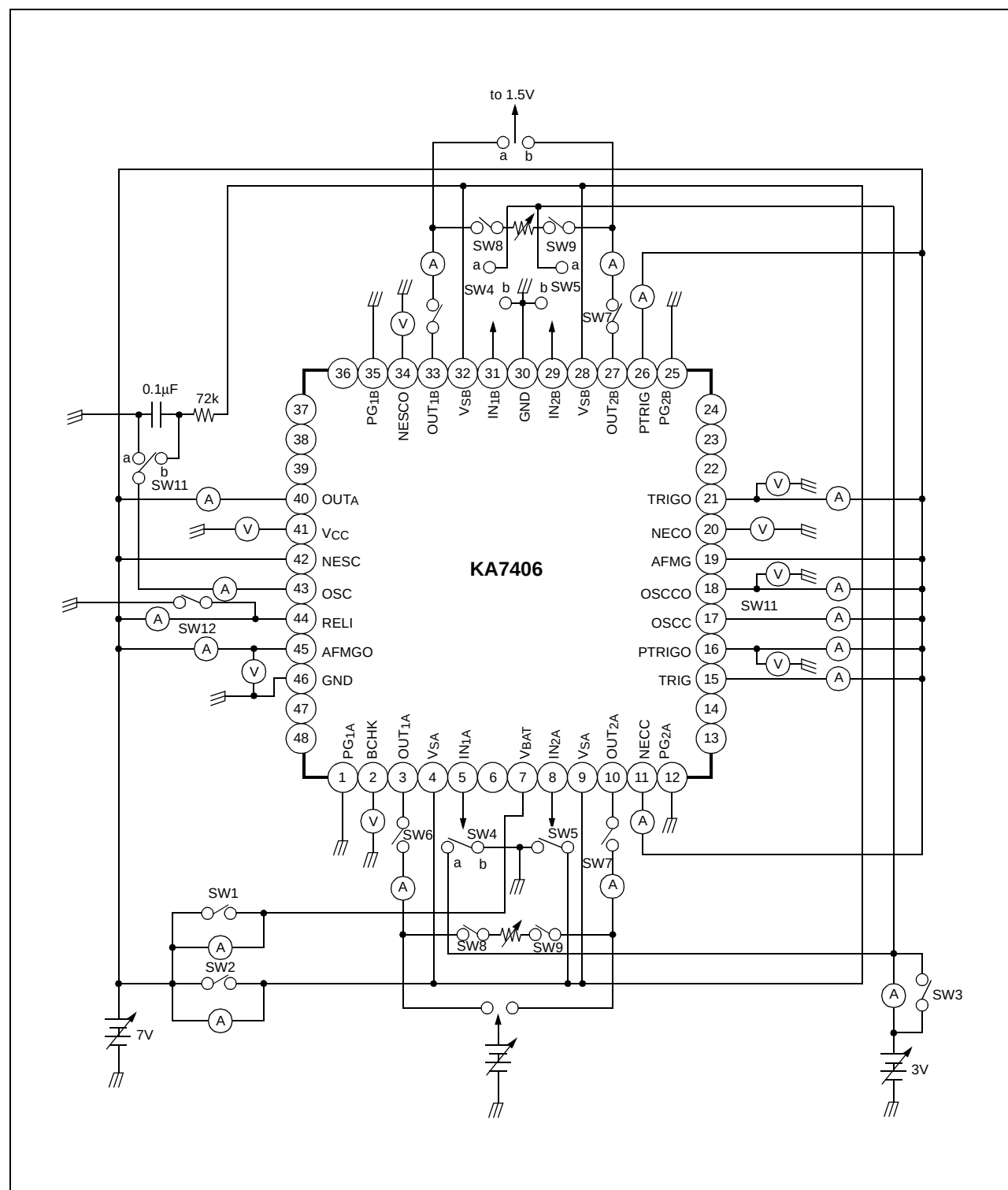
Motor Operation Truth Table

Input/Output Motor Operation	Input 1	Input 2	Output 1	Output 2	Remark
Stop	Low	Low	Off	Off	High impedance
Forward operation	Low	High	Low	High	CW / CCW
Backward operation	High	Low	High	Low	CCW / CW
Fast stop	High	High	Low	Low	Brake

Typical Performance Characteristics

((V_{CC}=5V, T_a=25°C, unless otherwise specified)

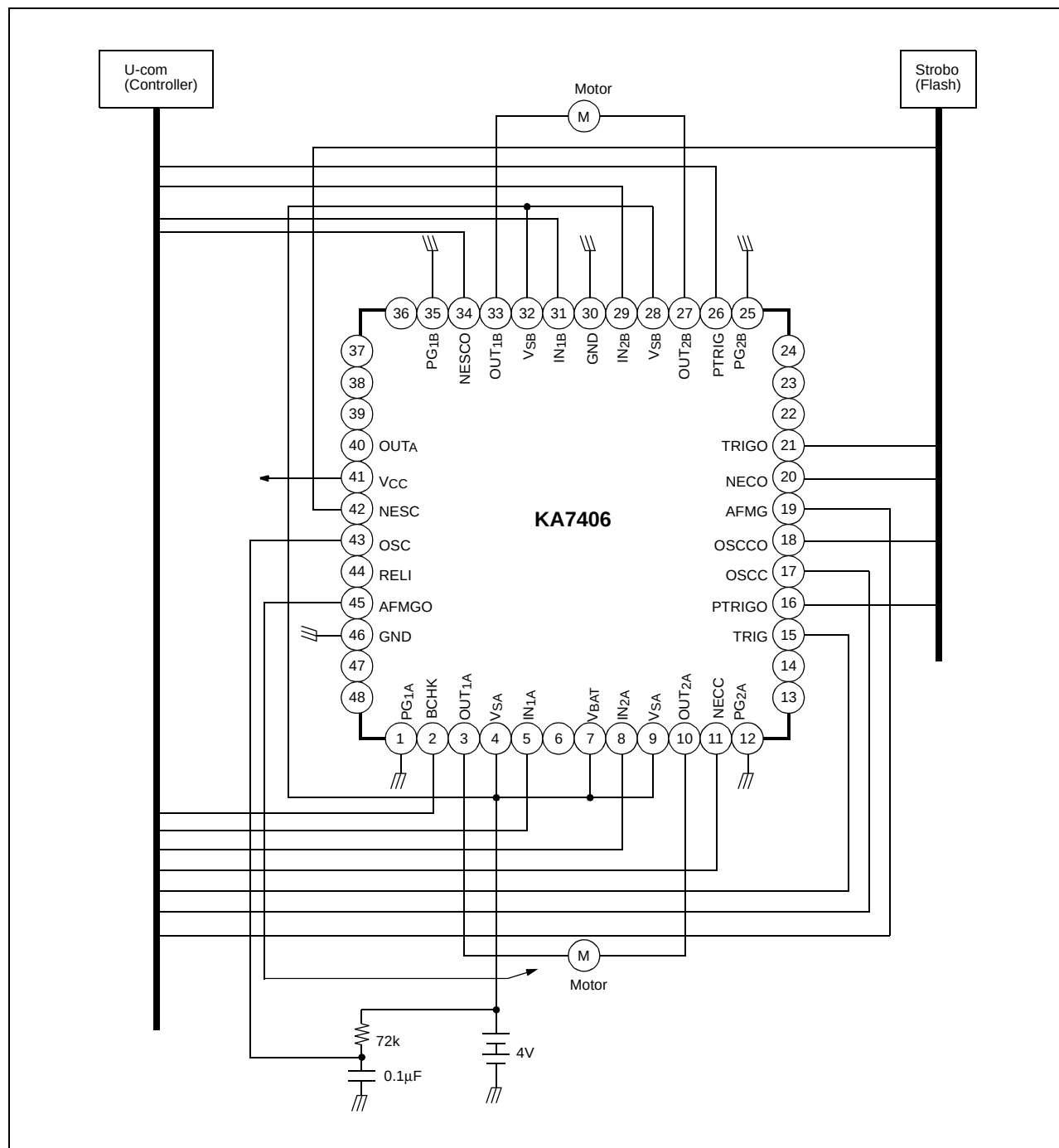


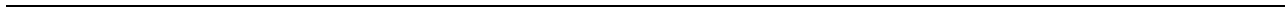


Test Conditions

Characteristics	SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8	SW9	SW10	Remark
I _{CC1}	Off	Off	X	b	b	Off	Off	X	X	Off	Supply current
I _{CC2}	Off	Off	On	a	b	Off	Off	X	X	Off	Supply current
I _{CC3}	Off	Off	On	b	a	Off	Off	X	X	Off	Supply current
I _{CC4}	Off	Off	On	a	a	Off	Off	X	X	Off	Supply current
I _{IN}	Off	Off	Off	a	a	Off	Off	X	X	Off	Input current
I _{LK}	Off	Off	X	b	b	Off	Off	X	X	Off	Leakage current
V _{SF1}	On	On	On	a	b	On	On	Off	Off	a	Spark diode
V _{SF2}	On	On	On	b	a	On	On	Off	Off	b	Spark diode
V _{O1A}	On	On	On	a	b	On	On	On	On	Off	Single mode
V _{O2A}	On	On	On	b	a	On	On	On	On	Off	Single mode
V _{O3A}	On	On	On	a	b	On	On	On	On	Off	Single mode
V _{O4A}	On	On	On	b	a	On	On	On	On	Off	Single mode
V _{O5}	On	On	On	a	b	On	On	On	On	Off	Parallel mode
V _{O6}	On	On	On	b	a	On	On	On	On	Off	Parallel mode
V _{O7}	On	On	On	a	b	On	On	On	On	Off	Parallel mode
V _{O8}	On	On	On	b	a	On	On	On	On	Off	Parallel mode
V _{SUS}	Off	Off	X	b	b	On	On	On	On	Off	Sustain voltage
Characteristics	SW1	SW2	SW11	SW12	-	-	-	-	-	-	Remark
I _{OUT}	On	On	a	On	-	-	-	-	-	-	-
T _{CHK}	On	On	b	Off	-	-	-	-	-	-	-
V _{CHK}	On	On	X	X	-	-	-	-	-	-	-
V _{TH}	On	On	X	X	-	-	-	-	-	-	-
I _{REL1}	On	On	X	X	-	-	-	-	-	-	-
V _{VCC}	On	On	X	X	-	-	-	-	-	-	-
I _{AFMGO}	On	On	X	X	-	-	-	-	-	-	-
V _{AFMGO}	On	On	X	X	-	-	-	-	-	-	-
I _{PTRG}	On	On	X	X	-	-	-	-	-	-	-
V _{PTRGO}	On	On	X	X	-	-	-	-	-	-	-
I _{TRIG}	On	On	X	X	-	-	-	-	-	-	-
V _{TRIGO}	On	On	X	X	-	-	-	-	-	-	-
I _{NECC}	On	On	X	X	-	-	-	-	-	-	-
I _{NECO}	On	On	X	X	-	-	-	-	-	-	-
I _{OSCC}	On	On	X	X	-	-	-	-	-	-	-
V _{OSCCO}	On	On	X	X	-	-	-	-	-	-	-

Application Circuits





TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACE ^x ™	FAST ^r ™	QFET™	VCX™
Bottomless™	GlobalOptoisolator™	QS™	
CoolFET™	GTO™	QT Optoelectronics™	
CROSSVOLT™	HiSeC™	Quiet Series™	
DOVE™	ISOPLANAR™	SuperSOT™-3	
E ² CMOS™	MICROWIRE™	SuperSOT™-6	
EnSigna™	OPTOLOGIC™	SuperSOT™-8	
FACT™	OPTOPLANAR™	SyncFET™	
FACT Quiet Series™	POP™	TinyLogic™	
FAST®	PowerTrench®	UHC™	

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.