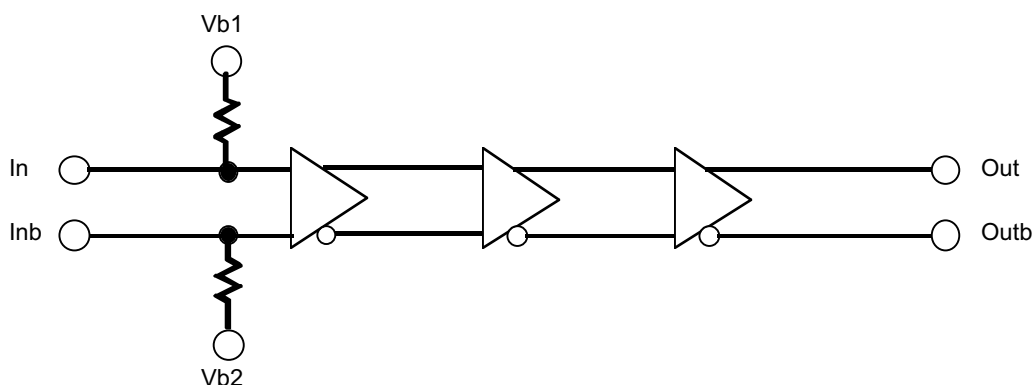


**KGA4117****Preliminary****15 GHz Ultra-Broadband Amplifier****DESCRIPTION**

KGA4117 is an ultra-broadband constant gain amplifier implemented 0.1  $\mu\text{m}$  gate GaAs P-HEMT device technology.

**FEATURES**

- Broadband Amplifier: to 15 GHz (Typ.)
- Single Supply Voltage:  $-5\text{ V}$

**BLOCK DIAGRAM****ABSOLUTE MAXIMUM RATINGS**

| Items               | Symbol   | Min.  | Max. | Unit               |
|---------------------|----------|-------|------|--------------------|
| Supply Voltage      | $V_{SS}$ | $-7$  | 0.3  | V                  |
| Input Amplitude     | $V_{in}$ |       | 1.6  | $V_{PP}$           |
| Storage Temperature | $T_{ST}$ | $-45$ | 125  | $^{\circ}\text{C}$ |

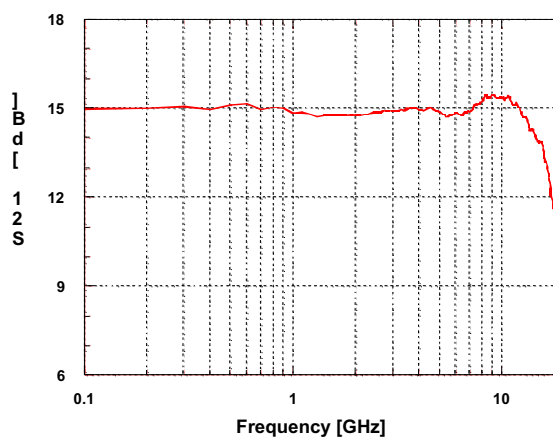
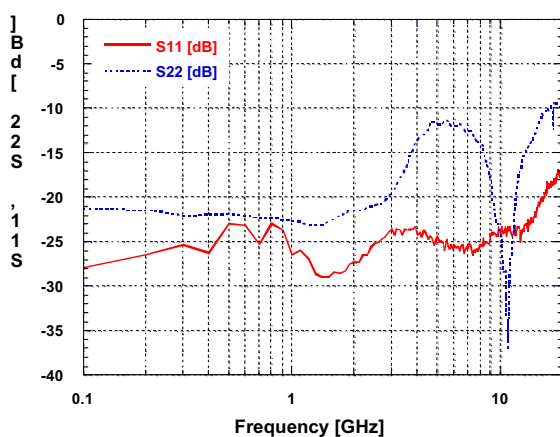
## ELECTRICAL CHARACTERISTICS

| Parameter                    | Symbol          | Min.  | Typ. | Max.  | Unit     |
|------------------------------|-----------------|-------|------|-------|----------|
| Supply Voltage               | $V_{SS}$        | -5.25 | -5   | -4.75 | V        |
| Supply Current               | $I_{SS}$        |       | 100  |       | mA       |
| Operating Temperature        | $T_a$           | 0     |      | 80    | °C       |
| Bandwidth (-3 dB)            | $F_c$           | 12    | 15   |       | GHz      |
| Gain                         | $G_{max}$       | 12    | 15   |       | dB       |
| Input Amplitude              | $V_{in}$        |       |      | 1.0   | $V_{PP}$ |
| Output Saturation Amplitude  | $\Delta V_{op}$ | 0.8   | 1.0  |       | $V_{PP}$ |
| Input Return Loss (<10 GHz)  | $S_{11}$        | 15    |      |       | dB       |
| Output Return Loss (<10 GHz) | $S_{22}$        | 10    |      |       | dB       |

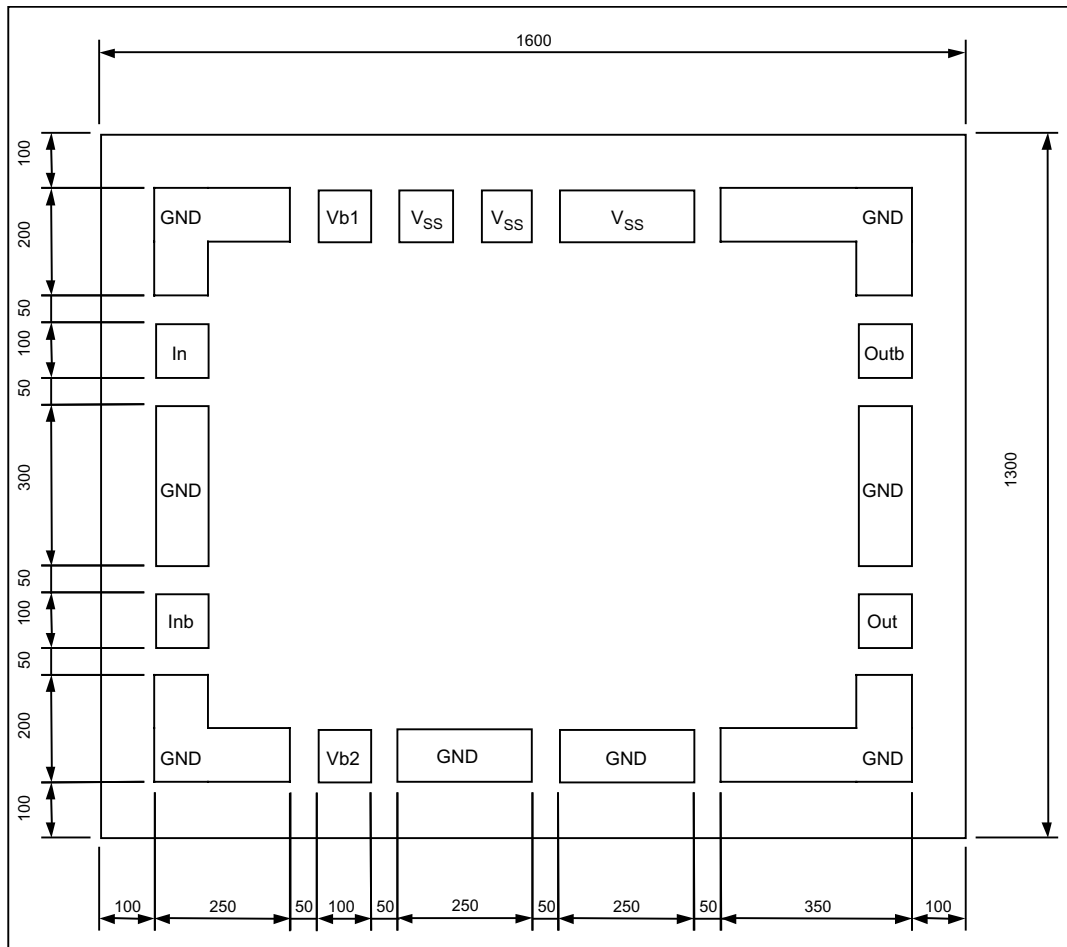
Input/Output Interface

50 $\Omega$  AC Coupling (External blocking capacitor is required)

## TYPICAL CHARACTERISTICS



Gain : 15 dB  
Bandwidth : 17 GHz

**PAD ARRANGEMENT**

In : Signal input port

Inb : Signal input-bar port

Out : Signal output port

Outb : Signal output-bar port

Vb1 : Input termination port (External capacitor is required) and input bias control port

Vb2 : Input-bar termination port (External capacitor is required) and input-bar bias control port

Vss : Supply voltage port

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1. The information contained herein can change without notice owing to product and/or technical improvements. Before using the product, please make sure that the information being referred to is up-to-date.
2. The outline of action and examples for application circuits described herein have been chosen as an explanation for the standard action and performance of the product. When planning to use the product, please ensure that the external conditions are reflected in the actual circuit, assembly, and program designs.
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