

VERTICAL DEFLECTION OUTPUT CIRCUIT

The KA2142A is a monolithic linear IC designed for color TV and monitor vertical deflection output. It is intended for direct drive of the deflection coil with a high efficiency.

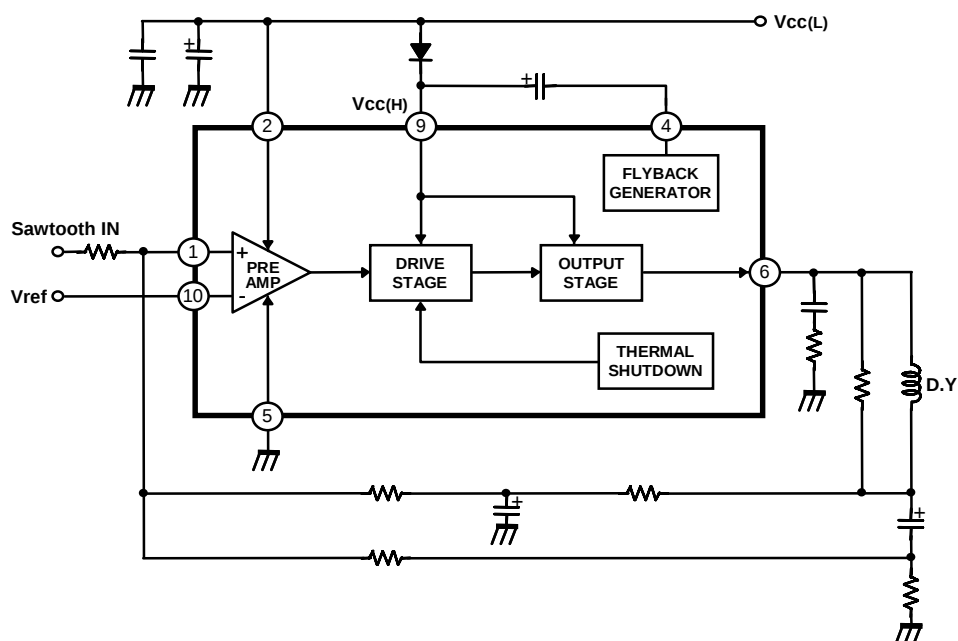
FEATURES

- High output current
- Pump - up circuit
- Low Power dissipation
- Minimum number of external parts required
- Direct drive to the deflection coil
- Internal thermal shutdown circuit

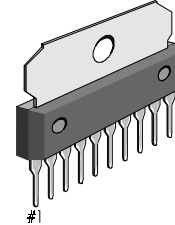
FUNCTIONS

- Power Amplifier
- Thermal Protection
- Flyback Generator

BLOCK DIAGRAM



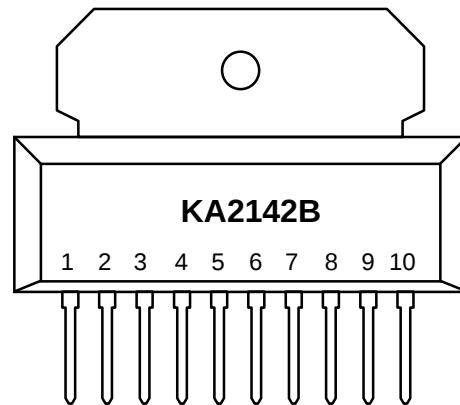
10-SIP-HS



ORDERING INFORMATION

Device	Package	Operating Temperature
KA2142B	10-SIP-HS	-25 ~ + 70°C

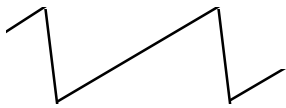
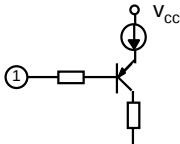
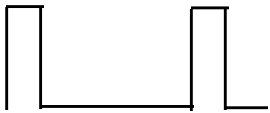
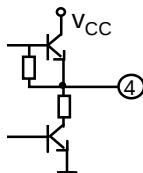
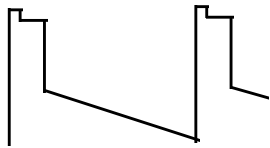
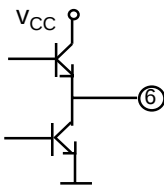
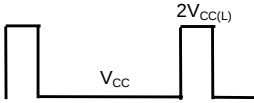
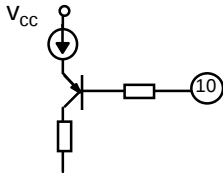
PIN CONFIGURATION



PIN DEFINITIONS

Pin No.	Symbol	I/O	Description
1	$V_{IN(-)}$	I	Inverting Input
2	$V_{CC(L)}$	I	Supply Voltage
3	—	-	N.C.
4	F.G	O	Flyback Generator
5	GND	-	Ground
6	V_O	O	Output
7	—	-	N.C.
8	—	-	N.C.
9	$V_{CC(H)}$	I	Output Stage Supply Voltage
10	$V_{IN(+)}$	I	Non-Inverting Input

PIN DESCRIPTION

Pin Nunber	Function	Waveform	Equivalent Circuit
1	Inverting Input		
2	Voltage Supply	DC	-
4	Flyback Generator		
5	Ground	DC	-
6	Output Voltage		
9	Output Stage Voltage Supply		
10	Non-Inverting Input	DC	

ABSOLUTE MAXIMUM RATING ($T_A = 25^\circ\text{C}$)

Characteristic	Symbol	Value	Unit
Supply Voltage	$V_{CC(L)}$	35	V
Flyback Peak Voltage	V6, V9	70	V
Flyback Generator Voltage	V4	35	V
Input Voltage	V1, V10	$V_{CC(L)}, -0.5$	V
Peak-to-Peak Output Current*	$I_O(p-p)$	3	A
Peak-to-Peak Flyback Current ($f = 50$ or 60Hz , $T_{fb} \leq 1.5\text{ms}$)	$I_4(p-p)$	3	A
Total Power Dissipation ($T_a = 25^\circ\text{C}$)	P_D	15	W
Storage Temperature Range	T_{STG}	$-40 \sim +150$	$^\circ\text{C}$
Operating Ambient Temperature	T_{OPT}	$-25 \sim +70$	$^\circ\text{C}$

NOTE: Maximum output peak to peak current in TV or Monitor set.

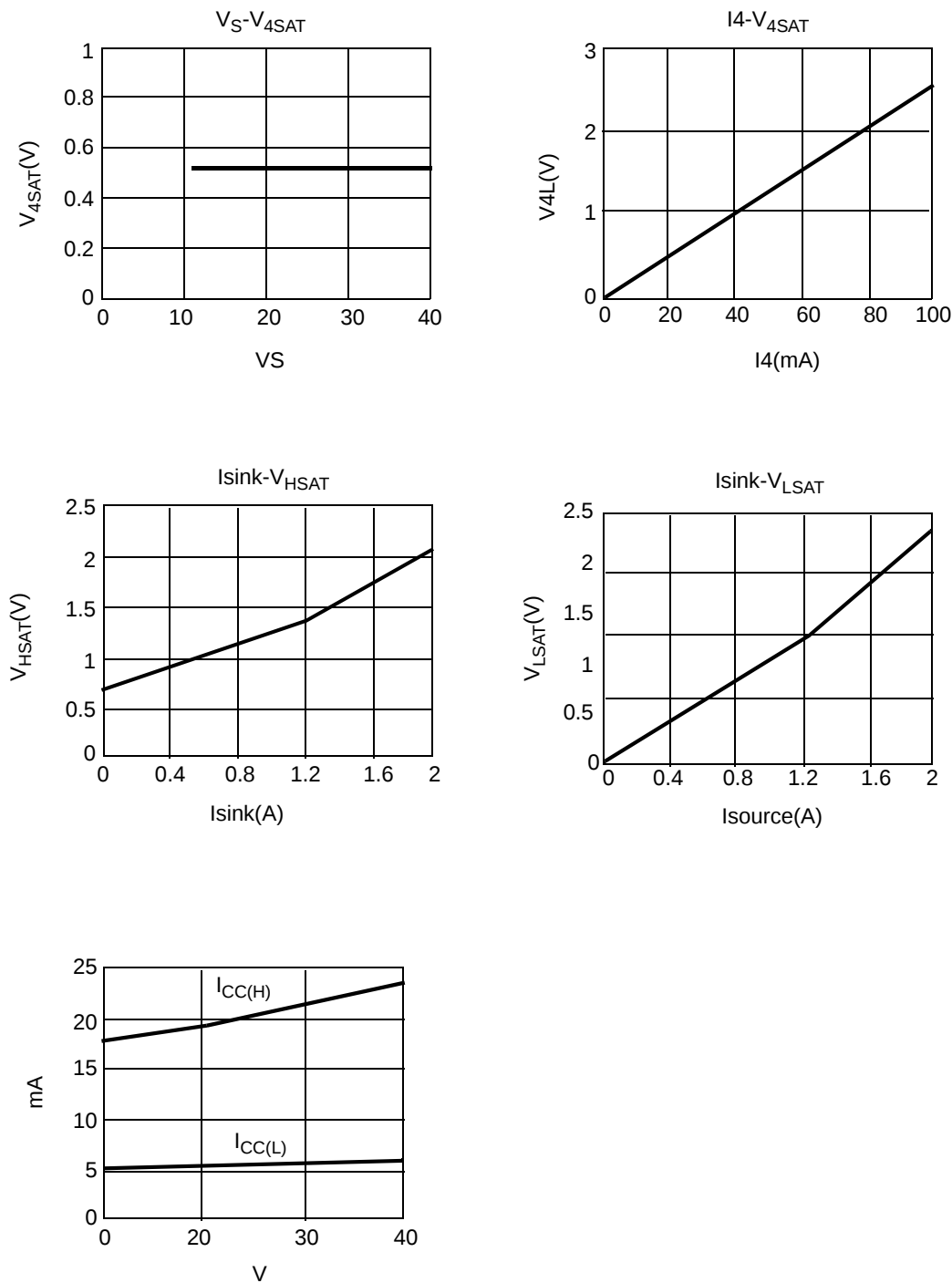
THERMAL DATA

Characteristic	Symbol	Value	Unit
Thermal Resistance Between Junction and Case	$R_{th(j-c)}$	12	$^\circ\text{C/W}$
Thermal Resistance Between Junction and Ambient	$R_{th(j-a)}$	60	$^\circ\text{C/W}$
Thermal Shut-down Temperature	T_{TSD}	150	$^\circ\text{C}$

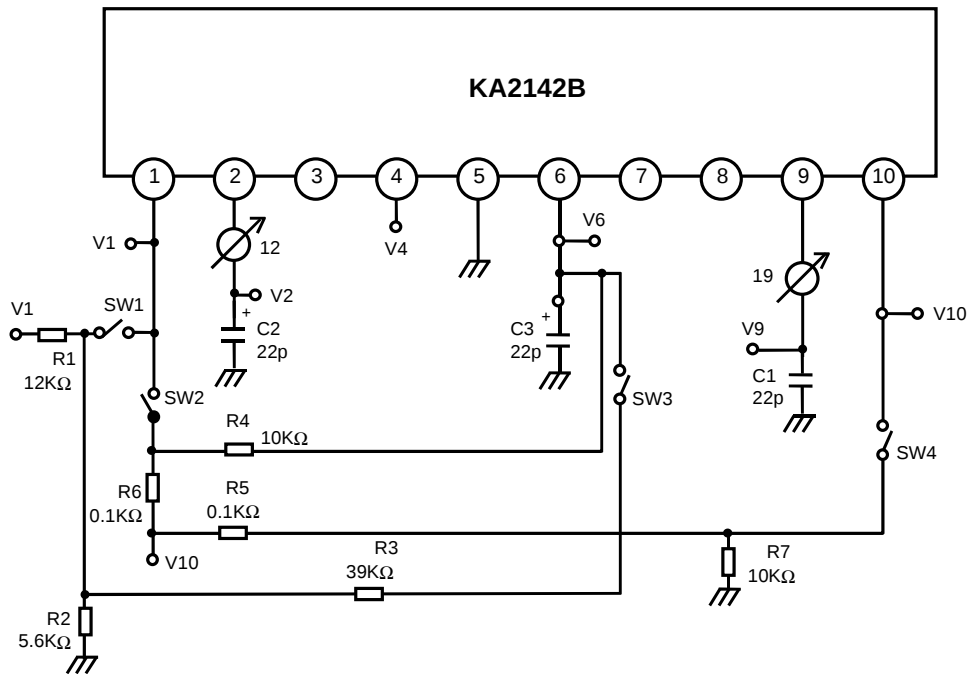
ELECTRICAL CHARACTERISTIC(Refer to the test circuit, $V_{CC}(L) = 35V$, unless otherwise specified)

Characteristic	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Supply Voltage	$V_{CC(L)}$	—	15	25	35	V
	$V_{CC(H)}$		15	—	70	V
Supply Quiescent Current	$I_{CC(L)}$	—	—	6	16	mA
	$I_{CC(H)}$		—	22	36	mA
Pin4 Saturation Voltage to Gnd	V_{4SAT}	$I_4 = 20mA$	—	.5	1	V
Output Saturation Voltage to supply	V_{HSAT}	$I_6 = -1.2A$	—	1.6	2.2	V
		$I_6 = -0.7A$	—	1.3	1.8	V
Output Saturation Voltage to ground	V_{LSAT}	$I_6 = 1.2A$	—	1	1.4	V
		$I_6 = 0.7A$	—	0.7	1	V
Output Center Voltage	V_{MID}	$R_1 = 5.6K$, $R_{fb} = 45K$ $V_1 = V_{10} = 2V$	—	18	—	V
Input Bias Current	I_{BIAS}	$V_1 = 1V$, $V_{10} = 2V$	—	-0.1	-1	μA

TYPICAL PERFORMANCE CHARACTERISTIC

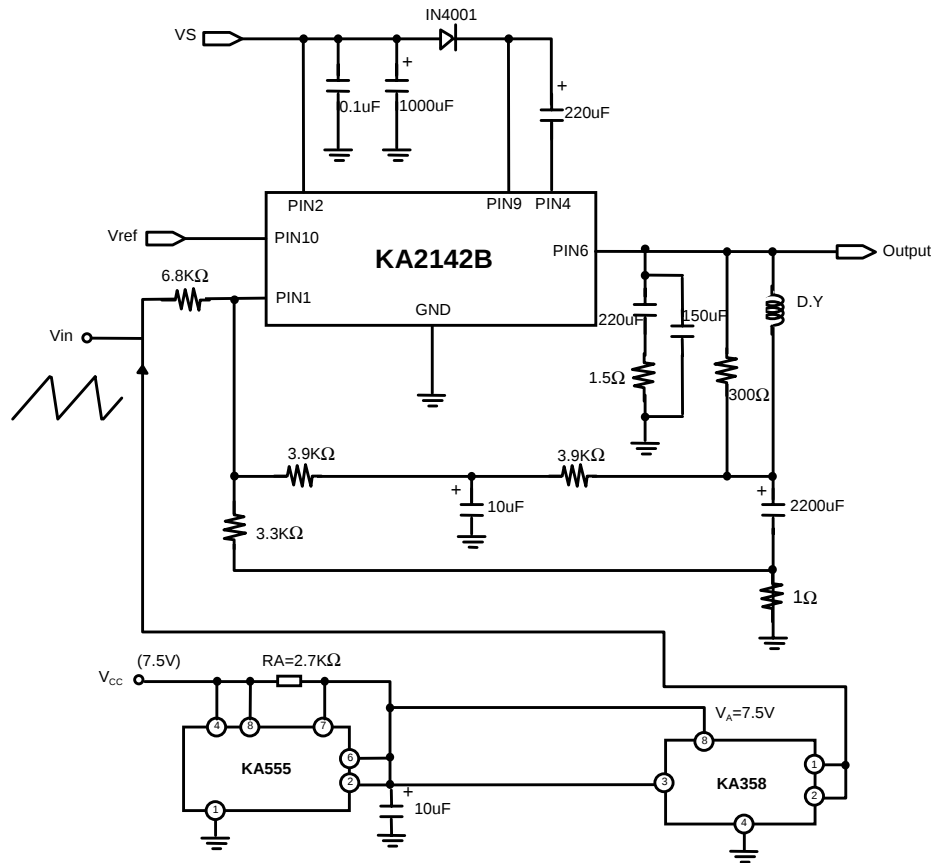


DC TEST CIRCUIT

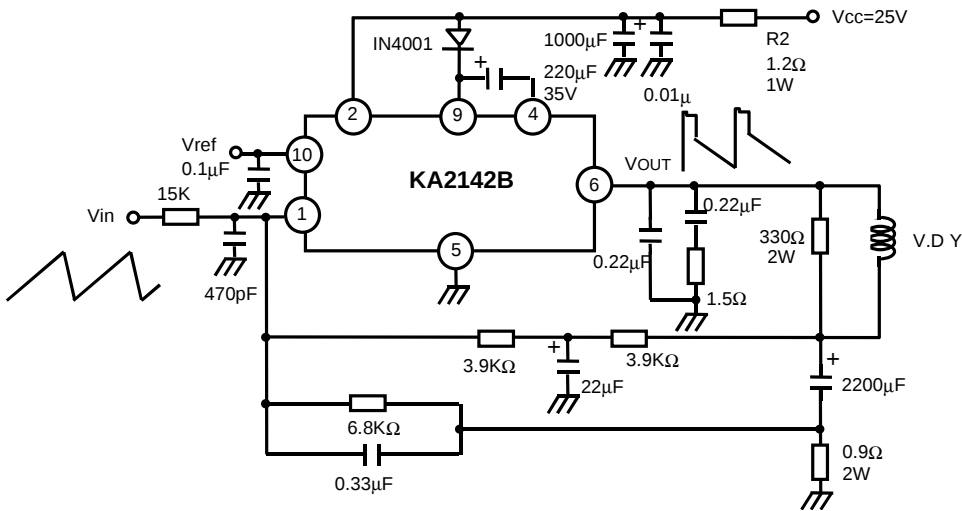


ITEM	Input Voltage (V)				Switch State			
	V1	V10	Vin1	Vin2	SW1	SW2	SW3	SW4
I _{CC(L)} , I _{CC(H)}	—	—	—	2	OFF	ON	OFF	ON
I _{BIAS}	1	2	—	—	OFF	OFF	OFF	OFF
V _{4SAT}	3	2	—	—	OFF	OFF	OFF	OFF
V _{LSAT}	3	2	—	—	OFF	OFF	OFF	OFF
V _{HSAT}	1	2	—	—	OFF	OFF	OFF	OFF

AC TEST CIRCUIT



TYPICAL APPLICATION CIRCUIT



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2. A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.