KA5M0765RQC

Fairchild Power Switch(FPS)

Features

- Precision fixed operating frequency (70KHz)
- Low start-up current (Typ. 100µA)
- Pulse by pulse current limiting
- Over load protection
- Over current protection
- Over voltage protection (Min. 25V)
- Internal thermal shutdown function
- Under voltage lockout
- Internal high voltage sense FET
- Auto-restart mode

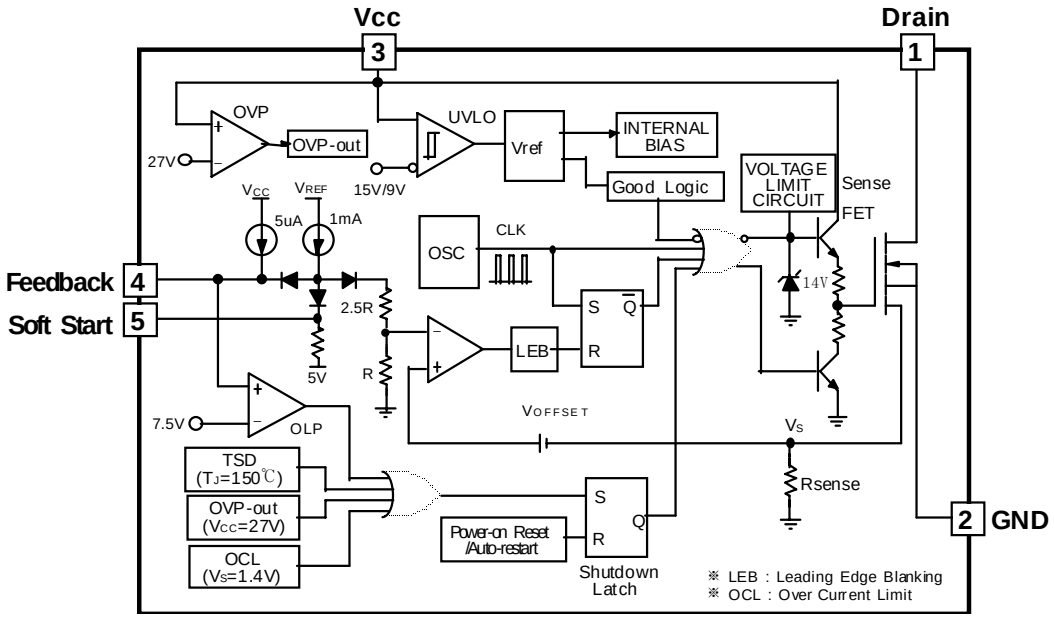
Description

The Fairchild Power Switch(FPS) product family is specially designed for an off line SMPS with minimal external components. The Fairchild Power Switch(FPS) consist of high voltage power SenseFET and current mode PWM IC. Included PWM controller features integrated fixed frequency oscillator, under voltage lock-out, leading edge blanking, optimized gate turn-on/turn-off driver, thermal shutdown protection, over voltage protection, and temperature compensated precision current sources for loop compensation and fault protection circuitry. compared to discrete MOSFET and PWM controller or R_{CC} solution, a Fairchild Power Switch(FPS) can reduce total component count, design size, weight and at the same time increase efficiency, productivity, and system reliability.
It has a basic platform well suited for cost-effective design in either a flyback converter or a forward converter.

TO-220-5L

1. DRAIN 2. GND 3. V_{CC} 4. FB 5. S/S

Internal Block Diagram



Rev.1.0.2

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Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Maximum Drain voltage (1)	$V_{D,MAX}$	650	V
Drain-Gate voltage (R_{GS}=1MΩ)	V_{DGR}	650	V
Gate-source (GND) voltage	V_{GS}	±30	V
Drain current pulsed (2)	I_{DM}	28.0	A_{DC}
Single pulsed avalanche energy (3)	E_{AS}	570	mJ
Continuous drain current (T_C=25°C)	I_D	7.0	A_{DC}
Continuous drain current (T_C=100°C)	I_D	5.6	A_{DC}
Maximum Supply voltage	$V_{CC,MAX}$	30	V
Input voltage range	V_{FB}	–0.3 to V_{SD}	V
Total power dissipation	P_D	140	W
	Derating	1.11	W/°C
Operating ambient temperature	T_A	–25 to +85	°C
Storage temperature	T_{STG}	–55 to +150	°C

Note:

1. Tj=25°C to 150°C
2. Repetitive rating: Pulse width limited by maximum junction temperature
3. L=24mH, starting Tj=25°C

Electrical Characteristics (SFET part)

(Ta=25°C unless otherwise specified)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Drain-source breakdown voltage	BV_{DSS}	V_{GS}=0V, I_D=50μA	650	-	-	V
Zero gate voltage drain current	I_{DSS}	V_{DS}=Max., Rating, V_{GS}=0V	-	-	50	μA
		V_{DS}=0.8Max., Rating, V_{GS}=0V, T_C=125°C	-	-	200	μA
Static drain-source on resistance (note)	$R_{DS(ON)}$	V_{GS}=10V, I_D=3.5A	-	1.25	1.6	Ω
Forward transconductance (note)	gfs	V_{DS}=50V, I_D=3.5A	3.0	-	-	S
Input capacitance	Ciss	V_{GS}=0V, V_{DS}=25V, f=1MHz	-	1120	-	pF
Output capacitance	Coss		-	125	-	
Reverse transfer capacitance	Crss		-	25	-	
Turn on delay time	td(on)	V_{DD}=0.5BV_{DSS}, I_D=7.0A (MOSFET switching time are essentially independent of operating temperature)	-	25	60	nS
Rise time	tr		-	70	150	
Turn off delay time	td(off)		-	105	220	
Fall time	tf		-	65	140	
Total gate charge (gate-source+gate-drain)	Qg	V_{GS}=10V, I_D=7.0A, V_{DS}=0.8BV_{DSS}	-	38	50	nC
Gate-source charge	Qgs		-	6.5	-	
Gate-drain (Miller) charge	Qgd		-	18	-	

Note:

Pulse test: Pulse width ≤ 300μS, duty cycle ≤ 2%

$S = \frac{1}{R}$

Electrical Characteristics (Control part)

(Ta=25°C unless otherwise specified)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
UVLO SECTION						
Start threshold voltage	V_{START}	-	8.4	9	9.6	V
Stop threshold voltage	V_{STOP}	After turn on	14	15	16	V
OSCILLATOR SECTION						
Initial accuracy	F_{OSC}	Ta=25°C	61	67	73	kHz
Frequency change with temperature (2)	$\Delta F/\Delta T$	–25°C ≤ Ta ≤ +85°C	-	±5	±10	%
Maximum duty cycle	Dmax	-	74	77	80	%
FEEDBACK SECTION						
Feedback source current	I_{FB}	Ta=25°C, 0V ≤ Vfb ≤ 3V	0.7	0.9	1.1	mA
Shutdown Feedback voltage	V_{SD}	Vfb ≥ 6.5V	6.9	7.5	8.1	V
Shutdown delay current	Idelay	Ta=25°C, 5V ≤ Vfb ≤ V_{SD}	4	5	6	µA
SOFT START SECTION						
Soft Start Voltage	V_{SS}	V_{FB} =2V	4.7	5.0	5.3	V
Soft Start Current	I_{SS}	Sync & S/S=GND	0.8	1.0	1.2	mA
REFERENCE SECTION						
Output voltage (1)	Vref	Ta=25°C	4.80	5.00	5.20	V
Temperature Stability (1)(2)	Vref/ΔT	–25°C ≤ Ta ≤ +85°C	-	0.3	0.6	mV/°C
CURRENT LIMIT (SELF-PROTECTION) SECTION						
Peak Current Limit	I_{OVER}	Max. inductor current	4.40	5.00	5.60	A
PROTECTION SECTION						
Thermal shutdown temperature (Tj) (1)	T_{SD}	-	140	160	-	°C
TOTAL DEVICE SECTION						
Start Up current	I_{START}	V_{CC}=14V	-	0.1	0.17	mA
Operating supply current (control part only)	I_{OP}	V_{CC}≤28	-	7	12	mA

Note:

1. These parameters, although guaranteed, are not 100% tested in production
2. These parameters, although guaranteed, are tested in EDS (wafer test) process

Typical Performance Characteristics (SFET part)

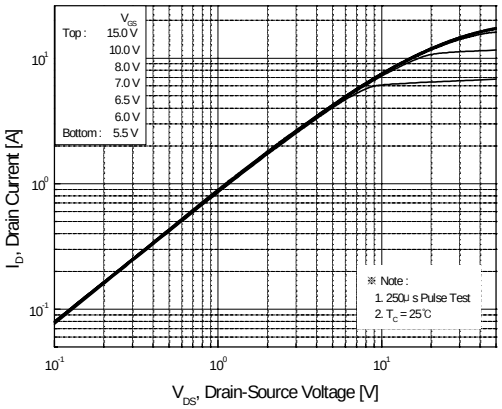


Figure 1. Output Characteristics

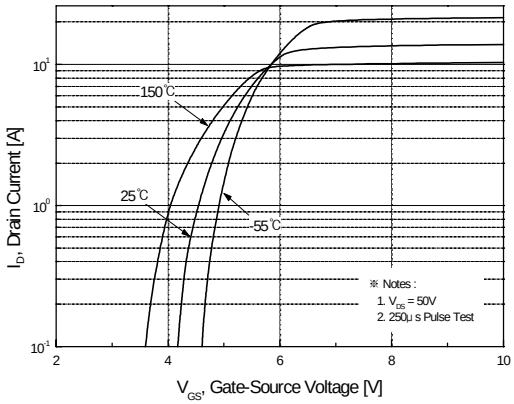


Figure 2. Transfer Characteristics

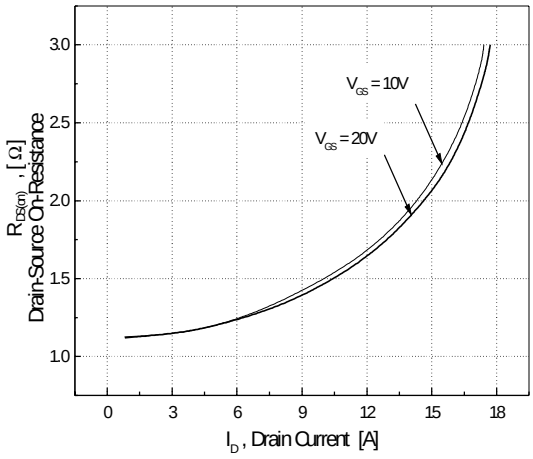


Figure 3. On-Resistance vs. Drain Current

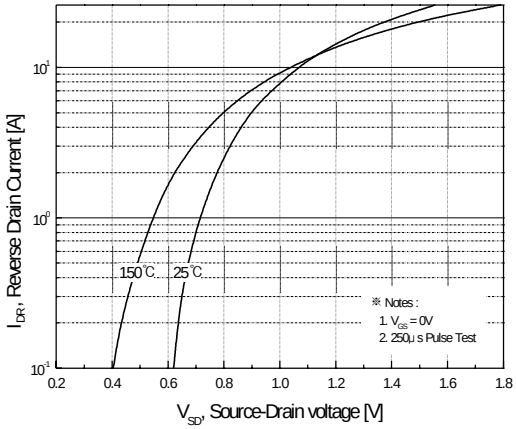


Figure 4. Source-Drain Diode Forward Voltage

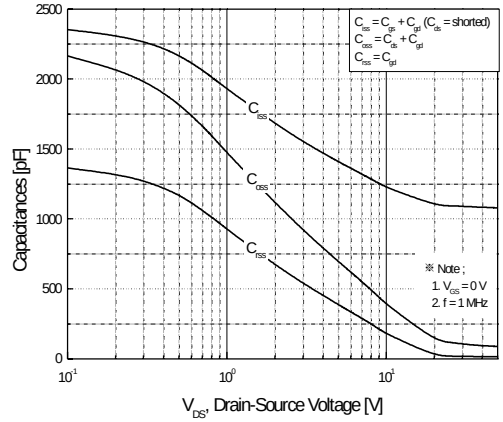


Figure 5. Capacitance vs. Drain-Source Voltage

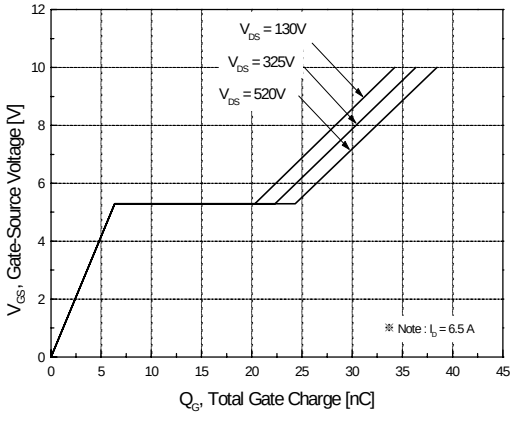


Figure 6. Gate Charge vs. Gate-Source Voltage

Typical Performance Characteristics (Continued)

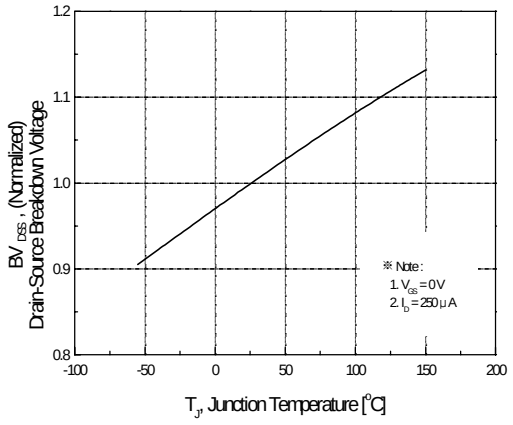


Figure 7. Breakdown Voltage vs. Temperature

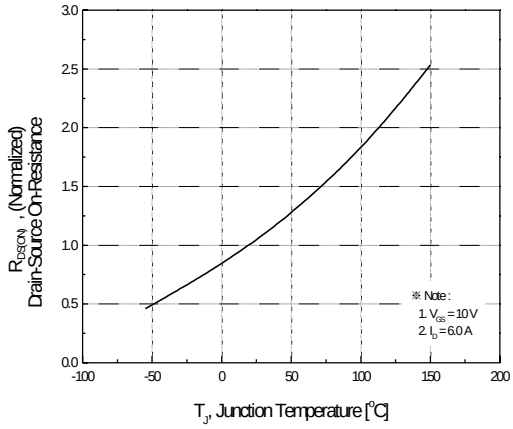


Figure 8. On-Resistance vs. Temperature

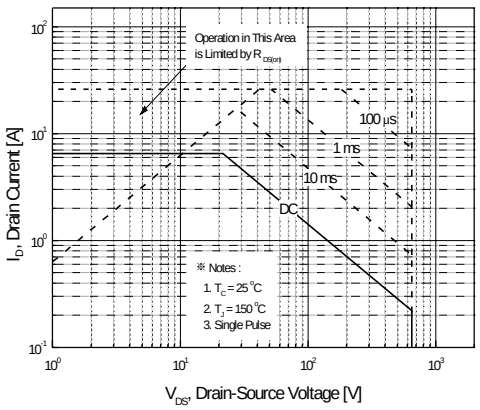


Figure 9. Max. Safe Operating Area

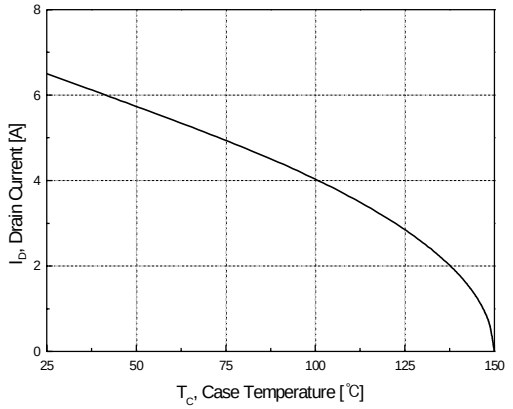


Figure 10. Max. Drain Current vs. Case Temperature

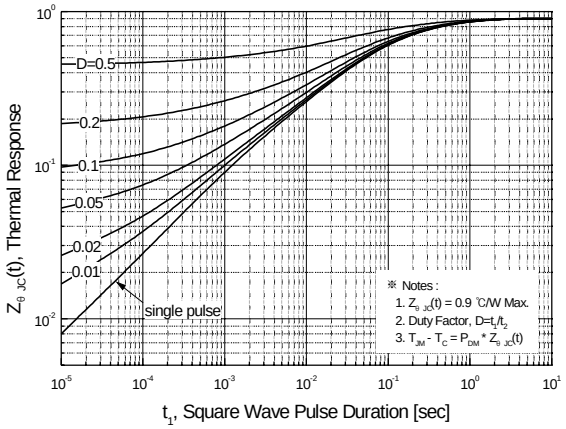


Figure 11. Thermal Response

Typical Performance Characteristics (Control part)

(These characteristic graphs are normalized at Ta=25°C)

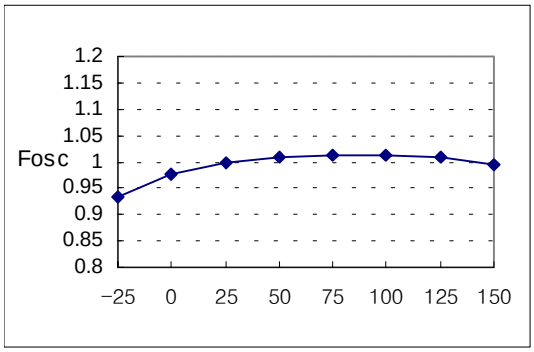


Figure 1. Operating Frequency

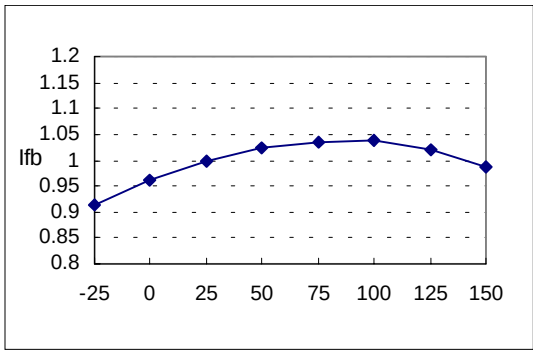


Figure 2. Feedback Sourece Current

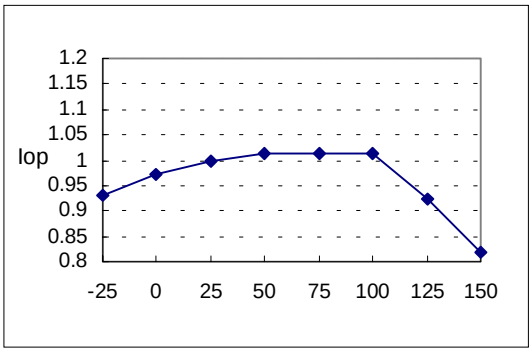


Figure 3. Operating Supply Current

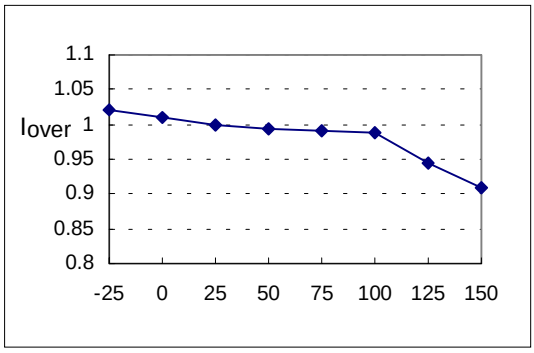


Figure 4. Peak Current Limit

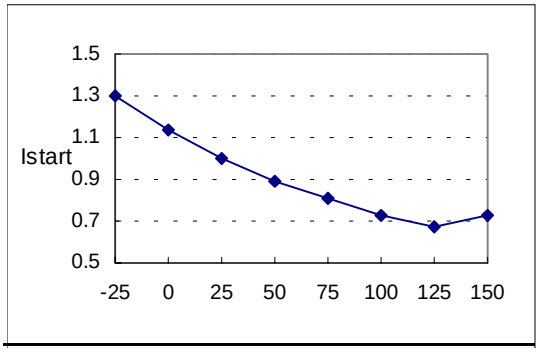


Figure 5. Start up Current

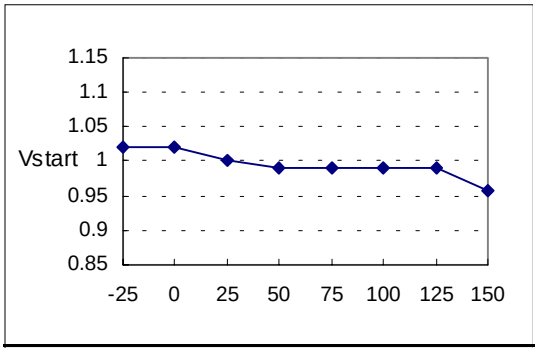


Figure 6. Start Thershold Voltage

Typical Performance Characteristics (Continued)

(These characteristic graphs are normalized at Ta=25°C)

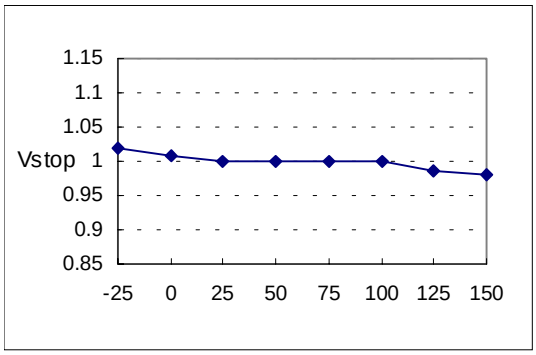


Figure 7. Stop Threshold Voltage

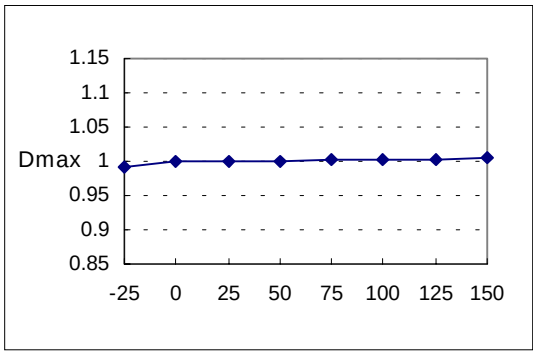


Figure 8. Maximum Duty Cycle

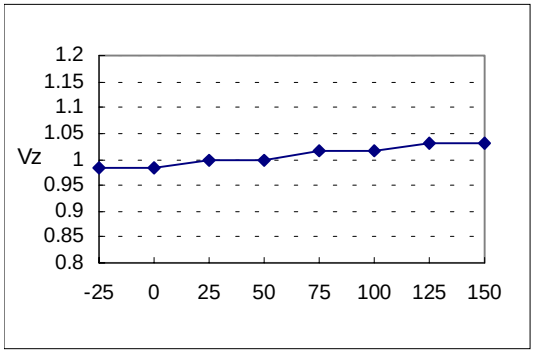


Figure 9. V_{CC} Zener Voltage

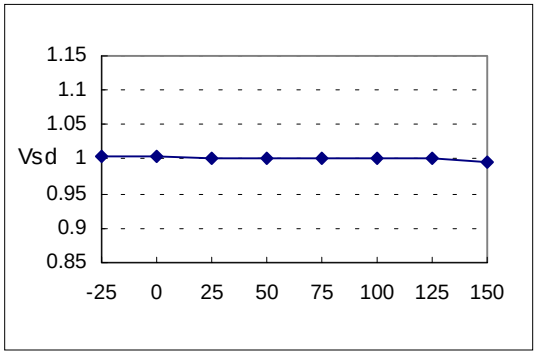


Figure 10. Shutdown Feedback Voltage

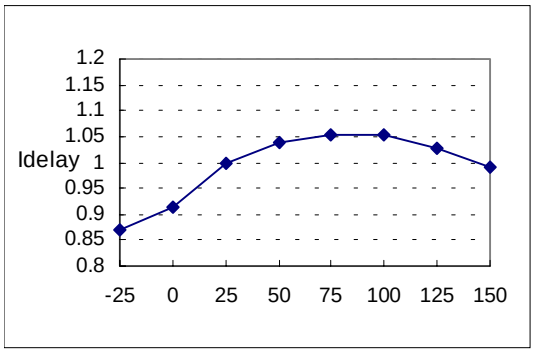


Figure 11. Shutdown Delay Current

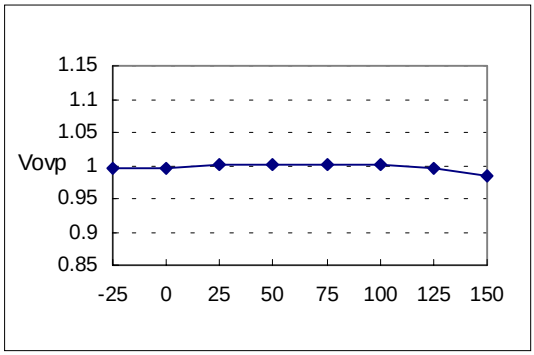


Figure 12. Over Voltage Protection

Typical Performance Characteristics (Continued)

(These characteristic graphs are normalized at Ta=25°C)

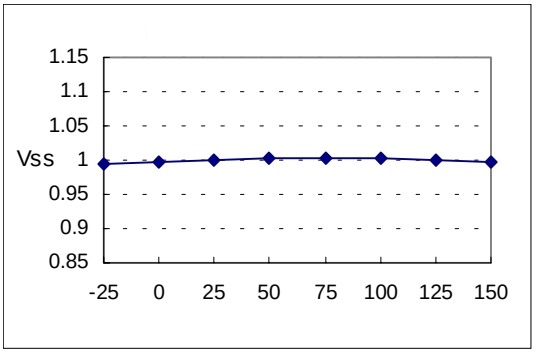


Figure 13. Soft Start Voltage

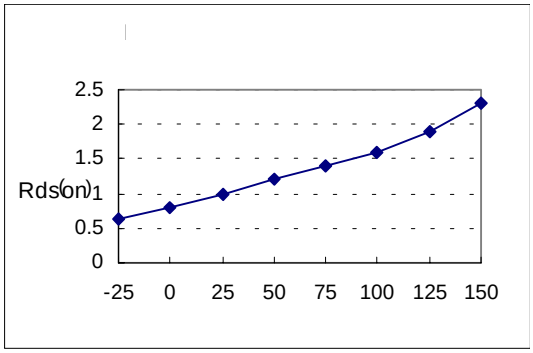
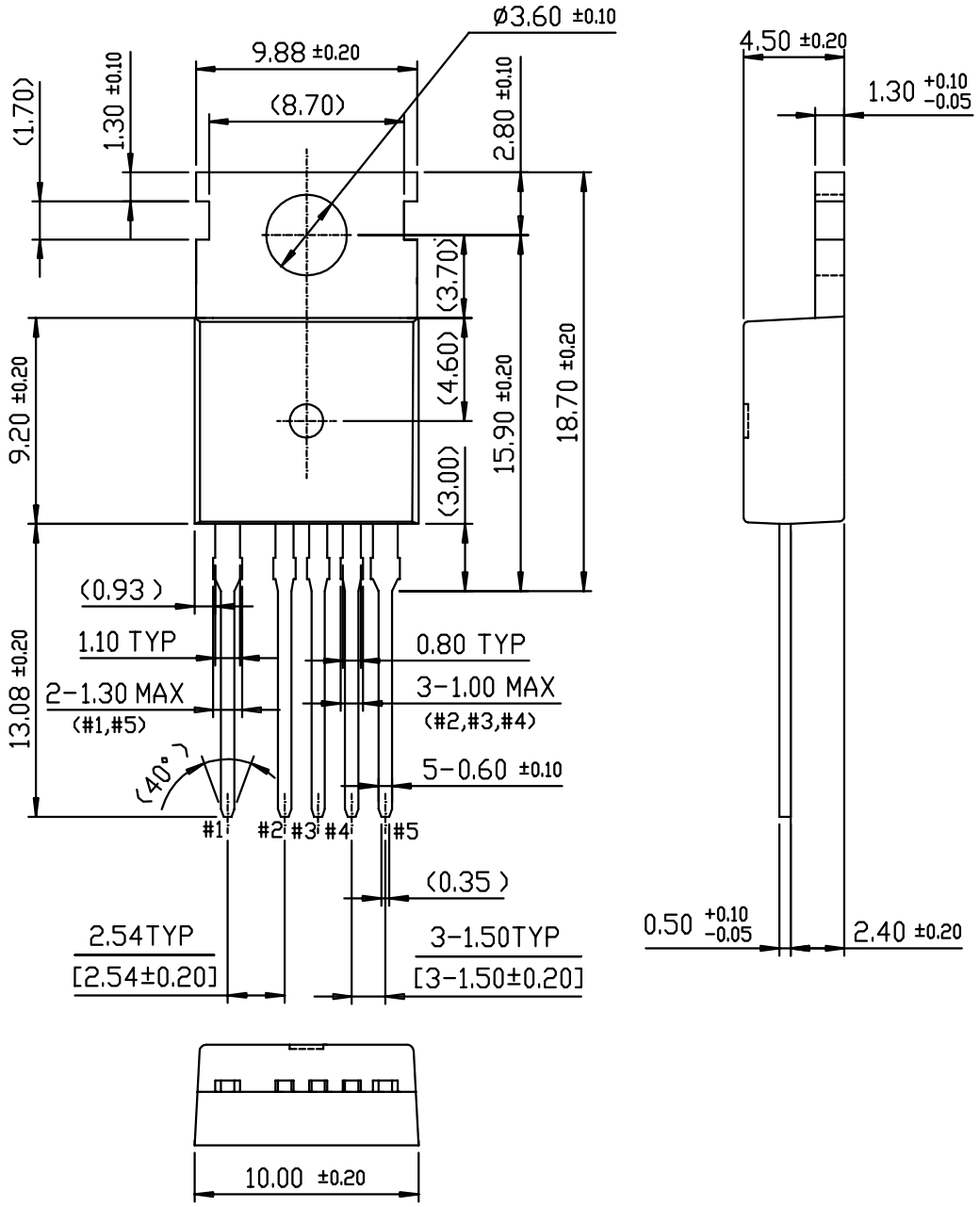


Figure 14. Static Drain-Source on Resistance

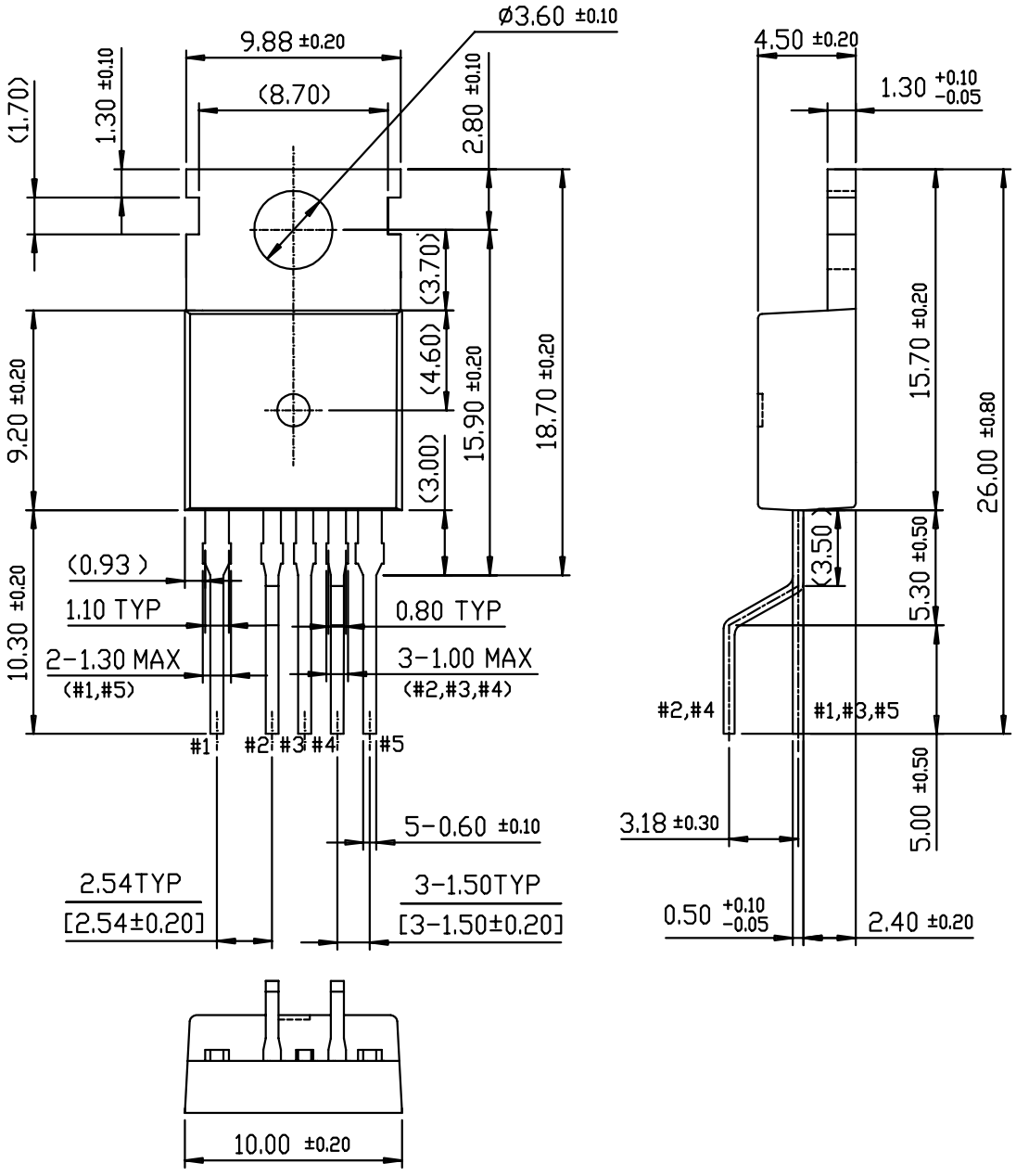
Package Dimensions

TO-220-5L



Package Dimensions

TO-220-5L(Forming)



Ordering Information

Product Number	Package	Rating	Topr (°C)
KA5M0765RQC-TU	TO-220F-5L	650V, 7A	–25°C to +85°C
KA5M0765RQC-YDTU	TO-220F-5L(Forming)		

TU : Non Forming Type
YDTU : Forming Type

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2. A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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