

Data Sheet K 6259 K





SAW Components K 6259 K IF Filter for Intercarrier/Multistandard Applications

38,90 MHz

Plastic package **DIP10K**

Data Sheet

Standard

- D/K
- M/N

Features

- TV IF filter switchable from M/N mode to D/K mode
- M/N mode with Nyquist slope and sound shelf at 34,40 MHz
- Constant group delay
- D/K mode with Nyquist slope and broad sound shelf for sound carriers at 32,40 MHz and 33,40
- Group delay predistortion

12,7 8 7 6 18,5 11,5 0,29 0,49 4 x 2,54

Dimensions in mm, approx. weight 1,8 g

Terminals

■ Tinned CuFe alloy

Pin configuration

1	Input

2 Input - ground

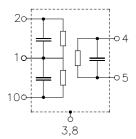
3;8 Chip carrier - ground

4; 5 Output

6; 7 Not connected

Free

10 Switching input



Туре	Ordering code	Marking and package according to	Packing according to
K 6259 K	B39389-K6259-K100	C61157-A2-A3	F61074-V8068-Z000

Maximum ratings

Operable temperature range	T_{A}	-25/+65	°C	
Storage temperature range	$T_{\rm stg}$	-40/+85	°C	
DC voltage	$V_{\rm DC}$	12	V	between any terminals
AC voltage	V_{pp}	10	V	between any terminals



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Characteristics in M/N mode (switching input pin 10 connected to input pin 1)

Reference temperature: $T_{\rm A} = 25\,^{\circ}{\rm C}$ Terminating source impedance: $Z_{\rm S} = 50\,\Omega$ Terminating load impedance: $Z_{\rm L} = 2\,{\rm k}\Omega\,||\,3\,{\rm pF}$

				min.	typ.	max.	
Insertion attenuation			α				
Reference level for the	37,40	MHz		15,2	16,7	18,2	dB
following data							
Relative attenuation			α_{rel}				
Picture carrier	38,90	MHz		5,0	6,0	7,0	dB
Color carrier	35,32	MHz		0,8	1,8	2,8	dB
Sound carrier	34,40	MHz		16,9	18,4	19,9	dB
Adjacent picture carrier	32,90	MHz		40,0	54,0	_	dB
Adjacent sound carrier	40,40	MHz		41,0	50,0	_	dB
Lower sidelobe	25,00 32,90	MHz		33,0	38,0	_	dB
Upper sidelobe	40,40 45,00	MHz		36,0	43,0	_	dB
Reflected wave signal suppression 1,2 μs 6,0 μs after main pulse (test pulse 250 ns, carrier frequency 37,40 MHz)				42,0	50,0	_	dB
Feedthrough signal suppression 1,2 μs 1,1 μs before main pulse (test pulse 250 ns, carrier frequency 37,40 MHz)				_	56,0	_	dB
Group delay ripple (p-p))		Δτ	_	40	_	ns
Impedance at 37,40 MHz							
Input:	$Z_{\rm IN} = R_{\rm IN} C_{\rm I}$	N		_	1,2 17,2	_	$k\Omega \parallel pF$
Output:	$Z_{\text{OUT}} = R_{\text{OUT}} \parallel C_0$	TUC			1,4 6,0	_	kΩ pF
Temperature coefficier	nt of frequency		TC_{f}	_	-72	_	ppm/K



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Characteristics in D/K mode (switching input pin 10 connected to ground input pin 2)

Reference temperature: $T_{\rm A}=25\,^{\circ}{\rm C}$ Terminating source impedance: $Z_{\rm S}=50\,\Omega$ Terminating load impedance: $Z_{\rm L}=2\,{\rm k}\Omega\,||\,3\,{\rm pF}$

					min.	typ.	max.	
Insertion attenuation				α				
Reference level for the		37,40	MHz		15,9	17,4	18,9	dB
following data								
Relative attenuation				α_{rel}				
Picture carrier		38,90	MHz		5,1	6,1	7,1	dB
Color carrier		34,47	MHz		-0,7	0,3	1,3	dB
Sound carrier		32,40	MHz		15,2	16,7	18,2	dB
		33,40	MHz		16,1	17,6	19,1	dB
Adjacent picture carrier		30,90	MHz		44,0	56,0	_	dB
Adjacent sound carrier		40,40	MHz		41,0	50,0		dB
Lower sidelobe	25,00	30,90	MHz		37,0	45,0		dB
Upper sidelobe	40,40	45,00	MHz		35,0	41,0	_	dB
Reflected wave signal	suppressi	on						
1,2 μs 6,0 μs after main pulse (test pulse 250 ns, carrier frequency 37,40 MHz)					42,0	51,0	_	dB
Feedthrough signal suppression 1,2 μs 1,1 μs before main pulse (test pulse 250 ns, carrier frequency 37,40 MHz)					_	56,0	_	dB
Group delay predistor				Δτ				
(reference frequency 38	,90 MHz)							
		37,10			_	- 75		ns
_		34,47	MHz			20	_	ns
Impedance at 37,40 MH	lz							
Input:	$Z_{IN} = R_{I}$	$_{N} \parallel C_{I}$	N		_	0,7 26,4		$k\Omega \parallel pF$
Output	$Z_{OUT} = R_0$	OUT C	TUC			1,4 6,0	_	$k\Omega \parallel pF$
Temperature coefficient of frequency			TC _f	_	-72	_	ppm/K	



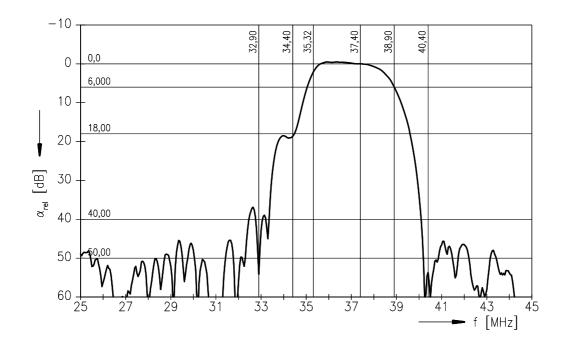
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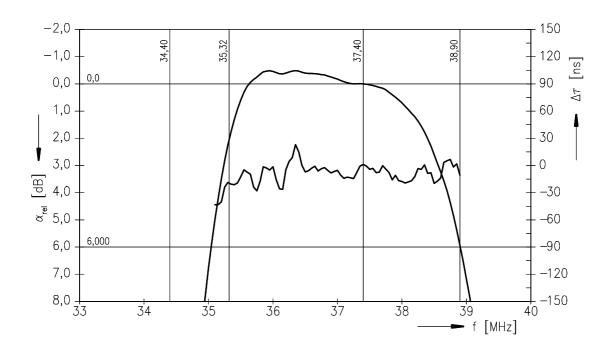
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Data Sheet

Frequency response M/N mode (switching input pin 10 connected to input pin 1)







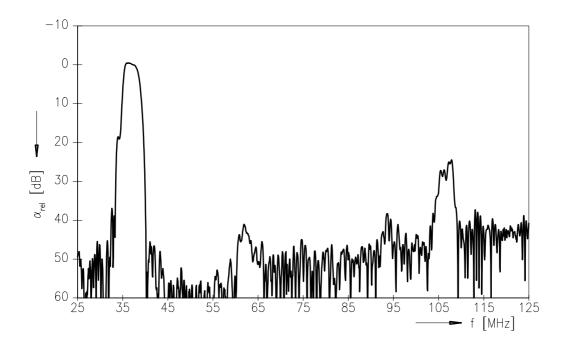
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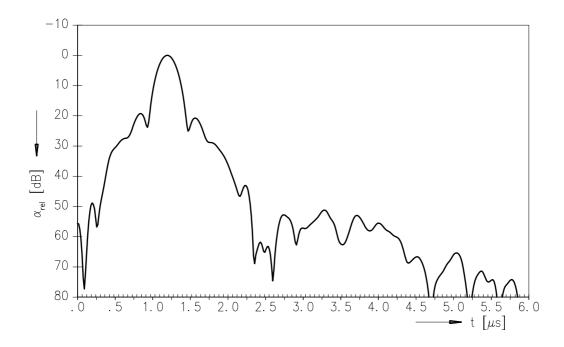
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Frequency response M/N mode (switching input pin 10 connected to input pin 1)



Time domain response M/N mode





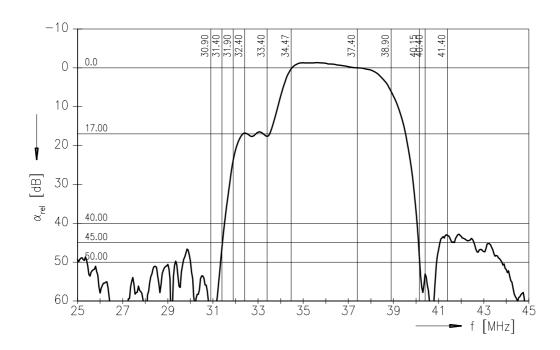
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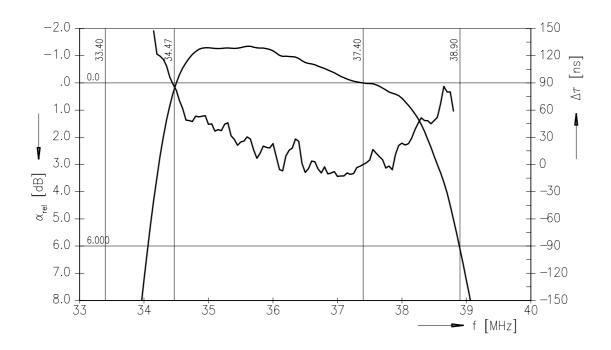
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Frequency response D/K mode (switching input pin 10 connected to ground input pin 2)







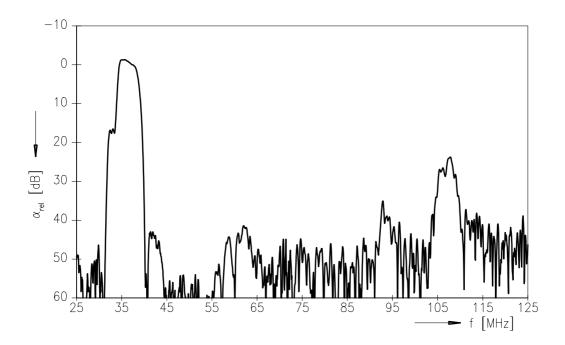
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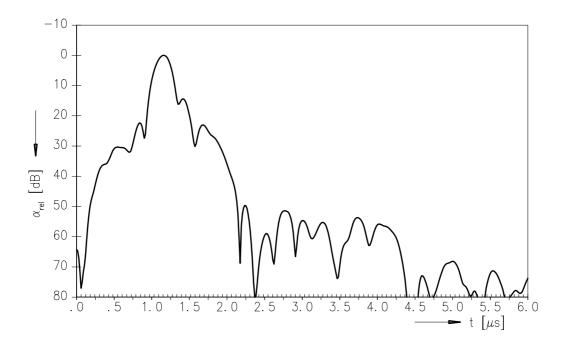
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Frequency response D/K mode (switching input pin 10 connected to ground input pin 2)



Time domain response D/K mode





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