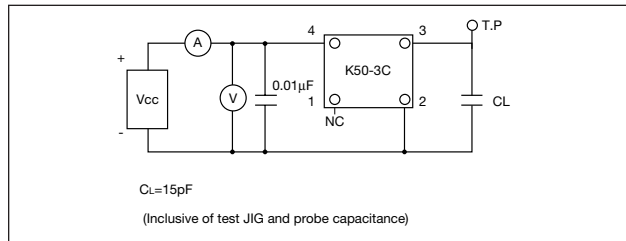


Clock Oscillators (SMD)

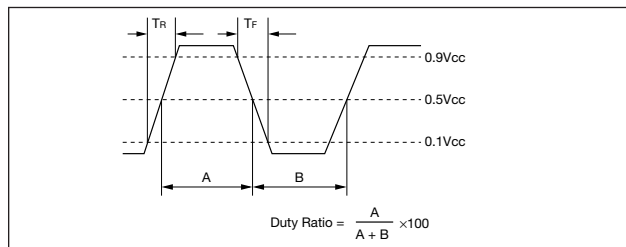


K50-3C Series (3.3V)

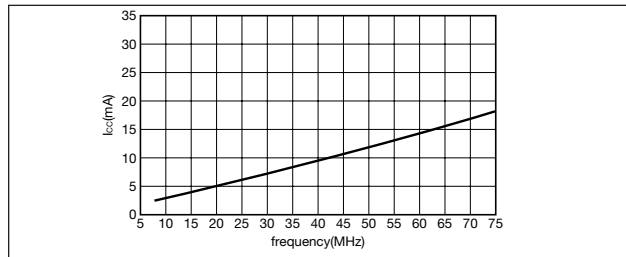
TEST CIRCUIT



OUTPUT WAVE FORM



SUPPLY CURRENT CHARACTERISTIC



PIN CONNECTION

Pin #	Function
1	CONTROL
2	CASE GND
3	OUTPUT
4	+Vcc

ENABLE/DISABLE

Pin #1	Pin #3
"H" or Open	Oscillation
"L"	High Impedance or Oscillation Stop

FEATURES

- Miniature SMD ceramic package type
- Frequency range = 8MHz to 75MHz
- Frequency precision = ± 100 ppm, ± 50 ppm
- Tristate output inhibit
- Low supply voltage (3.3V)
- Load capacitance 15pF

HOW TO ORDER

K50 - 3C 1 S E 40.0000M R

Packaging

R = Tape and reel,
1,000 pcs/reel

Frequency (MHz)

8.00000	27.0000	49.0000
13.0000	29.4989	49.1520
14.31818	30.0000	50.0000
16.0000	32.0000	53.1250
20.0000	33.8688	64.0000
24.0000	35.3280	66.6667
24.5760	40.0000	—
25.0000	44.0000	—

Enable/Disable Function

E = with function (STD)

Duty Ratio

S = 45% to 55%
(f > 32.0MHz)
NIL = 40% to 60%

Tolerance

1 = ± 100 ppm
0 = ± 50 ppm

Series

SPECIFICATIONS

Items	Code	Rating	Unit	Remarks
Output Frequency	F _{OUT}	8 to 75	MHz	—
Frequency Precision	$\Delta F/F$	± 100 , ± 50	ppm	Inclusive of temperature load, voltage variation
Aging Rate	$\Delta F/F$	± 5	ppm/y	—
Operating Temperature	T _{OPR}	-10 to 70	°C	—
Storage Temperature	T _{STR}	-55 to 125	°C	—
Supply Voltage	V _{CC}	3.3 $\pm$ 0.3	V	—
Supply Current	I _{CC}	25	mA	C _L =15pF, Temp.=25°C
Duty Ratio	SYM	40 to 60	%	0.5V _{CC} DC Level
Output 0 Level	V _{OL}	0.1V _{CC} max.	V	I _{OL} = 8mA
Output 1 Level	V _{OH}	0.9V _{CC} min.	V	I _{OH} = -8mA
Rise/Fall Time	T _R , T _F	10 max.	nsec	0.1V _{CC} -0.9V _{CC}
Load Capacitance	C _L	15 max.	pF	—
Enable/Disable Time	—	5	msec	8<F≤32MHz
		150	nsec	32<F≤50MHz
		5	msec	50<F≤68MHz
Input Voltage Low	V _{IL}	0.5 max.	V	—
Input Voltage High	V _{IH}	2.0 min.	V	—