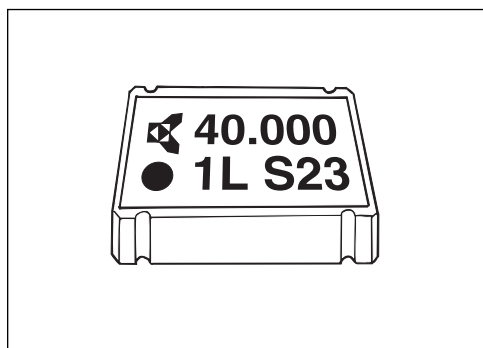


Crystal Clock Oscillators - Miniature SMT

K50-HC-CS Series: HCMOS Drive - TTL/CMOS Compatible



f_o : 8 to 68MHz



HOW TO ORDER

K50-HC 1 - CS E 40.000M R

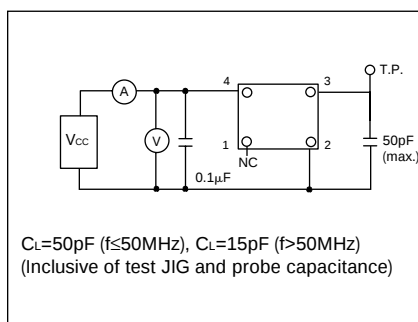
① ② ③ ④ ⑤ ⑥

- ① **Type:** (K50-HC series)
- ② **Stability:** 1-100ppm, 0-50ppm
- ③ **Output Compatibility:** CS = CMOS 45/55 duty cycle @ 50% V_{DD}
- ④ **Tristate Output:** E with function
- ⑤ **Frequency:** (MHz)
- ⑥ **Standard Packaging:** R = tape and reel (1k per reel)

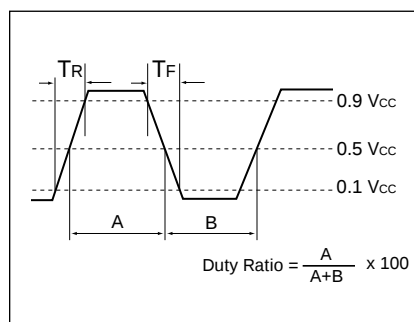
Specifications

CLASSIFICATION	CODE	RATING	UNIT	REMARKS
Output Frequency	f_{OUT}	8~68	MHz	—
Frequency Precision	$\Delta f/f$	$\pm 100, \pm 50$	ppm	Inclusive of Temp, Load, Voltage variation
Aging Rate	$\Delta f/f$	± 5	ppm/y	—
Operating Temp.	T_{OPR}	-10~70	°C	—
Storage Temp.	T_{STR}	-55~125	°C	—
Supply Voltage	V_{CC}	5 $\pm$ 0.5	V	—
Supply Current	I_{CC}	50 max.	mA	$C_L=50pF$, Temp.=25°C, $f=50MHz$
Duty Ratio	Sy	45~55	%	0.5 V_{CC} DC Level
Output 0 Level	V_{OL}	0.1 V_{CC} max.	V	$I_{OL}=16mA$
Output 1 Level	V_{OH}	0.9 V_{CC} min.	V	$I_{OH}=16mA$
Rise/Fall Time	T_R, T_F	10 max.	nsec	0.1 V_{CC} -0.9 V_{CC}
Load Capacitance	C_L	50 max. ($f \leq 50MHz$), 15 max. ($f > 50MHz$)	pF	—
Enable/Disable Time	—	100 max.	nsec	—
Input Voltage Low	V_{IL}	0.8 max.	V	—
Input Voltage High	V_{IH}	2.2 min.	V	—

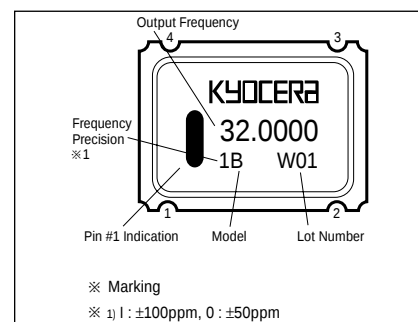
Test Circuit



Output Waveform



Marking Specifications



Pin Connection

Pad #	Function
1	CONTROL
2	CASE GND
3	OUTPUT
4	+V _{CC}

Enable / Disable

Pin #1	Pin #3
"H" or Open	Oscillation
"L"	High impedance

Additional information on this product is available from AVX's catalog or AVX's FAX Service.
Call 1-800-879-1613 and request document #135. Visit our website <http://www.avxcorp.com>

