BI-DIRECTIONAL DC MOTOR DRIVER

The KA8301 is a monolithic integrated circuit designed for driving bi-directional DC motor with braking and speed control, and it is suitable for the loading motor driver of VCR systems. The speed control can be achieved by adjusting the external voltage of the speed control pin.

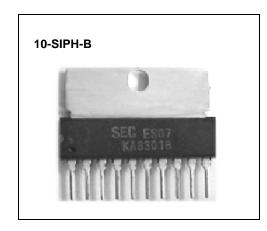
FEATURES

- Built-in brake function for stable brake characteristics.
- Built-in element to absorb a dash current derived from changing motor direction and braking motor drive.
- External motor speed control pin
- Stable motor direction change.
- Interfaces with CMOS devices.

TARGET APPLICATION

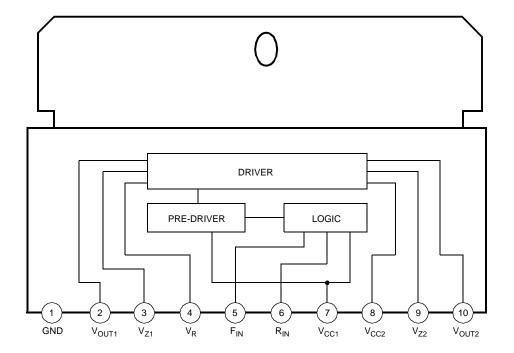
- VCR
- Low current DC motor such audio equipment

BLOCK DIAGRAM



ORDERING INFORMATION

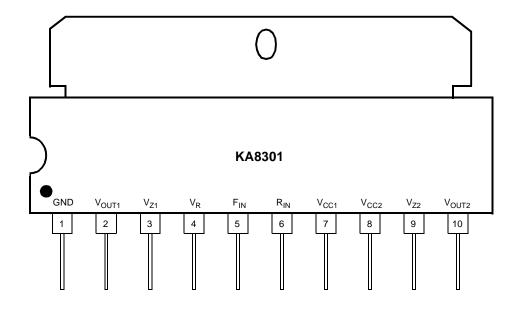
Device	Package	Operating Temperature
KA8301	10-SIPH-B	–25°C ~ +75°C





MIC-99D001 January 1999

PIN CONFIGURATIONS



PIN DESCRIPTION

Pin No.	Symbol	I/O	Description	Pin No.	Symbol	I/O	Description
1	GND	_	Ground	6	R _{IN}	Ι	Input 2
2	V _{OUT1}	0	Output 1	7	V _{CC1}	_	Supply voltage (Signal)
3	V _{Z1}	_	Phase compensation	8	V _{CC2}	Ι	Supply voltage (Power)
4	V _R	I	Motor speed control	9	V _{Z2}	Ι	Phase compensation
5	F _{IN}	Ι	Input 1	10	V _{OUT2}	0	Output 2



INTERNAL CIRCUIT

Description	Pin No.	Internal circuit
Output	2, 10	
Phase compensation	3, 9	
Speed control	4	



INTERNAL CIRCUIT (Continued)

Description	Pin No.	Internal circuit
Input	5, 6	
SVCC PVCC	7 8	
GND	1	



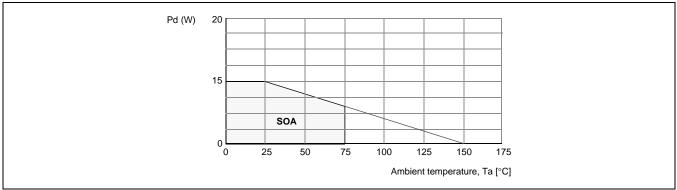
ABSOLUTE MAXIMUM RATING (Ta=25°C)

Characteristics	Symbol	Value	Unit
Supply voltage	V _{CCmax}	18	V
Maximum output current	I _{Omax}	1.6 ^{note1}	A
Power dissipation	Pd	15 ^{note2}	W
Input voltage	V _{IN}	-0.3 ~ V _{CC}	V
Operating temperature	T _{OPR}	-25 ~ +75	°C
Storage temperature	T _{STG}	-55 ~ +150	°C

NOTES:

- 1. Duty 1/100, pulse width 500µs
- 2. 1) When mounted on glass epoxy PCB ($76.2 \times 114 \times 1.57$ mm)
 - 2) Power dissipation reduces 120mW / °C for using above Ta=25°C
 - 3) Do not exceed Pd and SOA.

PD GRAPH



RECOMMENED OPERATING CONDITIONS (Ta=25°C)

Characteristics	Symbol	Value	Unit
Operating supply voltage	V _{CC}	8 ~ 18	V



ELECTRICAL CHARACTERISTICS (Ta=25°C, V_{CC}=12V)

Characteristic	Symbol	Test conditions	Min.	Тур.	max.	Unit
Quiescent current	۱ _Q	Pin5 & 6: GND, R _L =∞	3	5.5	20	mA
Min. input-on current 1	I _{IN1}	R _L =∞, pin5=I _{IN1} , pin6=L	_	10	50	μA
Min. input-on current 2	I _{IN2}	R _L =∞, pin5=L, pin6=I _{IN2}	_	10	50	μA
Input threshold voltage 1	V _{INTH1}	R _L =∞, pin5=V _{INTH1} , pin6=L	0.7	1.3	2.0	V
Input threshold voltage 2	V _{INTH2}	R _L =∞, pin5=L, pin6=V _{INTH2}	0.7	1.3	2.0	V
Output leakage current 1	I _{OL1}	R _L =∞, pin5 & 6=GND	_	-	1	mA
Output leakage current 2	I _{OL2}	R _L =∞, pin5 & 6=GND	_	-	1	mA
Zener current 1	I _{Z1}	R _L =∞, pin5=H, pin6=L	_	0.85	1.5	mA
Zener current 2	I _{Z2}	R _L =∞, pin5=L, pin6=H	-	0.85	1.5	mA
Output voltage 1	V _{O1}	R _L =60Ω, pin5=H, pin6=L	6.6	7.2	_	V
Output voltage 2	V _{O2}	R _L =60Ω, pin5=L, pin6=H	6.6	7.1	-	V
Saturation voltage (Pin10-1)	V _{CE10-1}	R _L =R _C =∞, pin5=H, pin6=L, I _{SINK} =100mA	_	0.83	1.5	V
Saturation voltage (Pin2-1)	V _{CE2-1}	R _L =R _C =∞, pin5=L, pin6=H, I _{SINK} =100mA	-	0.83	1.5	V
Saturation voltage (Pin8-2)	V _{CE8-2}	R _L =R _C =∞, pin5=H, pin6=L, I _{SOURCE} =100mA	-	0.83	1.5	V
Saturation voltage (Pin8-10)	V _{CE8-10}	R _L =R _C =∞, pin5=L, pin6=H, I _{SOURCE} =100mA	_	0.83	1.5	V



APPLICATION INFORMATIONS

Pin #5	Pin #6	Pin #2	Pin #10	Function
L (0.7V ↓)	L (0.7V ↓)	L	L	Brake
L (0.7V ↓)	H (2.0V ↑)	L	Н	Reverse
H (2.0V ↑)	L (0.7V ↓)	Н	L	Forward
H (2.0V ↑)	H (2.0V ↑)	Н	Н	Brake

1. FORWARD AND REVERSE CONTROL LOGIC

- If pin #5=H, pin #6=L, load current flows from pin #2 to pin #10 through a motor.
- If pin #5=L, pin #6=H, load current flows from pin #10 to pin #2 through a motor.
- If pin #5=pin #6=L or pin #5=pin #6=H, the KA8301 stops supplying the power to motor while absorbing counter EMF from the motor as a brake.

2. RUSH CURRENT ABSORBING CIRCUIT

If a high voltage generated during reversing operation is applied across pin #2 and pin #10, an internal comparator activates the rush current absorbing circuit.

3. DRIVE STAGE

In the forward mode, the drive stage supplies a load current to the motor from pin #2 to pin #10. In the reverse mode, it supplies the current from pin #10 to pin #2.

The output voltage (V_{OUT}) applied to the motor is given by the followings;

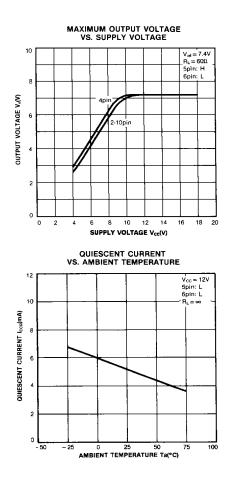
 $V_{OUT} = V_{ZD} - V_{CE}$ (sat), where V_{ZD} : a zener voltage applied to pin #4

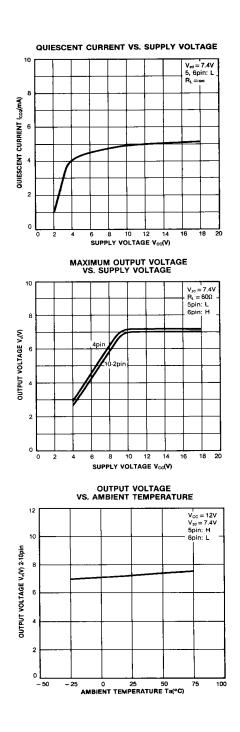
If pin #4 is left open, the output voltage is given by the followings;

 $V_{OUT} = V_{CC1} - V_{CE}(sat, pnp) - 2V_F - V_{CE}(sat)$



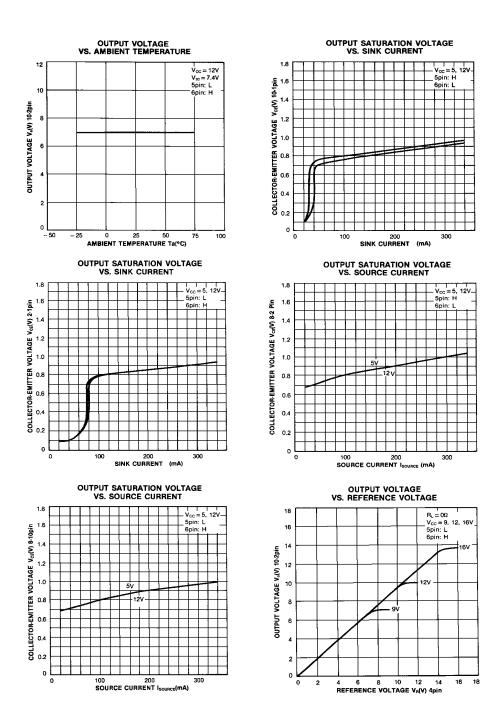
CHARACTERISTIC GRAPHS







CHARACTERISTIC GRAPHS (Continued)



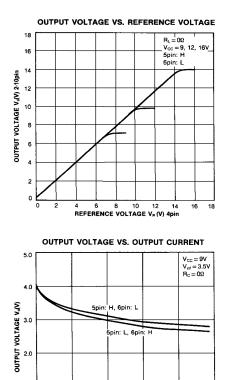


CHARACTERISTIC GRAPHS (Continued)

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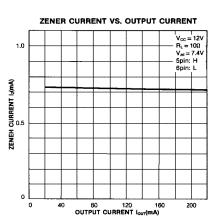
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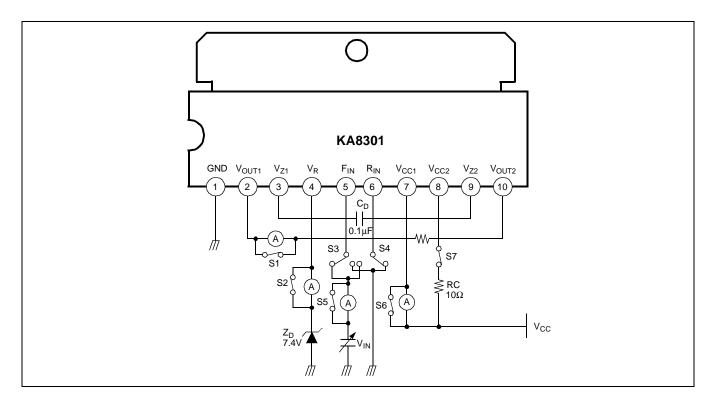
200 400 600 OUTPUT CURRENT Iout(mA) 800

1000



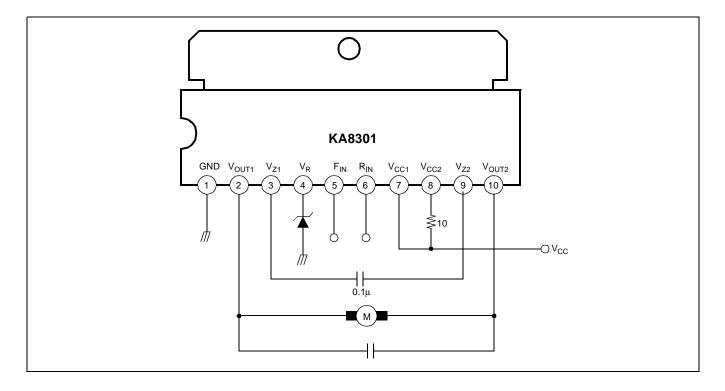
FAIRCHILD SEMICONDUCTOR

TEST CIRCUIT





APPLICATION CIRCUIT





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