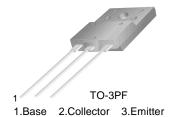


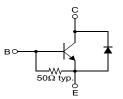
## KSC5086

## **HIgh Definition Color Display Horizontal Deflection Output** (Damper Diode Built In)

- High Collector-Base Voltage: BV<sub>CBO</sub>=1500V
  High Speed Switching: t<sub>F</sub>=0.1µs (Typ.)



Equivalent Circuit



## **NPN Triple Diffused Planar Silicon Transistor**

### Absolute Maximum Ratings T<sub>C</sub>=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V <sub>CBO</sub>	Collector-Base Voltage	1500	V
V <sub>CEO</sub>	Collector-Emitter Voltage	800	V
V <sub>EBO</sub>	Emitter-Base Voltage	6	V
I <sub>C</sub>	Collector Current (DC)	7	Α
I <sub>CP</sub>	Collector Current (Pulse)	16	Α
P <sub>C</sub>	Collector Dissipation (T <sub>C</sub> =25°C)	50	W
TJ	Junction Temperature	150	°C
T <sub>STG</sub>	Storage Temperature	- 55 ~ 150	°C

### Electrical Characteristics T<sub>C</sub>=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
I <sub>CES</sub>	Collector Cut-off Current (V <sub>BE</sub> =0)	$V_{CE} = 1400V, R_{BE} = 0$			1	mA
I <sub>CBO</sub>	Collector Cu-toff Current	V <sub>CB</sub> =800V, I <sub>E</sub> =0			10	μΑ
I <sub>EBO</sub>	Emitter Cut-off Current	$V_{EB} = 4V, I_{C} = 0$	40		200	mA
V <sub>EBO</sub>	Base-Emitter Breakdown Voltage	$I_E = 250 \text{mA}, I_C = 0$	6			V
h <sub>FE</sub>	DC Current Gain	$V_{CE} = 5V, I_{C} = 1.0A$	8			
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	$I_C = 5A, I_B = 1.2A$			5	V
V <sub>BE</sub> (sat)	Base-Emitter Saturation Voltage	$I_C = 5A, I_B = 1.2A$			1.5	V
V <sub>F</sub>	Damper Diode Turn On Voltage	I <sub>F</sub> = 6A			2	V
t <sub>F</sub>	Fall Time	$V_{CC} = 200V, I_{C} = 4A$			0.2	μs
		$I_{B1} = 0.8A, I_{B2} = -1.6A$ $R_L = 50\Omega$				

### Thermal Characteristics $T_C=25$ °C unless otherwise noted

Symbol	ltem	Max	Unit
$R_{\theta jC}$	Thermal Resistance, Junction to Case	2.5	°C/W

# **Typical Characteristics**

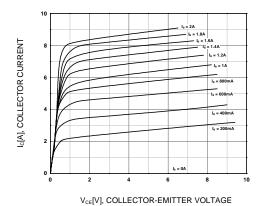


Figure 1. Static Characteristic

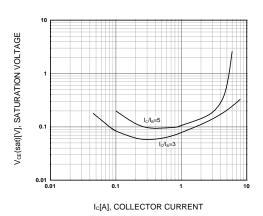


Figure 3. Collector-Emitter Saturation Voltage

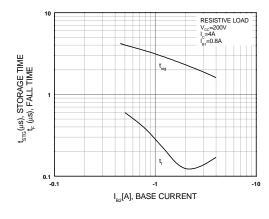


Figure 5. Switching Time

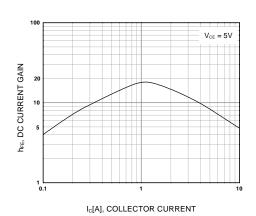


Figure 2. DC current Gain

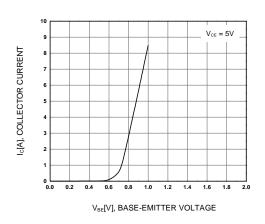


Figure 4. Base-Emitter On Voltage

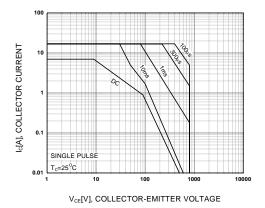
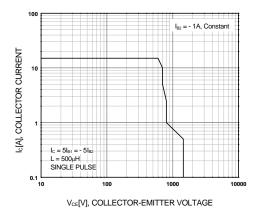


Figure 6. Safe Operating Area

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# Typical Characteristics (Continued)



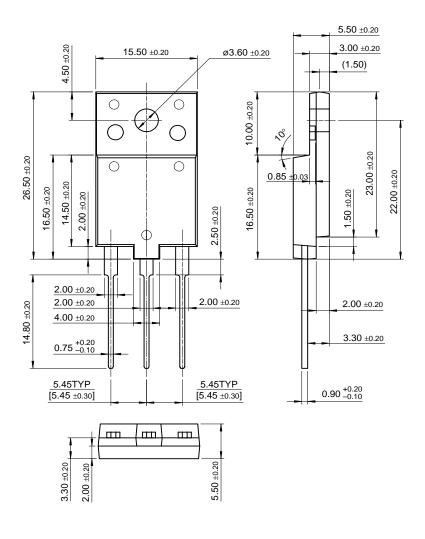
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Figure 1. Reverse Bias Safe Operating Area

Figure 2. Power Derating

## **Package Demensions**

## TO-3PF



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