

SPEC. NO.

TQ3C-8EAC0-E1AAWG17-00

DATE

July 28, 1999

S P E C

FOR:

TYPE: KHS057QV1AJ-G81

CONTENTS

1. Application
2. Construction and Outline
3. Mechanical Specifications
4. Absolute Maximum Ratings
5. Electrical Characteristics
6. Optical Characteristics
7. Circuit Block Diagram
8. Interface Signals
9. Interface Timing Chart
10. Data and Screen
11. Input Timing Characteristics
12. Supply Voltage Sequence Condition
13. Backlight Characteristics
14. Lot Number Identification
15. Warranty
16. Precautions for Use
17. Reliability Data / Environmental Test
18. Outline Drawing

KYOCERA CORPORATION
KAGOSHIMA HAYATO PLANT
LCD DIVISION

This specification is subject to change without notice.
Consult Kyocera before ordering.

Original	Designed by :Engineering Dept.			Confirmed by :QA Dept.	
Issue Data	Prepared	Checked	Approved	Checked	Approved
July 28, 1999	H.Tokumori	M.Fujitani	Whs Kio	S.Hagooko	J. Yoshida

Revision Record

Date		Designed by : Engineering Dept.			Confirmed by : QA Dept.	
		Prepared	Checked	Approved	Checked	Approved
Rev. No.	Date	Page	Descriptions			

1. Application

This data sheet defines the specification for a $(320 \times 3) \times 240$ dot, STN color dot matrix type Liquid Crystal Display with CFL backlight.

2. Construction and Outline

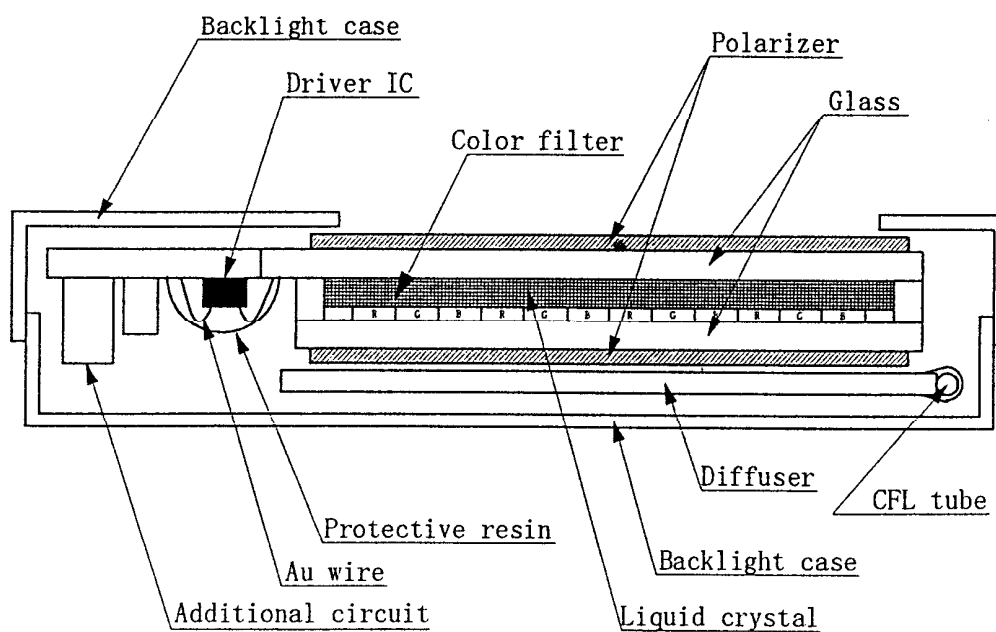
$(320 \times 3) \times 240$ dots, COB type LCD with CFL backlight.

Backlight system : Side-edge type CFL (1 tube).

Inverter : Option
Recommended Inverter : PH-BLC08-K3 (HITACHI MEDIA ELECTRONICS)
or equivalent

Polarizer : Glare treatment.

Additional circuit : Bias voltage circuit, Randomizing circuit.



3. Mechanical Specifications

ITEM	SPECIFICATION	UNIT
Outline dimensions	154.6 (W) × 114.8 (H) × 8.5 (D)	mm
Effective viewing area	118.18 (W) × 89.38 (H)	mm
Dot number	(320×3) (W) × 240 (H)	Dots
Dot size	0.10 (W) × 0.34 (H)	mm
Dot pitch	0.12 (W) × 0.36 (H)	mm
Display color *1	White *2	—
Base color *1	Black *2	—
Weight	200	g

*1 Due to the characteristics of the LC material, the color vary with environmental temperature.

*2 Negative-type display

Display data "H" :R,G,B Dots ON : White

Display data "L" :R,G,B Dots OFF : Black

4. Absolute Maximum Ratings

4-1 Electrical absolute maximum ratings

Temp. = 25 °C

ITEM	SYMBOL	MIN.	MAX.	UNIT
Supply voltage for logic	VDD	0	7.0	V
Supply voltage for LCD driving	VEE	0	44.0	V
Input voltage	Vin	0	VDD	V

4-2 Environmental absolute maximum ratings

ITEM	SYMBOL	MIN	MAX	UNIT
Operating temperature	Top	0	50	°C
Storage temperature *1	T _{STO}	-20	60	°C
Operating humidity *2	Hop	10	*3	%RH
Storage humidity *2	H _{STO}	10	*3	%RH
Vibration	—	*4	*4	—
Shock	—	*5	*5	—

*1 Temp. = -20°C < 24 Hr. , Temp = 60°C < 24 Hr.
No vibration and shock.

*2 Non-condensation.

*3 Temp. ≤ 40°C, 85% RH Max.
Temp. > 40°C, Absolute Humidity shall be less than 85%RH at 40°C.

*4

Frequency	10~55 Hz	Converted to acceleration value : (0.03~0.91G)
Vibration width	0.15 mm	
Interval	10-55-10 Hz 1 minute	

2 hours in each direction X/Y/Z (6 hours as total)
EIAJ ED-2531.

*5 Acceleration: 50 G
Pulse width : 11 msec
3 times in each direction : ±X/±Y/±Z.
EIAJ ED-2531.

5. Electrical Characteristics

Temp. = 25°C, VDD = +5.0V ± 5%

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Supply voltage for logic	VDD	—	4.75	5.00	5.25	V
LCD driving voltage *1	V _{op} = VEE	0 °C	(25.9)	(26.9)	(27.9)	V
		25 °C	(25.1)	(26.1)	(27.1)	V
		40 °C	(24.3)	(25.3)	(26.3)	V
Input voltage	V _{in}	"H" level	0.8VDD	—	VDD	V
		"L" level	0	—	0.2VDD	V
Clock frequency	f _{cp}	—	2.02	2.16	12.0	MHz
Frame frequency *2	f _{FRM}	—	70	75	80	Hz
Current consumption for logic	IDD	*3	—	(3.0)	(4.5)	mA
Current consumption for LCD driving	IEE		—	(7.5)	(11.3)	mA
Power consumption	P _{disp}		—	(211)	(330)	mW

*1 Maximum contrast ratio is obtained by adjusting the LCD supply voltage (V_{op}= VEE) for driving LCD.

*2 In consideration of display quality, it is recommended that frame frequency is set in the range of 70-80Hz. When you have to use higher frame and clock frequencies, confirm the LCD's performance and quality prior to finalizing the frequency values: Generally, as frame and clock frequencies become higher, current consumption will get bigger and display quality will be degraded.

*3 Display high frequency pattern, (see below).
VDD = 5.0V , V_{op} = VEE , f_{FRM} = 75 Hz , f_{cp} = 2.16MHz
Pattern:

```

1 2 3 4 5 6 ..... 960 (dot)
1 ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■
2 ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■
3 ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■
: ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■
: ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■
239 ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■
240 ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■
(dot)

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6. Optical Characteristics

6-1. Reflective mode

Measuring Spot = ϕ 6mm , Temp. = 25°C

ITEM		SYMBOL	CONDITION		MIN.	TYP.	MAX.	UNIT
Response time	Rise	Tr	$\theta = \phi = 0^\circ$		—	(200)	(300)	ms
	Down	Td	$\theta = \phi = 0^\circ$		—	(150)	(250)	ms
Viewing angle range		θ	$CR \geq 2$	$\phi = 0^\circ$	(-40)	—	(40)	deg.
		ϕ		$\theta = 0^\circ$	(-40)	—	(35)	deg.
Contrast ratio		CR	$\theta = \phi = 0^\circ$		(4.0)	(6.0)	—	—
Reflectance		ρ	—		(TBD)	(TBD)	—	%
Chromaticity coordinates	Red	x	$\theta = \phi = 0^\circ$		(TBD)	(TBD)	(TBD)	—
		y			(TBD)	(TBD)	(TBD)	
	Green	x	$\theta = \phi = 0^\circ$		(TBD)	(TBD)	(TBD)	
		y			(TBD)	(TBD)	(TBD)	
	Blue	x	$\theta = \phi = 0^\circ$		(TBD)	(TBD)	(TBD)	
		y			(TBD)	(TBD)	(TBD)	
	White	x	$\theta = \phi = 0^\circ$		(TBD)	(TBD)	(TBD)	
		y			(TBD)	(TBD)	(TBD)	

Optimum contrast is obtained by adjusting the LCD driving voltage(Vop) while at the viewing angle of $\theta = \phi = 0^\circ$.

* Definition of Reflectance:

$$\rho \text{ (Reflectance)} = \frac{\text{Measured Reflection Brightness}}{\text{Reflection Brightness against Standard White Board}}$$

* Definition of Contrast

$$CR \text{ (Contrast)} = \frac{\text{Reflectance at all pixels "White"}}{\text{Reflectance at all pixels "Black"}}$$

6-2. Transmissive mode

Measuring Spot = ϕ 6mm , Temp. = 25°C

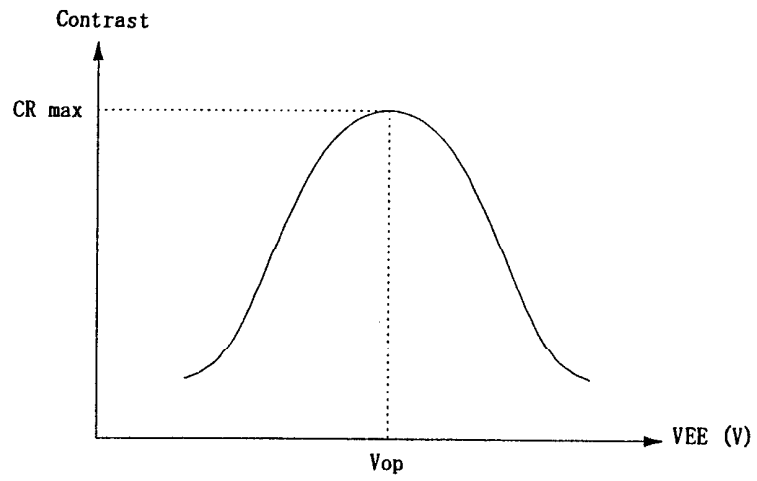
ITEM		SYMBOL	CONDITION		MIN.	TYP.	MAX.	UNIT
Response time	Rise	Tr	$\theta = \phi = 0^\circ$		—	(200)	(300)	ms
	Down	Td	$\theta = \phi = 0^\circ$		—	(150)	(250)	ms
Viewing angle range		θ	$CR \geq 2$	$\phi = 0^\circ$	(-40)	—	(40)	deg.
		ϕ		$\theta = 0^\circ$	(-40)	—	(35)	deg.
Contrast ratio		CR	$\theta = \phi = 0^\circ$		(9)	(18)	—	—
Brightness		L	—		(90)	(130)	—	cd/m ²
Chromaticity coordinates	Red	x	$\theta = \phi = 0^\circ$		(TBD)	(TBD)	(TBD)	—
		y			(TBD)	(TBD)	(TBD)	
	Green	x	$\theta = \phi = 0^\circ$		(TBD)	(TBD)	(TBD)	
		y			(TBD)	(TBD)	(TBD)	
	Blue	x	$\theta = \phi = 0^\circ$		(TBD)	(TBD)	(TBD)	
		y			(TBD)	(TBD)	(TBD)	
	White	x	$\theta = \phi = 0^\circ$		(TBD)	(TBD)	(TBD)	
		y			(TBD)	(TBD)	(TBD)	

Optimum contrast is obtained by adjusting the LCD driving voltage(Vop) while at the viewing angle of $\theta = \phi = 0^\circ$.

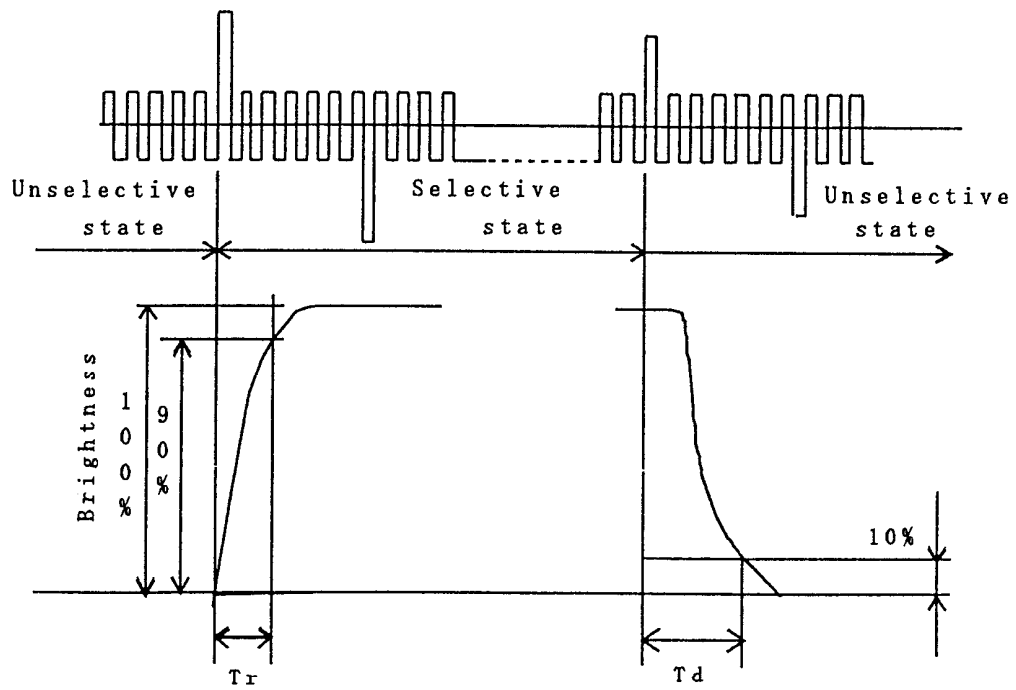
* Contrast ratio is defined as follows :

$$CR(\text{Contrast}) = \frac{\text{Brightness all pixels "White"}}{\text{Brightness all pixels "Black"}}$$

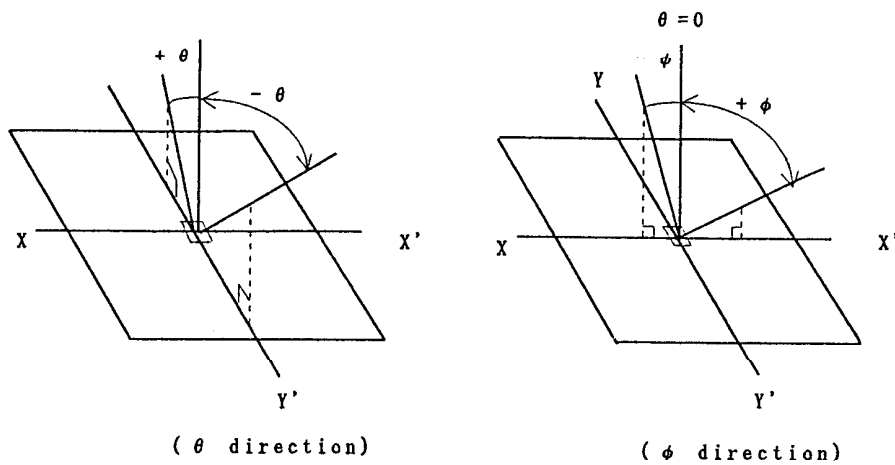
6-1. Definition of V_{op}



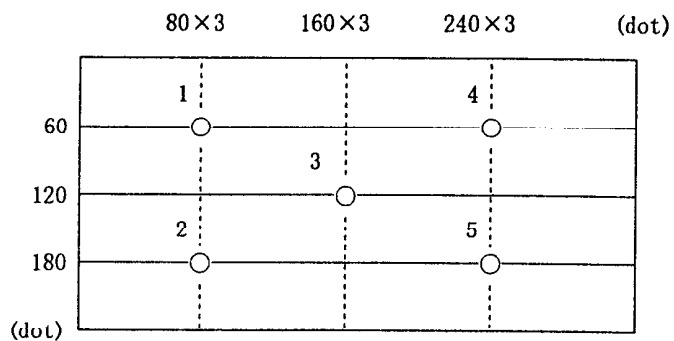
6-2. Definition of response time



6-3. Definition of viewing angle

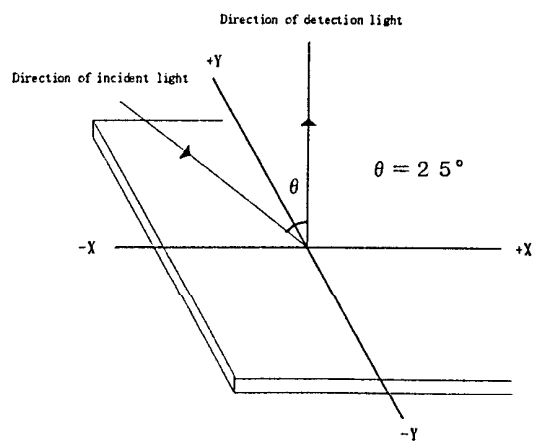


6-4. Measuring points

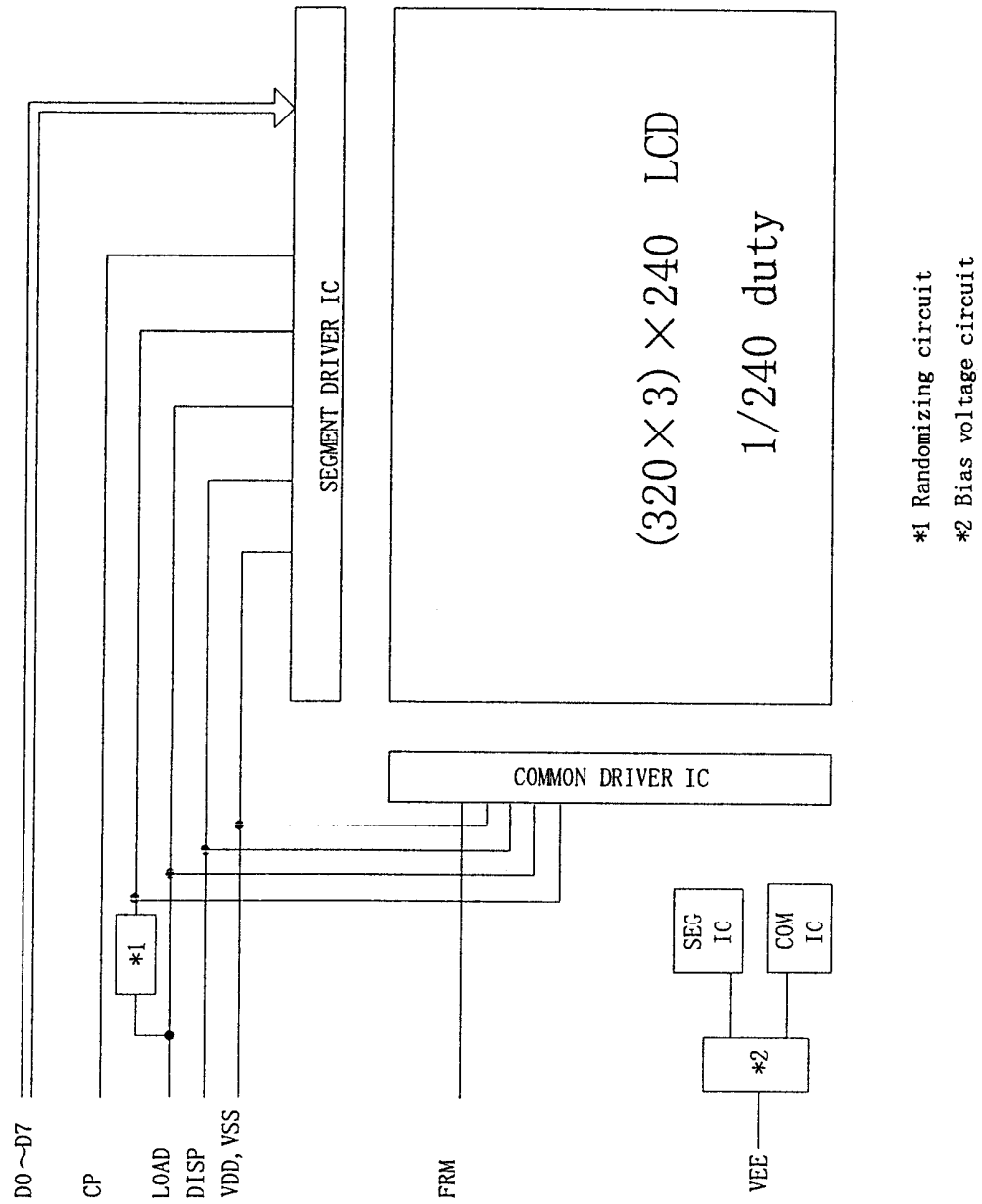


- 1) Rating is defined as the average brightness inside the viewing area.
- 2) 30 minutes after CFL is turned on. (Ambient Temp.=25°C)
- 3) The inverter should meet the eccentric conditions;
-Sine, symmetric waveform without spike in positive and negative.

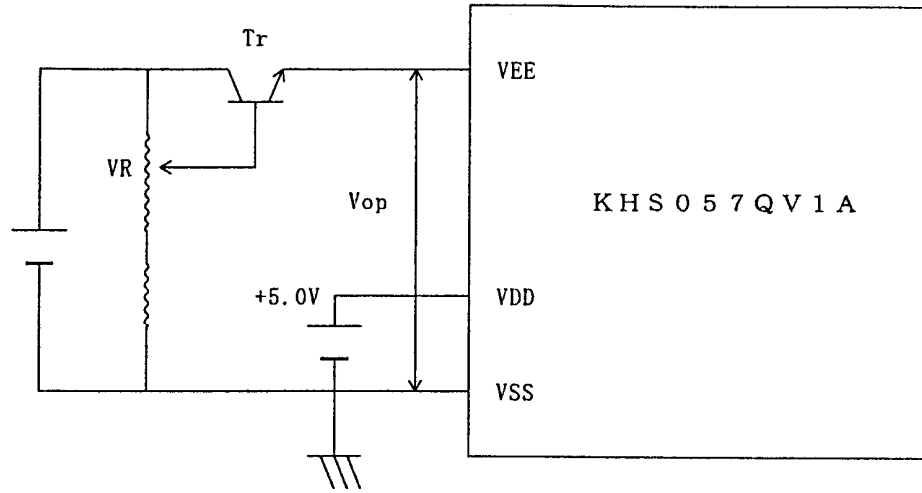
6-5. Measurement method of reflectance



7. Circuit Block Diagram of LCD



7-1. Power supply



8. Interface signals

8-1. LCD

CNI : 53261-1510(Molex)

PIN NO.	SYMBOL	DESCRIPTION	LEVEL
1	FRM	Synchronous signal for driving scanning line	H
2	LOAD	Data signal latch clock	H → L
3	CP	Data signal shift clock	H → L
4	DISP	Display control signal	H(ON), L(OFF)
5	VDD	Power supply for logic	—
6	VSS	GND	—
7	VEE	Power supply for LCD	—
8	D7	Display data	H(ON), L(OFF)
9	D6		
10	D5		
11	D4		
12	D3		
13	D2		
14	D1		
15	D0		

Recommended matching connector : 51021-1500(Molex)

This pin assignment is the reverse of what Molex defined.
Remember that for you designing.

PIN No	SYMBOL	DESCRIPTION	LEVEL
1	HV	Power supply for CFL	AC
2	NC	—	—
3	GND	Ground line(from inverter)	—

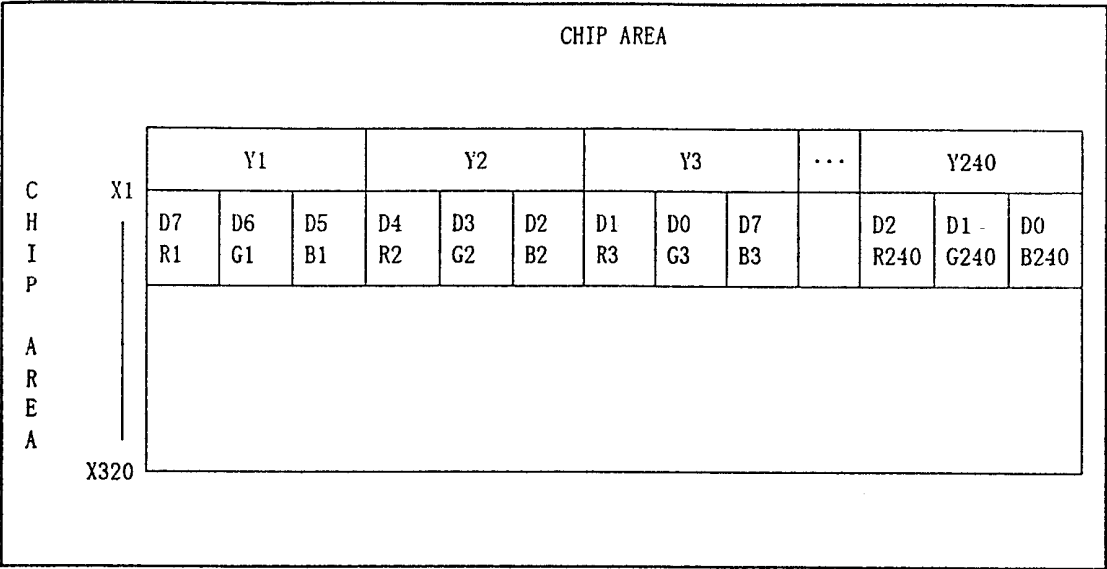
LCD side connector : BHR-03VS-1 (JST)

Recommended matching connector : SM02-(8.0)B-BHS-1 (JST)

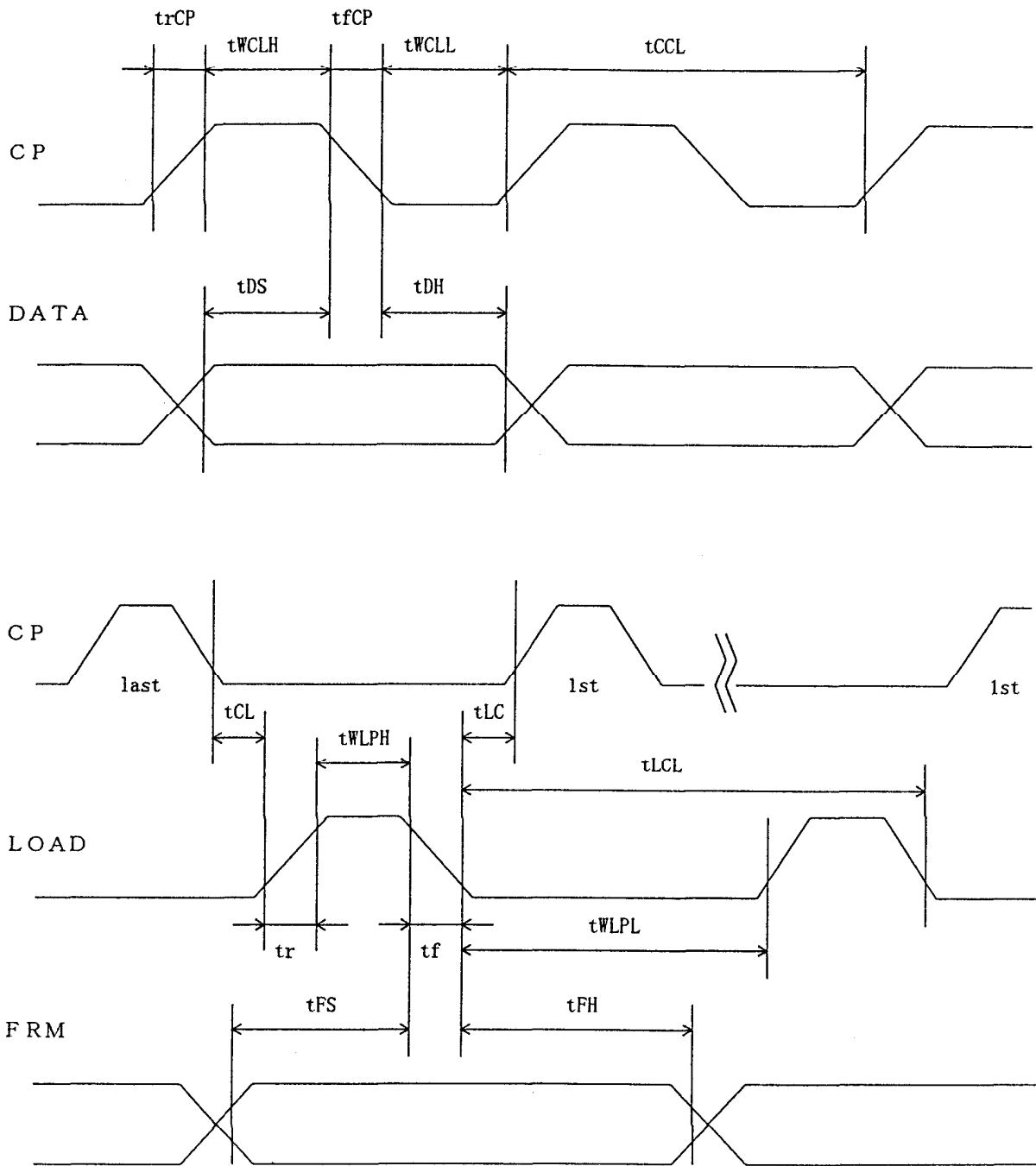
Timing diagram for the 320x320 dot clock mode. The diagram illustrates the relationship between the LOAD, CP (Clock Pulse), and FRM (Frame Transfer Mode) signals and the data bus (D0~D7).

The top section shows a single frame transfer sequence. The FRM signal is active for a duration of $CP \times (320 \times 3) / 8$ pulse. The data bus (D0~D7) is shown with data being transferred in bursts during the active FRM period.

The bottom section shows a burst transfer sequence. The FRM signal is active for 1, 2, 240, and 1 dot clock cycles, with a break symbol (//) indicating the long 240-cycle period. The data bus (D0~D7) is shown with data being transferred in bursts during the active FRM period.



11. Input Timing Characteristics



11-1. Switching characteristics

Input characteristics; VDD = +5.0V ± 5% Temp. = 25 °C

ITEM	SYMBOL	MIN.	MAX.	UNIT
CP Cycle *1	tCCL	82	—	ns
CP "H" Pulse Width	tWCLH	28	—	ns
CP "L" Pulse Width	tWCLL	28	—	ns
CP Rise Up Time *2	trCP	—	30	ns
CP Fall Down Time *2	tfCP	—	30	ns
Data Set Up Time	tDS	28	—	ns
Data Hold Time	tDH	20	—	ns
LOAD "H" Pulse Width	tWLPH	55	—	ns
LOAD "L" Pulse Width	tWLPL	370	—	ns
LOAD Cycle	tLCL	400	—	ns
CP→LOAD Delay Time	tCL	0	—	ns
LOAD→CP Delay Time	tLC	0	—	ns
Input Signal Rise Up Time	tr	—	30	ns
Input Signal Fall Down Time	tf	—	30	ns
FRM Data Set Up Time	tFS	100	—	ns
FRM Data Hold Time	tFH	30	—	ns

*1 CP Cycle is adjust so that FRM signal is 75Hz.

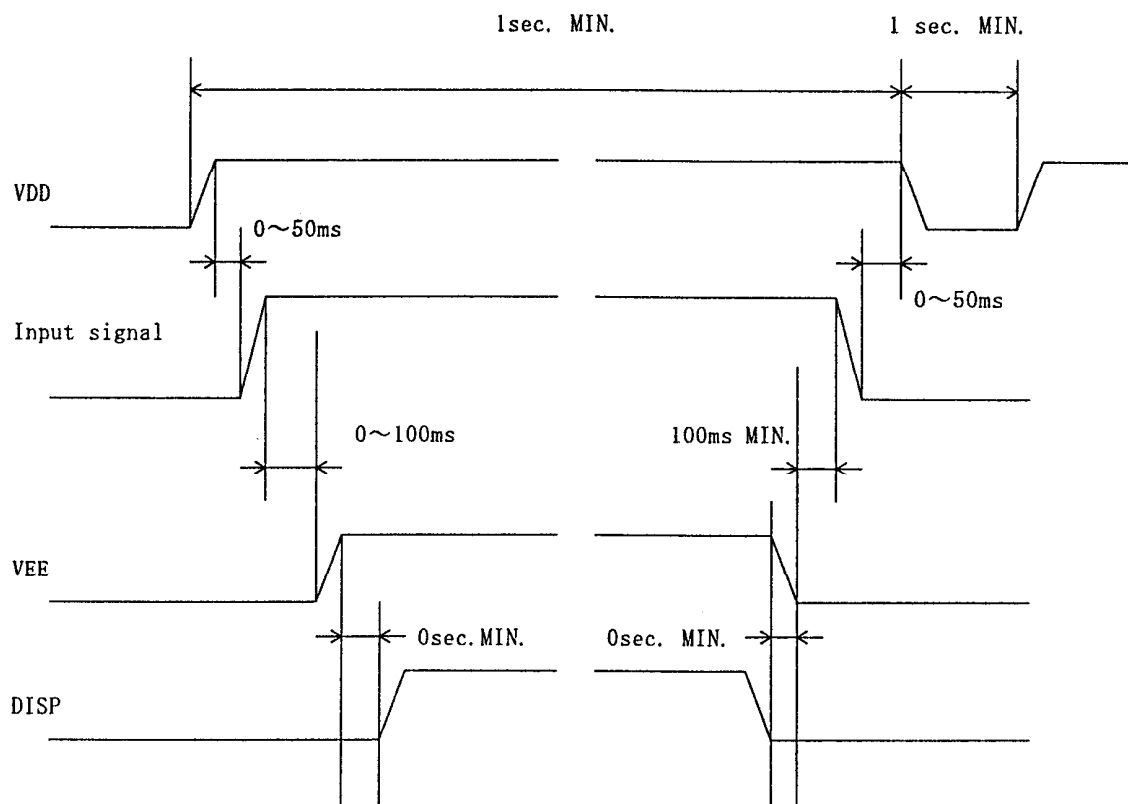
*2 The formula of condition

$$\textcircled{1} trCP, tfCP < \{tCCL - (tWCLH + tWCLL)\} / 2$$

Please use on condition that $\textcircled{1}$ is filled.

1 2. Supply Voltage Sequence Condition

DO NOT apply DC voltage to the LCD panel. DC voltage induce irreversible electrochemical reactions and reduce LCD life. Always follow the power supply ON/OFF sequence of VDD first, input signal second, VEE third and finally DISP. This will prevent DC driving of the LCD or CMOS LSI latch up as shown below.



1 3 . Backlight Characteristics

13-1. CFL ratings

Measuring Inverter:PH-BLC08-K3 (HITACHI MEDIA ELECTRONICS)

Temp. = 25℃

ITEM	SYMBOL	MIN.	TYP.	MAX.	NOTE
Starting discharge Voltage *1	VS	—	—	795 Vrms.	0 ℃
		—	—	530 Vrms.	25 ℃
Discharging tube current	IL	2.0 mArms.	5.0 mArms.	6.0 mArms.	—
Discharging tube voltage	VL	—	320 Vrms.	—	—
Operating life *2 (IL=5.0 mArms.)	T	25,000 Hr	40,000 Hr.	—	—
Operating frequency	F	30 kHz	—	100 kHz	—

*1 The Non-load output voltage (VS) of the inverter should be designed to have some margin, because VS may increase due to the leak current which may be caused by wiring of CFL cables. (Reference value: 1,035 Vrms MIN.)

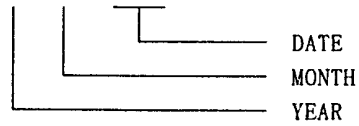
*2 When the illuminance or quantity of light has decreased to 50 % of the initial value.

*3 Do not apply more than 6.0mA discharge tube current. Because CFL maybe broken due to over current.

1 4. Lot Number Identification

The lot number shall be indicated on the back of the backlight case of each LCD.

K H S 0 5 7 Q V 1 A J - G 8 1 - □ □ - □ □



YEAR	1999	2000	2001	2002	2003	2004
CODE	1	0	1	2	3	4

MONTH	JAN.	FEB.	MAR.	APR.	MAY	JUN.
CODE	1	2	3	4	5	6

MONTH	JUL.	AUG.	SEP.	OCT.	NOV.	DEC.
CODE	7	8	9	X	Y	Z

1 5. Warranty

15-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

15-2. Production Warranty

Kyocera warrants its LCDs for a period of 12 months after receipt by the purchaser, and within the limits specified. Kyocera shall, by mutual agreement, replace or rework defective LCDs that are shown to be Kyocera's responsibility.

1 6. Precautions for use

16-1. Installation of the LCD

1. Please ground either of the mounting(screw) holes located at each corner of an LCD module, in order to stabilize brightness and display quality.
2. A transparent protection plate shall be added to protect the LCD and its polarizers.
3. The LCD shall be installed so that there is no pressure on the LSI chips.
4. The LCD shall be installed flat, without twisting or bending.
5. The display window size should be the same as the effective viewing area.
6. In case you use outside frame of effective viewing area as outward appearance of your product, unevenness of its outward appearance is out of guarantee.
7. Do not pull the CFL lead wires and do not bend the root of the wires. Housing should be designed to protect CFL lead wires from external stress.

16-2. Static Electricity

1. Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required. Operation should wear ground straps.

16-3. LCD Operation

1. The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
2. Vop must be adjusted to optimize viewing angle and contrast.
3. Operation of the LCD at temperature below the limit specified may cause image degradation and/or bubbles. It may also change the characteristics of the liquid crystal. This phenomenon may not recover. The LCD shall be operated within the temperature limits specified.

16-4. Storage

1. The LCD shall be stored within the temperature and humidity limits specified. Store in a dark area, and protected the LCD from direct sunlight or fluorescent light.
2. The LCD should be packaged to prevent damage.

16-5. Screen Surface

1. DO NOT store in a high humidity environment for extended periods. Image degradation, bubbles, and/or peeling off of polarizer may result.
2. The front polarizer is easily scratched or damaged. Prevent touching it with any hard material, and from being pushed or rubbed.
3. The LCD screen may be cleaned with a soft cloth or cotton pad. Methanol, or Isopropyl Alcohol may be used, but insure that all solvent residue is removed.
4. Water may cause damage or discoloration of the polarizer. Clean any condensation or moisture from any source immediately.
5. Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizers.

17. Reliability Data / Environmental Test

TEST ITEM	TEST CONDITION	TEST TIME	RESULT
High Temp. Atmosphere	70°C	240 Hr.	Display Quality : No defect Display Function : No defect Current Consumption : No defect
Low Temp. Atmosphere	-20°C	240 Hr.	Low Temp. Bubble : None Solid Crystallization of Liquid Crystal : None Display Quality : No defect Display Function : No defect Current Consumption : No defect
High Temp. Humidity Atmosphere	40°C 90%RH	240 Hr.	Display Quality : No defect Display Function : No defect Peel-off of Organic Sealing : None Current Consumption : No defect
Temp. Cycle	-20°C 0.5 Hr. R. T. 0.5 Hr. 70°C 0.5 Hr.	10cycles	Display Quality : No defect Display Function : No defect Peel-off of Organic Sealing : None Bubble on Cell : None
High Temp. Operation	50°C Vop	500 Hr.	Display Quality : No defect Current Consumption : No defect

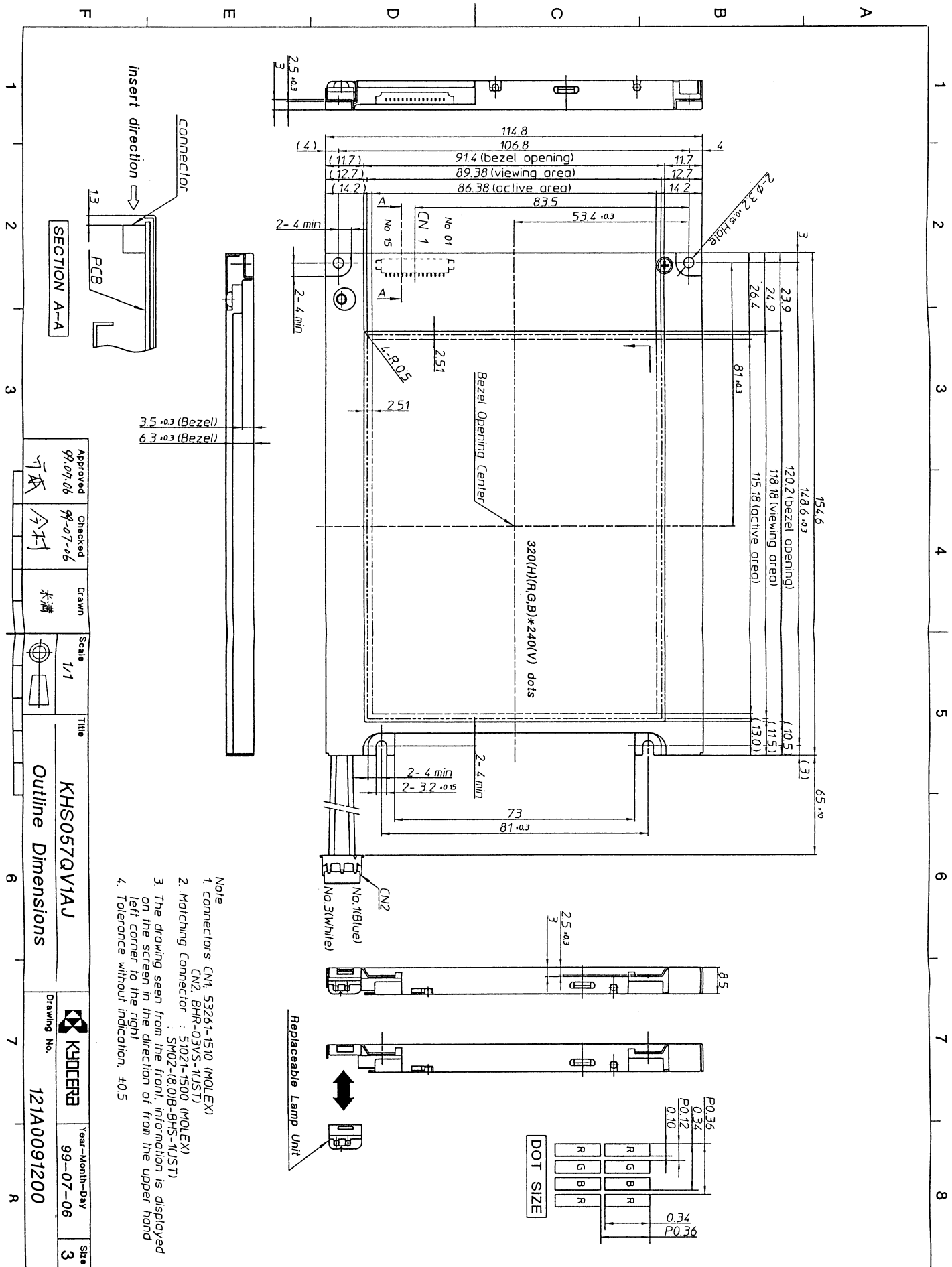
* Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

* The LCD is tested in circumstances in which there is no condensation.

* The tested LCD is inspected after 24 hours of storage at room temperature and room humidity after each test is finished.

* The reliability test is not an out-going inspection.

* The results of the reliability test are for your reference purpose only.
The reliability test is conducted only to examine the LCD's capability.



SPEC. NO.

TQ3C-8EAC0-E2AAWG17-00

DATE

July 28, 1999

FOR : _____

KYOCERA INSPECTION STANDARDTYPE : KHS057QV1AJ-G81KYOCERA CORPORATION
KAGOSHIMA HAYATO PLANT
LCD DIVISION

Original	Designed by :Engineering Dept.			Confirmed by :QA Dept.	
Issue Data	Prepared	Checked	Approved	Checked	Approved
July 28, 1999	H.Tokumori	M.Fujitani	A. Ishino	S. Hozashi	Y. Yoshida

Revision Record

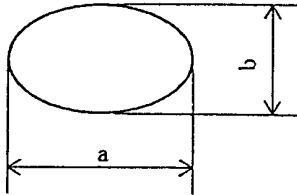
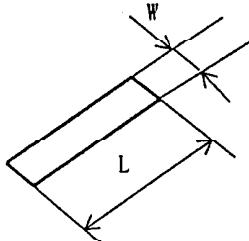
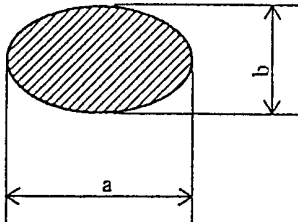
Date	Designed by : Engineering Dept.			Confirmed by : QA Dept.	
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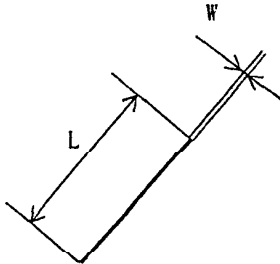
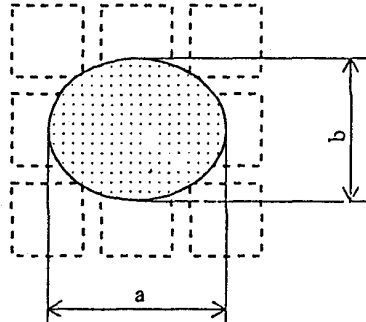
Visuals specification

1)Note

Item	Note		
General	<p>1. When defects specified in this Inspection Standards are inspected, operating voltage(Vop) shall be set at the level where optimized contrast is available. Display quality is applied up to effective viewing area. (Bi-Level INSPECTION)</p> <p>2. This inspection standard about the image quality shall be applied to any defect within the effective viewing area and shall not be applicable to outside of the area.</p> <p>3. Should any defects which are not specified in this standard happen, additional standard shall be determined by mutual agreement between customer and Kyocera.</p> <p>4. Inspection conditions</p> <p>Luminance : 500 Lux minimum Inspection distance : 300 mm (from the sample) Temperature : 25 ± 5 °C Direction : right above</p>		
Definition of Inspection item	<table><tr><td>Pinhole, Bright spot Black spot, Scratch Foreign particle</td><td>The color of a small area is different from the remainder. The phenomenon dose not change with voltage.</td></tr></table>	Pinhole, Bright spot Black spot, Scratch Foreign particle	The color of a small area is different from the remainder. The phenomenon dose not change with voltage.
	Pinhole, Bright spot Black spot, Scratch Foreign particle	The color of a small area is different from the remainder. The phenomenon dose not change with voltage.	
	<table><tr><td>Contrast variation</td><td>The color of a small area is different from the remainder. The phenomenon changes with voltage.</td></tr></table>	Contrast variation	The color of a small area is different from the remainder. The phenomenon changes with voltage.
Contrast variation	The color of a small area is different from the remainder. The phenomenon changes with voltage.		
<table><tr><td>Polarizer (Scratch, Bubble, Dent)</td><td>Scratch, Bubble and Dent in the polarizer which can be observed in on / off state.</td></tr></table>	Polarizer (Scratch, Bubble, Dent)	Scratch, Bubble and Dent in the polarizer which can be observed in on / off state.	
Polarizer (Scratch, Bubble, Dent)	Scratch, Bubble and Dent in the polarizer which can be observed in on / off state.		

2)Standard

Inspection item	Judgement standard																						
Pinhole, Bright spot Black spot, Foreign particle	<div></div> <div>$d = (a + b) / 2$</div> <table><tr><th>Category</th><th>Size (mm)</th><th>Acceptable number</th></tr><tr><td>A</td><td>$d \leq 0.2$</td><td>neglected</td></tr><tr><td>B</td><td>$0.2 < d \leq 0.3$</td><td>5</td></tr><tr><td>C</td><td>$0.3 < d \leq 0.5$</td><td>3</td></tr><tr><td>D</td><td>$0.5 < d$</td><td>0</td></tr></table>	Category	Size (mm)	Acceptable number	A	$d \leq 0.2$	neglected	B	$0.2 < d \leq 0.3$	5	C	$0.3 < d \leq 0.5$	3	D	$0.5 < d$	0							
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Scratch, Foreign particle	<div></div> <table><tr><th></th><th>Width (mm)</th><th>Length (mm)</th><th>Acceptable No.</th></tr><tr><td>A</td><td>$W \leq 0.03$</td><td>—</td><td>neglected</td></tr><tr><td>B</td><td rowspan="3">$0.03 < W \leq 0.1$</td><td>$L \leq 2.0$</td><td>neglected</td></tr><tr><td>C</td><td>$2.0 < L \leq 4.0$</td><td>3</td></tr><tr><td>D</td><td>$4.0 < L$</td><td>0</td></tr><tr><td>E</td><td>$0.1 < W$</td><td>—</td><td>According to 'Circular'</td></tr></table>		Width (mm)	Length (mm)	Acceptable No.	A	$W \leq 0.03$	—	neglected	B	$0.03 < W \leq 0.1$	$L \leq 2.0$	neglected	C	$2.0 < L \leq 4.0$	3	D	$4.0 < L$	0	E	$0.1 < W$	—	According to 'Circular'
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Contrast variation	<div></div> <div>$d = (a + b) / 2$</div> <table><tr><th>Category</th><th>Size (mm)</th><th>Acceptable number</th></tr><tr><td>A</td><td>$d \leq 0.5$</td><td>neglected</td></tr><tr><td>B</td><td>$0.5 < d \leq 0.7$</td><td>3</td></tr><tr><td>C</td><td>$0.7 < d$</td><td>0</td></tr></table>	Category	Size (mm)	Acceptable number	A	$d \leq 0.5$	neglected	B	$0.5 < d \leq 0.7$	3	C	$0.7 < d$	0										
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Inspection item	Judgement standard																		
Polarizer (Scratch, Bubble, Dent)	(1) Scratch 																		
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	(2)Bubble (dent)  $d = (a + b) / 2$ <table><tr><th>Category</th><th>Size (mm)</th><th>Acceptable number</th></tr><tr><td>A</td><td>$d \leq 0.2$</td><td>neglected</td></tr><tr><td>B</td><td>$0.2 < d \leq 0.3$</td><td>5</td></tr><tr><td>C</td><td>$0.3 < d \leq 0.5$</td><td>3</td></tr><tr><td>D</td><td>$0.5 < d$</td><td>0</td></tr></table>	Category	Size (mm)	Acceptable number	A	$d \leq 0.2$	neglected	B	$0.2 < d \leq 0.3$	5	C	$0.3 < d \leq 0.5$	3	D	$0.5 < d$	0			
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