SPEC. NO.	TQ3C-8EACO-E1AAWQ01-00
DATE	June 10, 1999

SPEC

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<u>TYPE: KCS043EV1AA-G03</u>

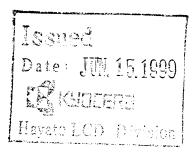
CONTENTS

- 1. Application
- 2. Construction and Outline
- 3. Mechanical Specifications
- 4. Absolute Maximum Ratings
- 5. Electrical Characteristics
- 6. Optical Characteristics
- 7. Circuit Block Diagram
- 8. Interface Signals
- 9. Interface Timing Chart
- 10. Data and Screen
- 11. Input Timing Characteristics
- 12. Supply Voltage Sequence Condition
- 13. Recommended additional circuit
- 14. Backlight Characteristics
- 15. Lot Number Identification
- 16. Warranty
- 17. Precaution for Use
- 18. Reliability Data / Environmental Test
- 19. Outline Drawing



This specification is subject to change without notice. Consult Kyocera before ordering.

Original	Designed by	:Engineerin	Confirmed by :QA Dept		
Issue Data	Prepared	Checked	Approved	Checked	Approved
June 10, 1999	K. Shimizu	4 masumor	O Who ho	S:/Hayash	Je Joshde
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Revision Record

Date	Designed by	y :Engineering	Confirmed by :QA Dept.			
	Prepared	Check	Checked Approved		Checked	Approved
Rev. No.	Date	Page		De	scriptions	

1. Application

This data sheet defines the specification for a $(320\times3)\times120$ dot, STN color dot matrix type Liquid Crystal Display with CFL backlight.

2. Construction and Outline

 $(320\times3)\times120$ dots, COB type LCD with CFL backlight.

Backlight system : Side-edge type CFL (1 tube).

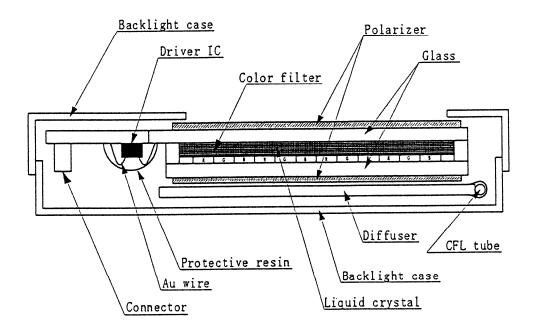
Inverter : Option.

Recommended Inverter : PH-BLC08-K3 (HITACHI MEDIA ELECTRONICS)

or equivalent.

Polarizer : Glare treatment.

Additional circuit : None



3. Mechanical Specifications

ITEM	SPECIFICATION	UNIT
Outline dimensions	124.8 (W) × 63.2 (H) × 9.9 (D)	mm
Effective viewing area	103.74 (W) × 43.98 (H)	mm
Dot number	(320×3) (W) × 120 (H)	Dots
Dot size	0.086 (W) × 0.33 (H)	mm
Dot pitch	0.106 (W) × 0.35 (H)	mm
Display color *1	White *2	_
Base color *1	Black *2	_
Weight	(98)	g

- *1 Due to the characteristics of the LC material, the color vary with environmental temperature.
- *2 Negative-type display

Display data "H" :R,G,B Dots ON : White Display data "L" :R,G,B Dots OFF : Black

4. Absolute Maximum Ratings

4-1 Electrical absolute maximum ratings

Temp. = 25 ℃

ITEM	SYMBOL	MIN.	MAX.	UNIT
Supply voltage for logic	VDD	0	7. 0	V
Supply voltage for LCD driving	V0-V5	0	45. 0	V
Input voltage	Vin	0	VDD	V

4-2 Environmental absolute maximum ratings

ITEM		SYMBOL	MIN	MAX	UNIT
Operating temperature	*1	Тор	0	60	Ç
Storage temperature	*2	Тѕто	-20	60	Ç
Operating humidity	*3	Нор	10	*4	%RH
Storage humidity	*3	Нято	10	*4	%RH
Vibration		_	* 5	* 5	_
Shock		_	*6	*6	_

- *1 LCD's display quality shall not be guaranteed at the temperature range of : below 0°C and upper 40°C.
- *2 Tëmp. = -20° C < 24Hr. , Temp = 60° C < 24Hr. No vibration and shock.
- *3 Non-condensation.
- *4 Temp. \leq 40°C, 85% RH Max. Temp. > 40°C, Absolute Humidity shall be less than 85%RH at 40°C.

*****5

Frequency	10~55 Hz	Converted to acceleration value :
Vibration width	0.15 mm	(0.03~0.91G)
Interval	10-55-10 Hz	l minute

2 hours in each direction X/Y/Z (6 hours as total) EIAJ ED-2531

*6 Acceleration: 50 G
Pulse width: 11 msec

3 times in each direction : $\pm X/\pm Y/\pm Z$.

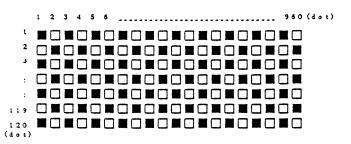
EIAJ ED-2531

5. Electrical Characteristics

Temp. = 25° C, VDD = $5V \pm 5\%$

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Supply voltage for logic	ODV		4. 75	5. 00	5. 25	٧
LCD driving voltage *1	Vop=	0 ℃	(17. 3)	(18.3)	(19.3)	٧
	V0-V5	25 ℃	(16.7)	(17.7)	(18.7)	V
		40 ℃	(15.7)	(16.7)	(17.7)	٧
Input voltage	Vin	"H" level	0. 8VDD	-	VDD	V
		"L" level	0		0. 2VDD	٧
Clock frequency	f cp		1.01	1.08	12.0	MHz
Frame frequency *2	f FRM		70	75	80	Hz
Randomizing frequency *3	for		300		800	
Current consumption for logic	IDD		_	TBD	TBD	mA
Current consumption for LCD driving	IEE	*4	_	TBD	TBD	
Power consumption	Pdisp		-	TBD	TBD	wm

- *1 Maximum contrast ratio is obtained by adjusting the LCD supply voltage (Vop= VO-V5) for driving LCD.
- *2 In consideration of display quality, it is recommended that frame frequency is set in the range of 70-80Hz. When you have to use higher frame and clock frequencies, confirm the LCD's performance and quality prior to finalizing the frequency values: Generally, as frame and clock frequencies become higher, current consumption will get bigger and display quality will be degraded.
- *3 It is recommended that randomizing frequency be set in the range of 300-800Hz. At finalizing the frequency, confirm with actual tests that phenomena like flickering and/or horizontal lines do not appear on screen.
- *4 Display high frequency pattern, (see below). VDD = 5.0V , Vop = VO-V5 , f_{FRM} = 75 Hz , fcp = 1.08MHz Pattern:



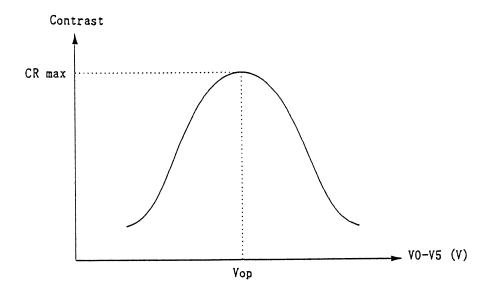
6. Optical Characteristics

Measuring Spot = ϕ 6mm , Temp. = 25°C

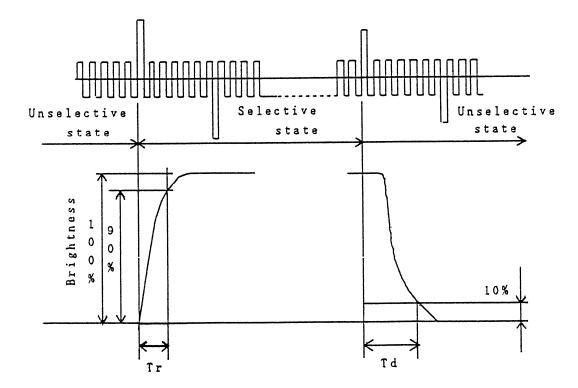
ITEM		SYMBOL	MBOL CONDITION		MIN.	TYP.	MAX.	UNIT
Response	onse time Down Td $\theta = \phi = 0^{\circ}$		∌ =0°	_	(220)	(320)	ms	
time			∌ =0°	-	(110)	(210)	ns	
Viewing angl	e range	θ	CR≧2	φ =0°	(-35)	_	(55)	deg.
		φ	UK ≦ Z	θ =0°	(-55)	_	(45)	deg.
Contrast rat	io	CR	$\theta = \phi = 0^{\circ}$		(20.0)	(40.0)	_	
Brightness ((I1=5mA)	L			(140)	(200)	_	cd/m³
Chromaticity coordinates	Red	х	$\theta = \phi = 0^{\circ}$		(0. 49)	(0.54)	(0.59)	
coordinates	pordinates y		0 - 0	θ-φ-0	(0.30)	(0.35)	(0.40)	
	Green	х	$\theta = \phi = 0^{\circ}$		(0.26)	(0.31)	(0.36)	
		у			(0.47)	(0.52)	(0.57)	
	Blue	х	θ = φ =0°		(0.11)	(0. 16)	(0.21)	
		у			(0.11)	(0. 16)	(0.21)	_
	White	x	0 -	6 =0°	(0.27)	(0.32)	(0.37)	
		у	θ = q	p ÷U	(0.30)	(0.35)	(0.40)	
	Black	х	۵ – ۵	6 =0°	(0.24)	(0.29)	(0.34)	
	DIACK	y	<i>U</i> – <i>q</i>		(0. 25)	(0.30)	(0.35)	

Optimum contrast is obtained by adjusting the LCD driving voltage(Vop) while at the viewing angle of θ = ϕ = 0°.

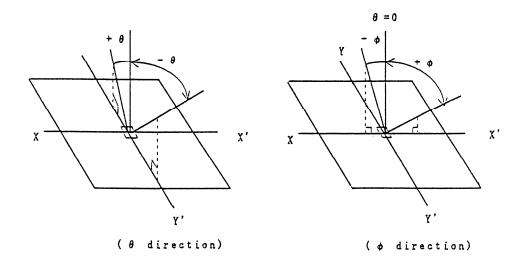
6-1. Definition of Vop



6-2. Definition of response time

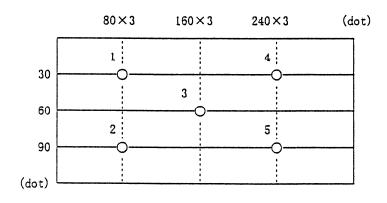


6-3. Definition of viewing angle

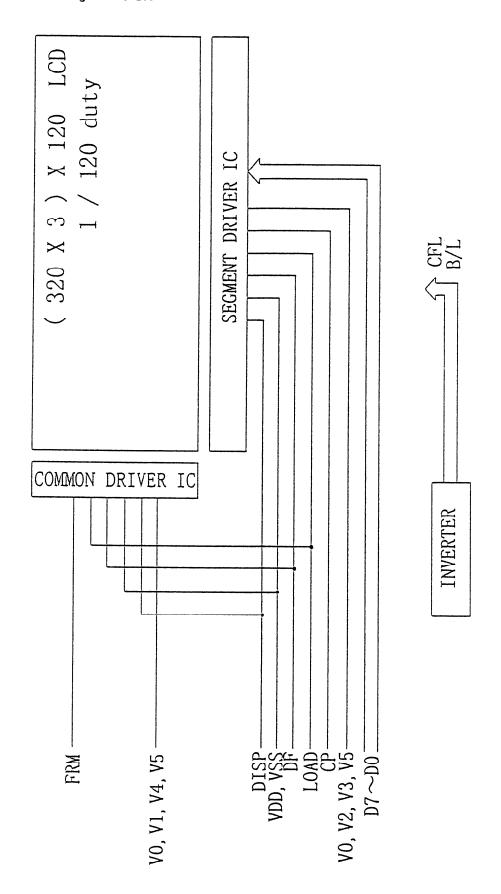


6-4 Contrast ratio is defined as follows:

6-5. Measuring points



- 1) Rating is defined as the average brightness inside the viewing area.
- 2) 30 minutes after CFL is turned on. (Ambient Temp. =25°C)
- 3) The inverter should meet the eccentric conditions;-Sine, symmetric waveform without spike in positive and negative.



8. Interface signals

8-1. LCD

CN1 : 6214-022-010 (ELCO)

PIN NO.	SYMBOL	DESCRIPTION	LEVEL
1	FRM	Synchronous signal for driving scanning line	Н
2	LOAD	Data signal latch clock	H → L
3	CP	Data signal shift clock	H → L
4	DISP	Display control signal	H(ON), L(OFF)
5	VDD	Power supply for logic	
6	VDD	Power supply for logic	
7	VSS	GND	
8	DF	AC signal for driving	
9	VO	*	
10	V1	*	
11	V2	*	
12	V3	*	
13	V4	*	
14	V5	*	
15	DO DO		
16	D1	Display data	
17	D2		
18	D3		H(ON),L(OFF)
19	D4		
20	D5		
21	D6		
22	D7		

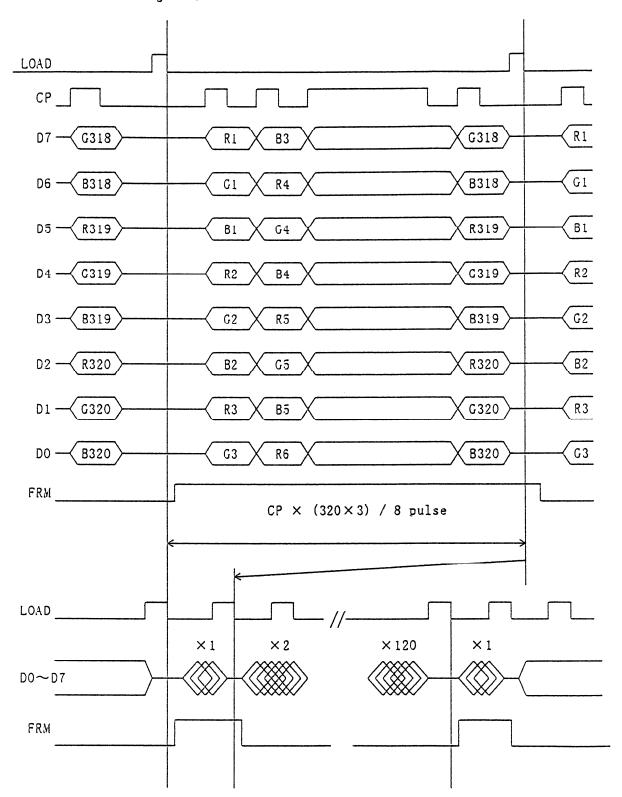
Recommended matcing cable for LCD side connector : FPC or FFC(P=0.5mm) * $V0 \sim V5$: Refer 13. Recommended additional circuit.

8-2. CFL

PIN No	SYMBOL	DESCRIPTION	LEVEL
1	HV	Power supply for CFL	AC
2	NC	-	
3	GND	Ground line(from inverter)	-

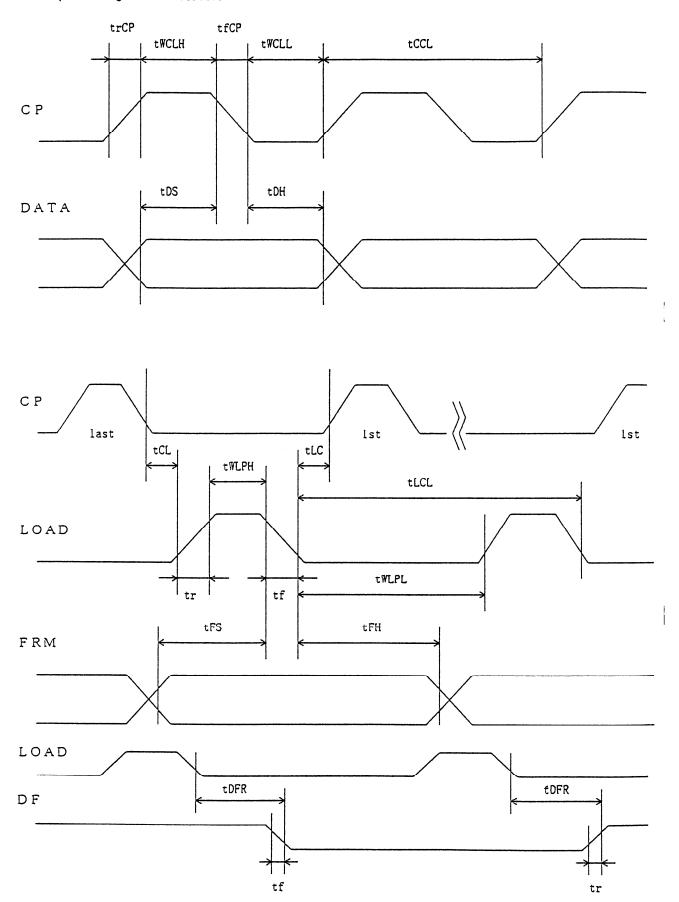
LCD side connector : BHR-03VS-1 (JST) Recommended matching connector : SM02-(0.8)B-BHS-1 (JST)

9. Interface Timing Chart



10. Data and Screen

	v.		YI			Y2			Y3			Y320	
C H I P	X1	D7 RI	D6 G1	D5 B1	D4 R2	D3 G2	D2 B2	D L	G3	D7 83	D2 R320	D1 G320	DO 8320
R E A	X120					нір	S	ARE	A	olice i de disposa	10 da 10		



Input characteristics ; VDD = $+5.0V \pm 5\%$

Temp. = 25℃

ITEM	SYMBOL	MIN.	MAX.	UNIT
CP Cycle *1	tCCL	82	-	ns
CP "H" Pulse Width	tWCLH	28	_	ns
CP "L" Pulse Width	tWCLL	28	_	ns
CP Rise Up Time	trCP	_	50	ns
CP Fall Down Time	t fCP	_	50	ns
Data Set Up Time	tDS	28	-	ns
Data Hold Time	tDH	20		ns
Load "H" Pulse Width	t₩LPH	55	_	ns
Load "L" Pulse Width	tWLPL	370	_	ns
LOAD Cycle	tLCL	400	-	ns
LOAD Signal Hold Time	tHF	25	_	ns
Data Strobe Set Up Time	tSL	0	-	ns
Data Strobe Hold Time	tH	40	_	ns
Input Signal Rise Up Time *2	tr	_	30	ns
Input Signal Fall Down Time *2	tf	-	30	ns
FRM Data Set Up Time	tFS	200	-	ns
FRM Data Hold Time	tFH	30		ns
DF Delay Time	tDFR	0	300	ns

^{*1} CP Cycle is adjust so that FRM signal is 240Hz.

Please use on condition that ①, ② are filled.

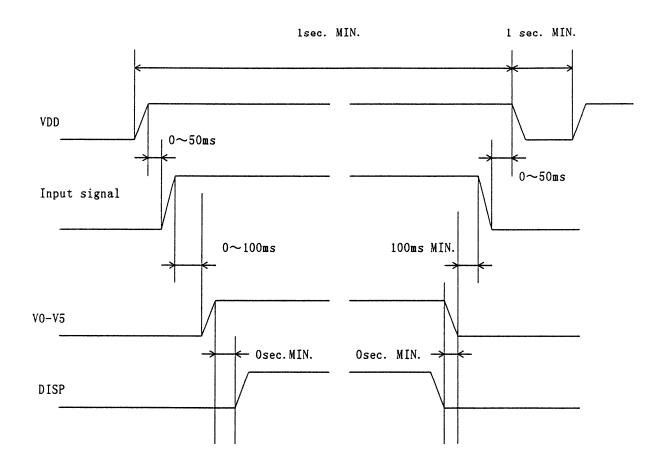
^{*2} The formula of condition

① tr,tf < {tCCL - (tWCLH + tWCLL)} / 2

 $[\]bigcirc$ tr, tf \leq 50 (ns)

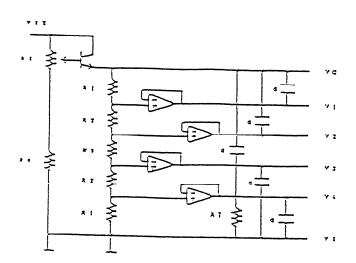
12. Supply Voltage Sequence Condition

<u>DO NOT</u> apply DC voltage to the LCD panel. DC voltage induce irreversible electrochemical reactions and reduce LCD life. Always follow the power supply ON/OFF sequence of VDD first, input signal second, VO-V5 third and finally DISP. This will prevent DC driving of the LCD or CMOS LSI latch up as shown below.



13. Recommended additional circuit

a) Recommended Bias Voltage for Driving LCD



 $\begin{array}{lll} R1 &=& 8.1 \, \mathrm{k}\, \Omega & \pm & 1\% \\ R2 &=& 8.1 \, \mathrm{k}\, \Omega & \pm & 1\% \\ R3 &=& 67.5 \, \mathrm{k}\, \Omega & \pm & 5\% \\ R7 &=& 15.0 \, \mathrm{k}\, \Omega & \pm & 5\% \\ C &=& 3.3 \!\sim\! 10.0 \ \mu \, \mathrm{F} \end{array}$

These value above are theoreticaly calculated. Fine tuning might be required in some cases.

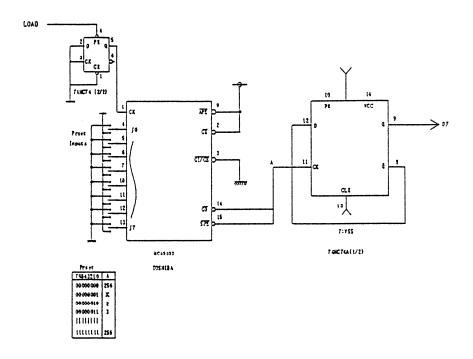
For fine tuning value of the resistor shall be adjusted to conform with the follwing equation.

|V0-V1| = |V1-V2| = |V3-V4| = |V4-V5|

R5, R6 to be abjusted for optimum readability.

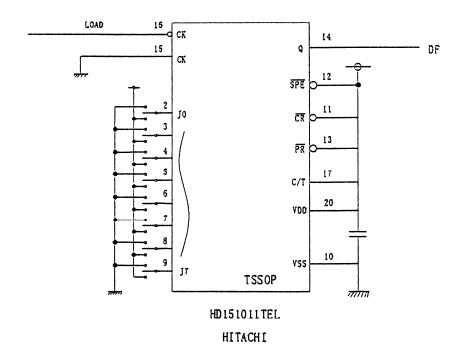
b) Example of Randomizing Circuit
Randomizing circuit is recommended in order to reduce cross-talk" phenomenun
of displayed images. (Tailing, vertical strips, etc.)
Its detail is subject to change according to actual operating condition and
application of the LCD. Should you have any question when installing an actual
randomizing circuit, please ask Kyocera for details.

b-1)



There may be flickering on screen according to some LOAD signal frequencies. If in happens, set PRESET INPUTS terminals to select optimized conditions.

b-2)



1 4 . Backlight Characteristics

14-1. CFL ratings

Measuring Inverter:PH-BLC08-K3 (HITACHI MEDIA ELECTRONICS)

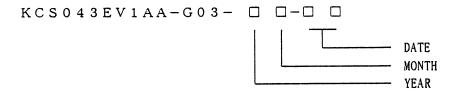
Temp. = 25℃

ITEM	SYMBOL	MIN.	TYP.	MAX.	NOTE
Starting discharge Voltage	VS		_	(770) Vrms.	0 ℃
*1	V 5	_	_	(510) Vrms.	25 ℃
Discharging tube current	IL	(2.0)mArms.	(5.0) mArms.	(6.0) mArms.	-
Discharging tube voltage	VL	_	(315) Vrms.	_	_
Operating life *2 (IL=5.0 mArms.)	Т	(25,000) Hr.	(40,000) Hr.		_
Operating frequency	F	(30) kHz	_	(100) kHz	-

- *1 The Non-load output voltage (VS) of the inverter should be designed to have some margin, because VS may increase due to the leak current which may be caused by wiring of CFL cables. (Reference value: 1000 Vrms MIN.)
- *2 When the illuminance or quantity of light has decreased to 50 % of the initial value.

15. Lot Number Identification

The lot number shall be indicated on the back of the backlight case of each LCD.



YEAR	1999	2000	2001	2002	2003
CODE	9	0	1	2	3

MONTH	JAN.	FEB.	MAR.	APR.	MAY	JUN.
CODE	1	2	3	4	5	6

MONTH	JUL.	AUG.	SEP.	ост.	NOV.	DEC.
CODE	7	8	9	х	Y	Z

16. Warranty

16-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

16-2. Production Warranty

Kyocera warrants its LCDs for a period of 12 months after receipt by the purchaser, and within the limits specified. Kyocera shall, by mutual agreement, replace or rework defective LCDs that are shown to be Kyocera's responsibility.

17. Precautions for use

17-1. Installation of the LCD

- 1. A transparent protection plate shall be added to protect the LCD and its polarizers.
- 2. The LCD shall be installed so that there is no pressure on the LSI chips.
- 3. The LCD shall be installed flat, without twisting or bending.
- 4. The display window size should be the same as the effective viewing area.
- 5. In case you use outside frame of effective viewing area as outward appearance of your product, unevenness of its outward appearance is out of guarantee.
- Do not pull the CFL lead wires and do not bend the root of the wires.
 Housing should be designed to protect CFL lead wires from external stress.

17-2. Static Electricity

1. Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required. Operation should wear ground straps.

17-3. LCD Operation

- 1. The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
- 2. Vop must be adjusted to optimize viewing angle and contrast.
- 3. Operation of the LCD at temperature below the limit specified may cause image degradation and/or bubbles. It may also change the characteristics of the liquid crystal. <u>This phenomenon may not recover.</u> The LCD shall be operated within the temperature limits specified.

17-4. Storage

- The LCD shall be stored within the temperature and humidity limits specified.
 Store in a dark area, and protected the LCD from direct sunlight or fluorescent light.
- 2. The LCD should be packaged to prevent damage.

17-5. Screen Surface

- DO NOT store in a high humidity environment for extended periods. Image degradation, bubbles, and/or peeling off of polarizer may result.
- 2. The front polarizer is easily scratched or damaged. Prevent touching it with any hard material, and from being pushed or rubbed.
- 3. The LCD screen may be cleaned with a soft cloth or cotton pad. Methanol, or Isopropyl Alcohol may be used, but insure that all solvent residue is removed.
- 4. Water may cause damage or discoloration of the polarizer. Clean any condensation or moisture from any source immediately.
- 5. Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizers.

18. Reliability Data / Environmental Test

TEST ITEM	TEST CONDITION	TEST TIME	RESULT
High Temp. Atmosphere	70℃	240 Hr.	Display Quality : No defect Display Function : No defect Current Consumption : No defect
Low Temp. Atmosphere	-20°C	240 Hr.	Low Temp. Bubble : None Solid Crystallization of Liquid Crystal : None Display Quality : No defect Display Function : No defect Current Consumption : No defect
High Temp. Humidity Atmosphere	40℃ 90%RH	240 Hr.	Display Quality : No defect Display Function : No defect Peel-off of Organic Sealing : None Current Consumption : No defect
Temp. Cycle	-20℃ 0.5 Hr. R.T. 0.5 Hr. 70℃ 0.5 Hr.		Display Quality : No defect Display Function : No defect Peel-off of Organic Sealing : None Bubble on Cell : None
High Temp. Operation	50℃ Vop	500 Hr.	Display Quality : No defect Current Consumption : No defect

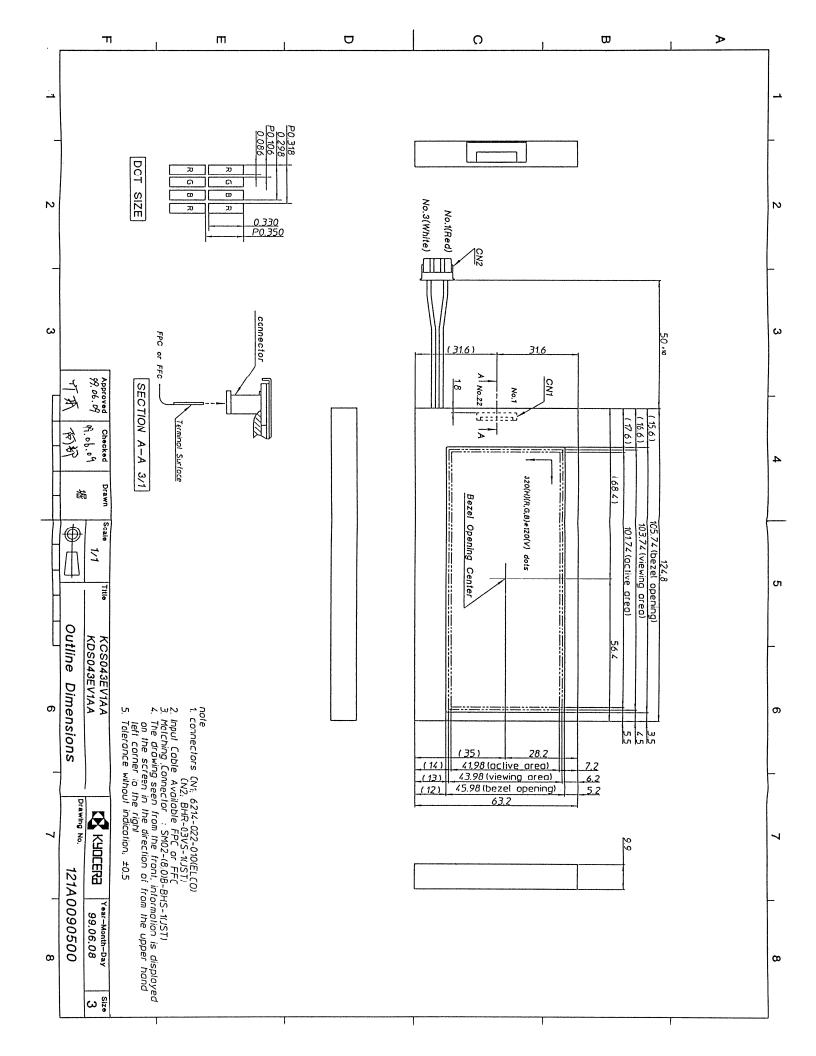
^{*} Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

st The LCD is tested in circumstances in which there is no condensation.

^{*} The tested LCD is inspected after 24 hours of storage at room temperature and room humidity after each test is finished.

^{*} The reliability test is not an out-going inspection.

^{*} The result of the reliability test are for your reference purpose only. The reliability test is conducted only to examine the LCD's capability.



SPEC. NO.	TQ3C-8EACO-E2AAWQ01-00				
DATE	June 10, 1999				

FOR	:
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KYOCERA INSPECTION STANDARD

<u>TYPE : KCS043EV1AA-G03</u>

KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

Original	Designed by	:Engineerin	Confirmed by :QA Dept.		
Issue Data	Prepared	Checked	Approved	Checked	Approved
June 10, 1999	A Shimizu	4. Marsamon	When his	S. Hayosh.	Jejoshida
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Revision Record

Date	Designed by	y :Engineering	Confirmed by :QA Dept.			
	Prepared	Check	Checked Approve		Checked	Approved
Rev. No.	Date	Page		De	scriptions	

1)Note

Items		Note				
General	1. When defects specified in this Inspection Standards are inspected, operating voltage(Vop) shall be set at the level where optimized contrast is available. Display quality is applied up to effective viewing area. (Bi-Level INSPECTION)					
	applied to any defect w	rd about the image quality shall be within the effective viewing area cable to outside of the area.				
	standard happen, additi	ch are not specified in this ional standard shall be determined tween customer and Kyocera.				
	4. Inspection conditions					
	Luminance : 500 Lux minimum . Inspection distance : 300 mm (from the sample) Temperature : 25 \pm 5 $^{\circ}$ C Direction : right above					
Definition of Inspection item	Pinhole, Bright spot Black spot, Scratch Foreign particle	The color of a small area is different from the remainder. The phenomenon dose not change with voltage.				
	Contrast variation	The color of a small area is different from the remainder. The phenomenon changes with voltage.				
	Polarizer (Scratch, Bubble, Dent)	Scratch, Bubble and Dent in the polarizer which can be observed in on / off state.				

Inspection item	Judge	ment standard				
Pinhole, Bright spot Black spot, Foreign particle	a	d = (a + 1	b) / 2			
	Category Size (mm) Acceptab	le number			
		0.2 neg	neglected			
	B 0.2 < d ≦	0.3	5			
	C 0.3 < d ≦	0.5	3			
	D 0.5 < d		0			
Scratch, Foreign particle		W				
	Width (mm)	Length (mm)	Acceptable No.			
	A ₩ ≦ 0.03	 L ≦ 2.0	neglected neglected			
	$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	$0.0 < L \leq 4.0$	a legiected			
	<u></u>	< L = 4.0	0			
	E 0.1 < W		According to Circular			
Contrast variation	$ \begin{array}{c cccc} \hline Category & Size (mm) \\ \hline A & d & \leq \\ \hline B & 0.5 < d & \leq \end{array} $	0.5 neg	b) / 2 le number lected 3			
	C 0.7 < d		0			

Inspection item	Judgement standard							
Polarizer (Scratch, Bubble, Dent)	(1) Scratch							
	Widt	Width (mm) Lengt		gth (mm)	Acceptable No.			
	A	₩ ≦ 0.1	-		neglected			
	B 0.1 <	< ₩ ≦0.3		દ≦ 5.0	neglected			
	c		5.0 <	. L	0			
	D 0.3	< \$7			0			
	(2) Bubble (dent) d = (a + b) / 2							
	Category	Size	(mm)	Acceptab	le number			
	A	d	≦ 0.2	neg	lected			
	В	0.2 < d	≤ 0.3		5			
	С	0.3 < d	≦ 0.5		3			
	D	0.5 < d			0			