International
IOR Rectifier

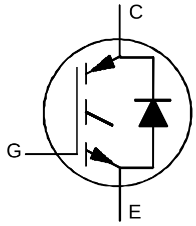
IRGP430UD2

INSULATED GATE BIPOLAR TRANSISTOR
WITH ULTRAFAST SOFT RECOVERY DIODE

UltraFast CoPack IGBT

Features

- Switching-loss rating includes all "tail" losses
- HEXFRED™ soft ultrafast diodes
- Optimized for high operating frequency (over 5kHz)
  See Fig. 1 for Current vs. Frequency curve



n-channel

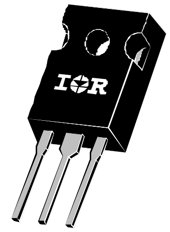
$V_{CES} = 500V$

$V_{CE(sat)} \leq 3.0V$

$@V_{GE} = 15V, I_C = 15A$

Description

Co-packaged IGBTs are a natural extension of International Rectifier's well known IGBT line. They provide the convenience of an IGBT and an ultrafast recovery diode in one package, resulting in substantial benefits to a host of high-voltage, high-current, motor control, UPS and power supply applications.



TO-247AC

Absolute Maximum Ratings

	Parameter	Max.	Units
V_{CES}	Collector-to-Emitter Voltage	500	V
I_C @ T_C = 25°C	Continuous Collector Current	25	A
I_C @ T_C = 100°C	Continuous Collector Current	15	
I_{CM}	Pulsed Collector Current ①	50	
I_{LM}	Clamped Inductive Load Current ②	50	
I_F @ T_C = 100°C	Diode Continuous Forward Current	12	
I_{FM}	Diode Maximum Forward Current	50	
V_{GE}	Gate-to-Emitter Voltage	± 20	V
P_D @ T_C = 25°C	Maximum Power Dissipation	100	W
P_D @ T_C = 100°C	Maximum Power Dissipation	42	
T_J T_{STG}	Operating Junction and Storage Temperature Range	-55 to +150	°C
	Soldering Temperature, for 10 sec.	300 (0.063 in. (1.6mm) from case)	
	Mounting Torque, 6-32 or M3 Screw.	10 lbf•in (1.1 N•m)	

Thermal Resistance

	Parameter	Min.	Typ.	Max.	Units
$R_{\theta JC}$	Junction-to-Case - IGBT	—	—	1.2	°C/W
$R_{\theta JC}$	Junction-to-Case - Diode	—	—	2.5	
$R_{\theta CS}$	Case-to-Sink, flat, greased surface	—	0.24	—	
$R_{\theta JA}$	Junction-to-Ambient, typical socket mount	—	—	40	
Wt	Weight	—	6 (0.21)	—	g (oz)

Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(BR)CES}$	Collector-to-Emitter Breakdown Voltage ③	500	—	—	V	V_{GE} = 0V, I_C = 250µA
$\Delta V_{(BR)CES}/\Delta T_J$	Temp. Coeff. of Breakdown Voltage	—	0.46	—	V/°C	V_{GE} = 0V, I_C = 1.0mA
$V_{CE(on)}$	Collector-to-Emitter Saturation Voltage	—	2.3	3.0	V	I_C = 15A V_{GE} = 15V
		—	2.8	—		I_C = 25A See Fig. 2, 5
		—	2.6	—		I_C = 15A, T_J = 150°C
$V_{GE(th)}$	Gate Threshold Voltage	3.0	—	5.5		V_{CE} = V_{GE}, I_C = 250µA
$\Delta V_{GE(th)}/\Delta T_J$	Temp. Coeff. of Threshold Voltage	—	-11	—	mV/°C	V_{CE} = V_{GE}, I_C = 250µA
g_{fe}	Forward Transconductance ④	2.3	8.1	—	S	V_{CE} = 100V, I_C = 15A
I_{CES}	Zero Gate Voltage Collector Current	—	—	250	µA	V_{GE} = 0V, V_{CE} = 500V
		—	—	2500		V_{GE} = 0V, V_{CE} = 500V, T_J = 150°C
V_{FM}	Diode Forward Voltage Drop	—	1.4	1.7	V	I_C = 12A See Fig. 13
		—	1.3	1.6		I_C = 12A, T_J = 150°C
I_{GES}	Gate-to-Emitter Leakage Current	—	—	±100	nA	V_{GE} = ±20V

Switching Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions	
Q_g	Total Gate Charge (turn-on)	—	31	47	nC	I_C = 15A V_{CC} = 400V See Fig. 8	
Q_{ge}	Gate - Emitter Charge (turn-on)	—	6.2	9.2			
Q_{gc}	Gate - Collector Charge (turn-on)	—	12	19			
$t_{d(on)}$	Turn-On Delay Time	—	73	—	ns	T_J = 25°C I_C = 15A, V_{CC} = 400V V_{GE} = 15V, R_G = 23Ω Energy losses include "tail" and diode reverse recovery. See Fig. 9, 10, 11, 18	
t_r	Rise Time	—	72	—			
$t_{d(off)}$	Turn-Off Delay Time	—	120	180			
t_f	Fall Time	—	100	150			
E_{on}	Turn-On Switching Loss	—	0.7	—	mJ		
E_{off}	Turn-Off Switching Loss	—	0.4	—			
E_{ts}	Total Switching Loss	—	1.1	1.7			
$t_{d(on)}$	Turn-On Delay Time	—	77	—	ns	T_J = 150°C, See Fig. 9, 10, 11, 18 I_C = 15A, V_{CC} = 400V V_{GE} = 15V, R_G = 23Ω Energy losses include "tail" and diode reverse recovery.	
t_r	Rise Time	—	75	—			
$t_{d(off)}$	Turn-Off Delay Time	—	200	—			
t_f	Fall Time	—	190	—			
E_{ts}	Total Switching Loss	—	1.5	—	mJ		
L_E	Internal Emitter Inductance	—	13	—	nH	Measured 5mm from package	
C_{ies}	Input Capacitance	—	660	—	pF	V_{GE} = 0V V_{CC} = 30V See Fig. 7 ƒ = 1.0MHz	
C_{oes}	Output Capacitance	—	110	—			
C_{res}	Reverse Transfer Capacitance	—	12	—			
t_{rr}	Diode Reverse Recovery Time	—	42	60	ns	T_J = 25°C See Fig.	I_F = 12A V_R = 200V di/dt = 200A/µs
		—	80	120		T_J = 125°C 14	
I_{rr}	Diode Peak Reverse Recovery Current	—	3.5	6.0	A	T_J = 25°C See Fig.	
		—	5.6	10		T_J = 125°C 15	
Q_{rr}	Diode Reverse Recovery Charge	—	80	180	nC	T_J = 25°C See Fig.	
		—	220	600		T_J = 125°C 16	
$di_{(rec)M}/dt$	Diode Peak Rate of Fall of Recovery	—	180	—	A/µs	T_J = 25°C See Fig.	
	During t_b	—	116	—		T_J = 125°C 17	

Notes:

① Repetitive rating; V_{GE}=20V, pulse width limited by max. junction temperature. (See fig. 20)

② V_{CC}=80%(V_{CES}), V_{GE}=20V, L=10µH, R_G= 23Ω, (See fig. 19)

③ Pulse width ≤ 80µs; duty factor ≤ 0.1%.

④ Pulse width 5.0µs, single shot.

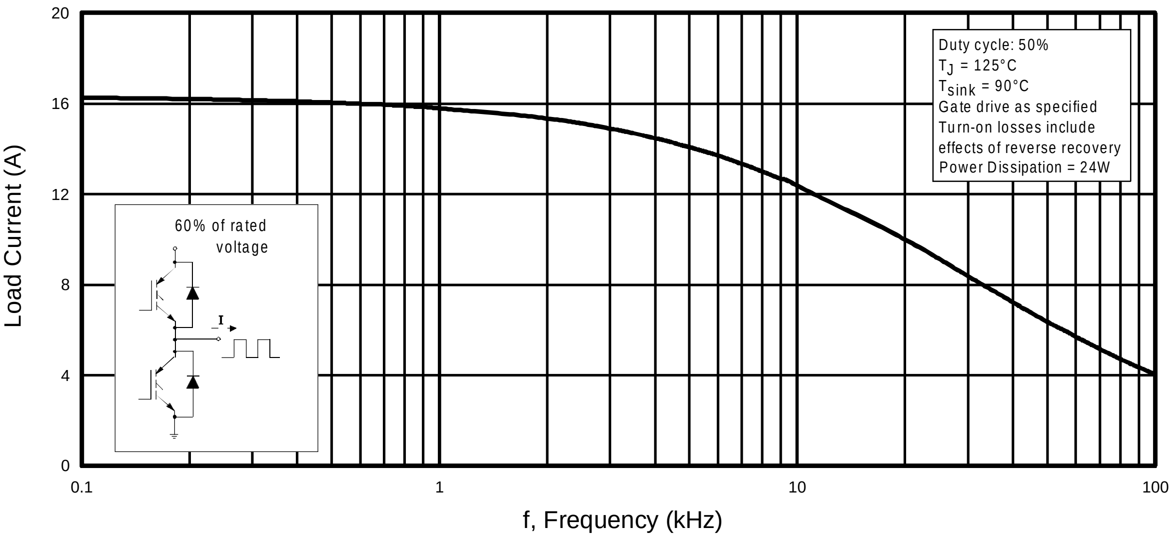


Fig. 1 - Typical Load Current vs. Frequency
(Load Current = I_{RMS} of fundamental)

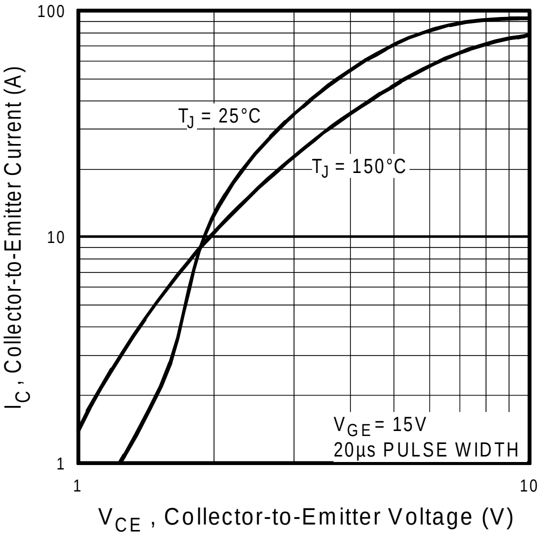


Fig. 2 - Typical Output Characteristics

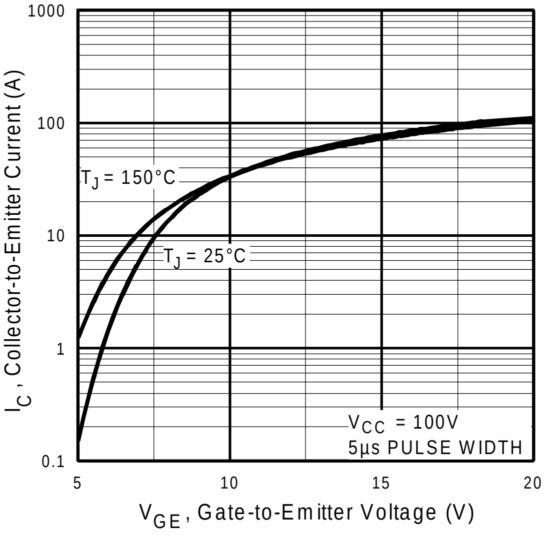
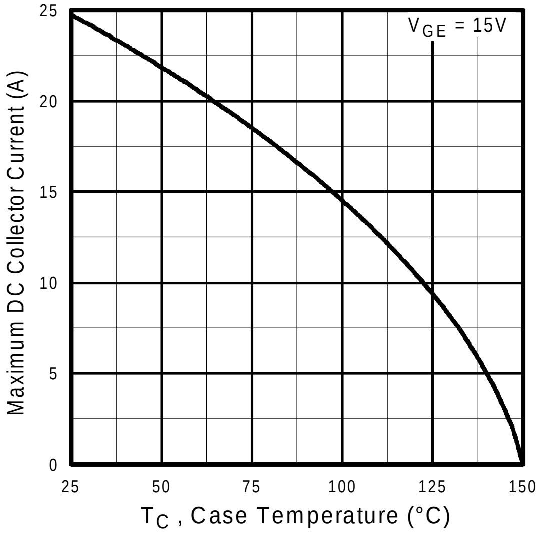


Fig. 3 - Typical Transfer Characteristics



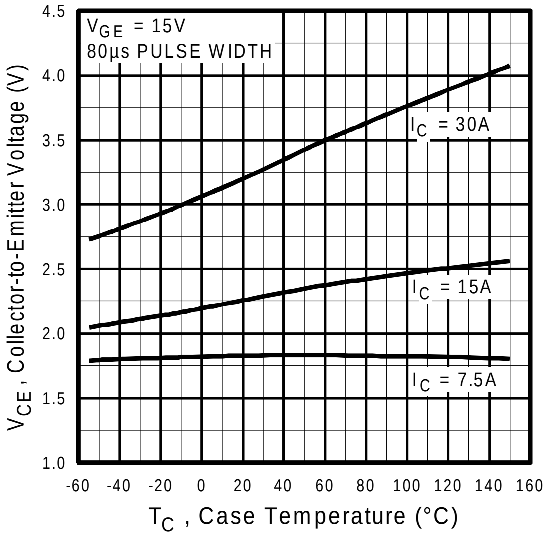


Fig. 4 - Maximum Collector Current vs. Case Temperature

Fig. 5 - Collector-to-Emitter Voltage vs. Case Temperature

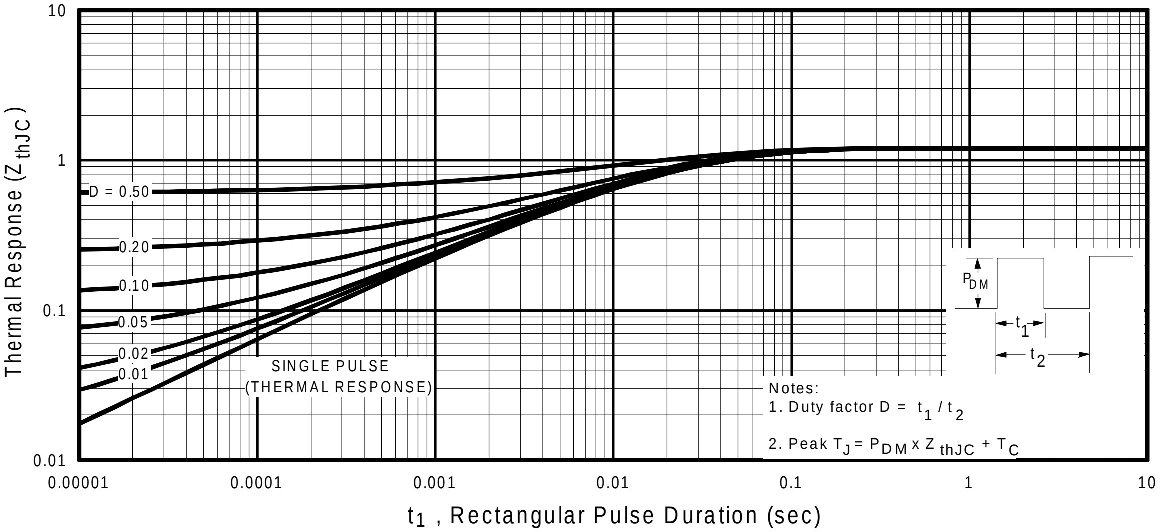


Fig. 6 - Maximum IGBT Effective Transient Thermal Impedance, Junction-to-Case

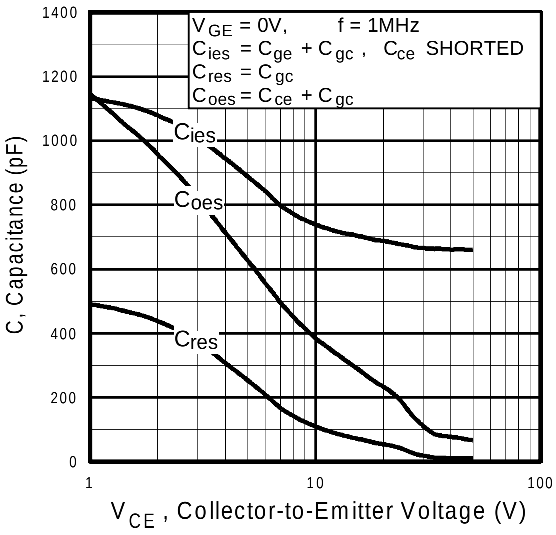


Fig. 7 - Typical Capacitance vs. Collector-to-Emitter Voltage

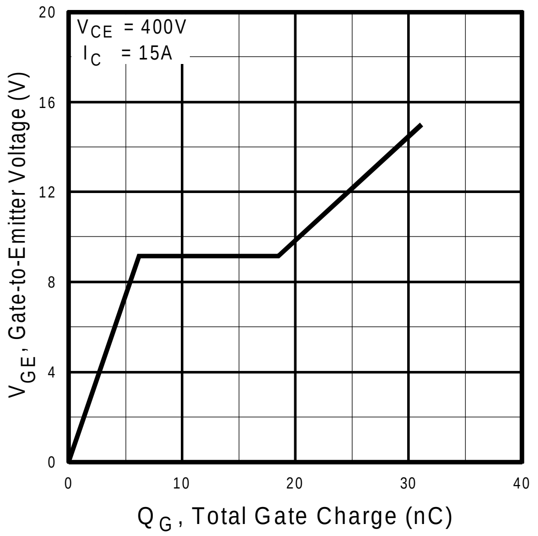


Fig. 8 - Typical Gate Charge vs. Gate-to-Emitter Voltage

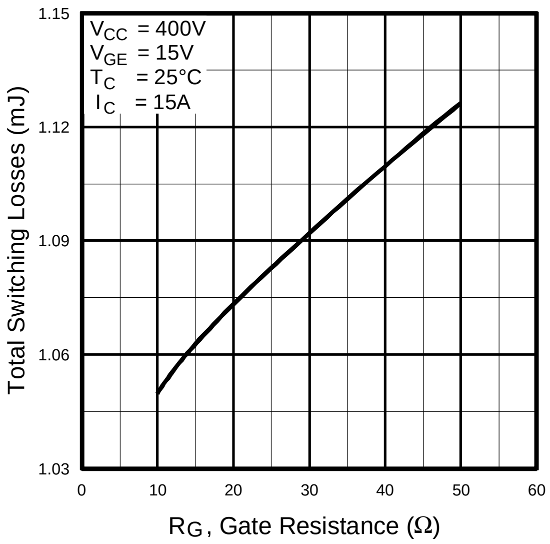


Fig. 9 - Typical Switching Losses vs. Gate Resistance

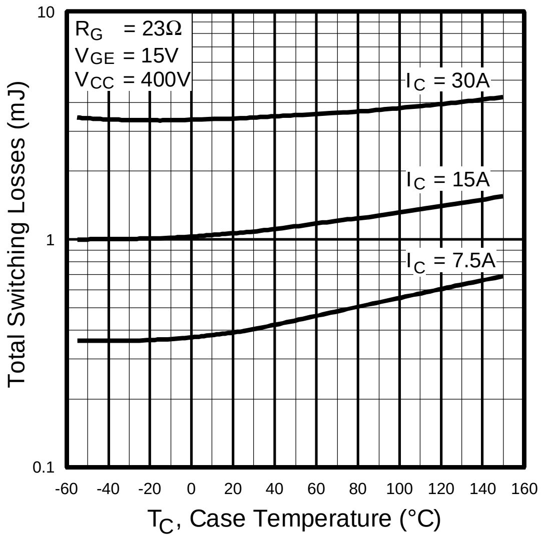


Fig. 10 - Typical Switching Losses vs. Case Temperature

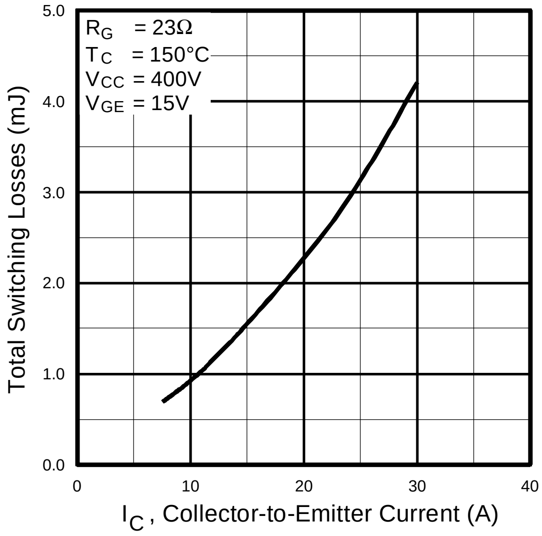


Fig. 11 - Typical Switching Losses vs. Collector-to-Emitter Current

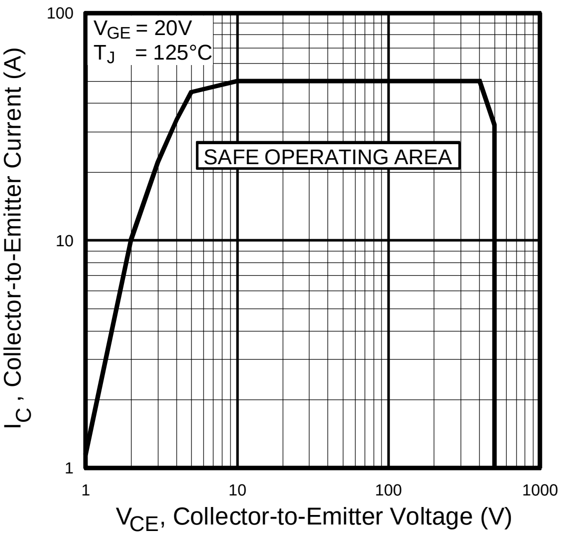


Fig. 12 - Turn-Off SOA

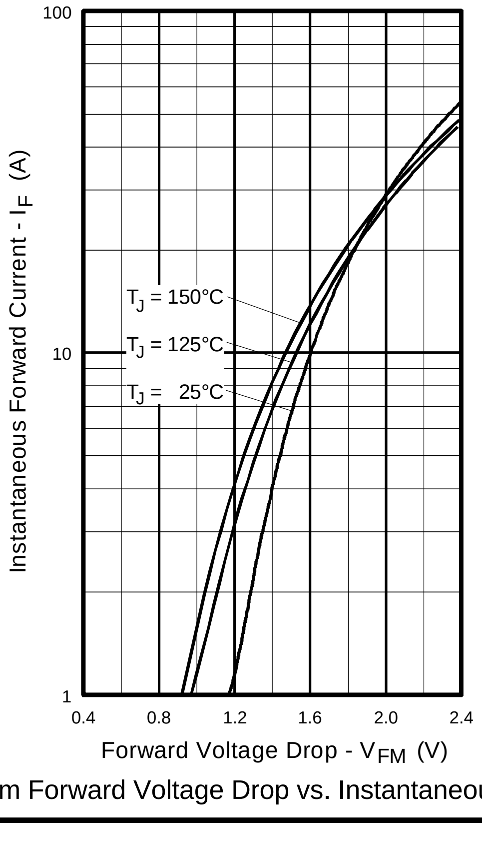


Fig. 13 - Maximum Forward Voltage Drop vs. Instantaneous Forward Current

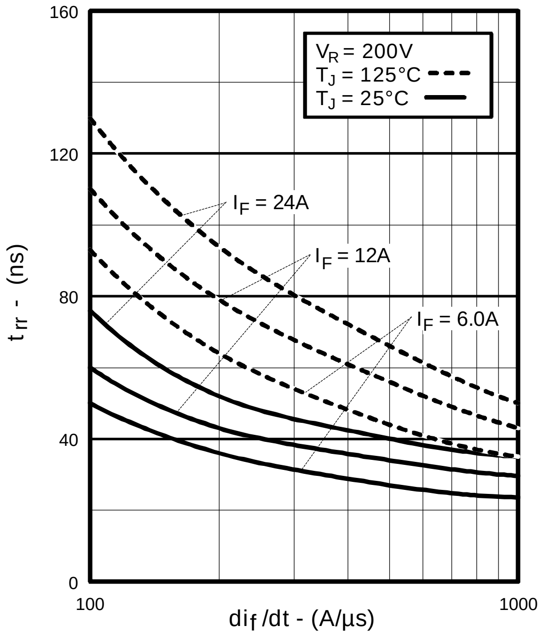


Fig. 14 - Typical Reverse Recovery vs. di_f/dt

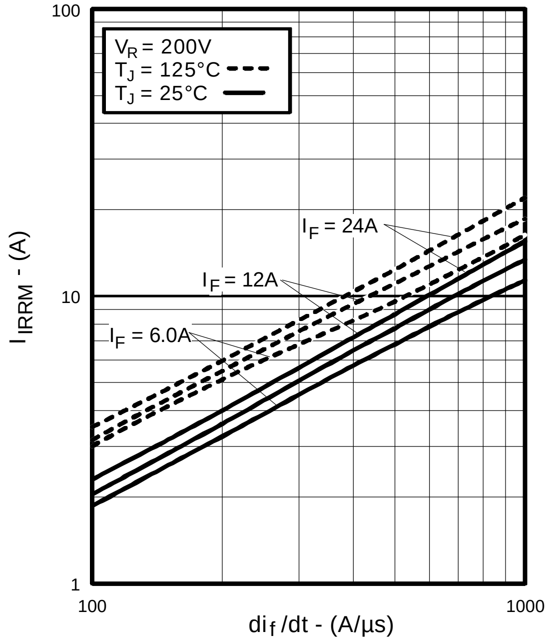


Fig. 15 - Typical Recovery Current vs. di_f/dt

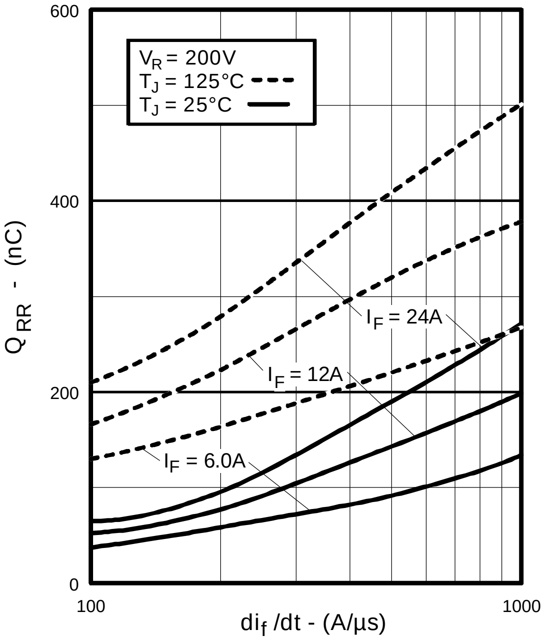


Fig. 16 - Typical Stored Charge vs. di_f/dt

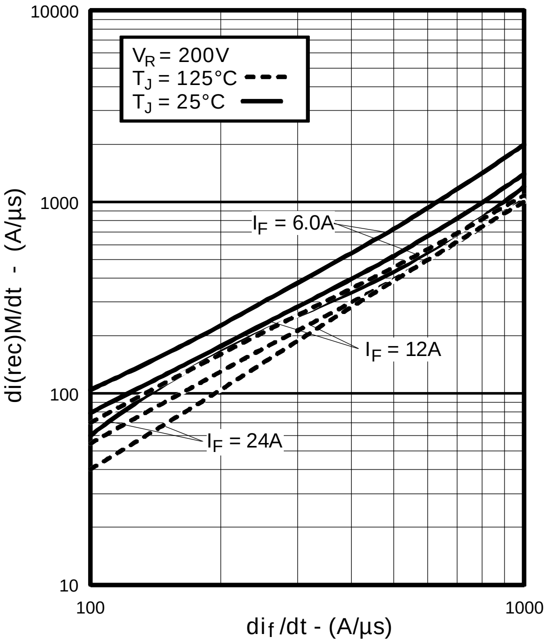


Fig. 17 - Typical $di_{(rec)M}/dt$ vs. di_f/dt

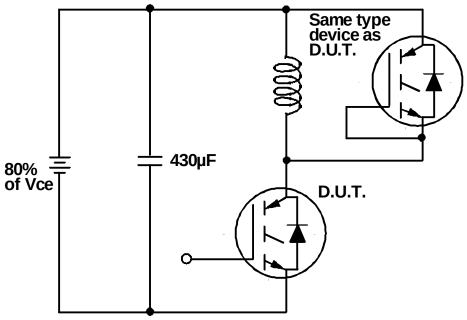


Fig. 18a - Test Circuit for Measurement of I_{LM}, E_{on}, $E_{off(diode)}$, t_{rr}, Q_{rr}, I_{rr}, $t_{d(on)}$, t_r, $t_{d(off)}$, t_f

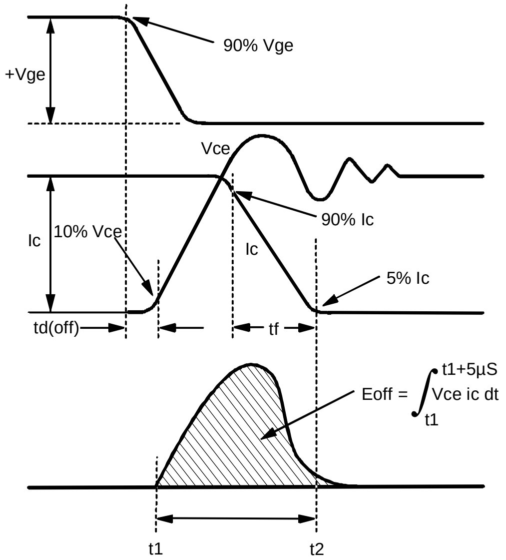


Fig. 18b - Test Waveforms for Circuit of Fig. 18a, Defining E_{off}, $t_{d(off)}$, t_f

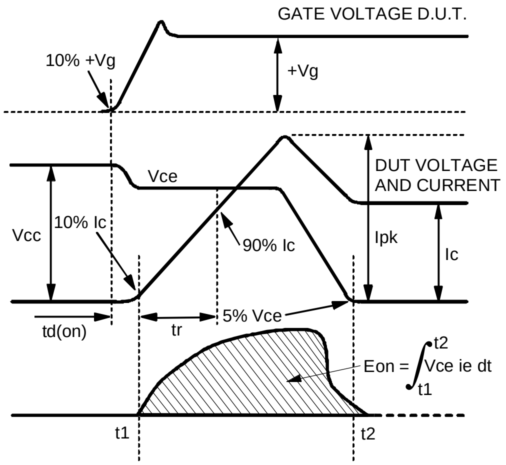


Fig. 18c - Test Waveforms for Circuit of Fig. 18a, Defining E_{on}, $t_{d(on)}$, t_r

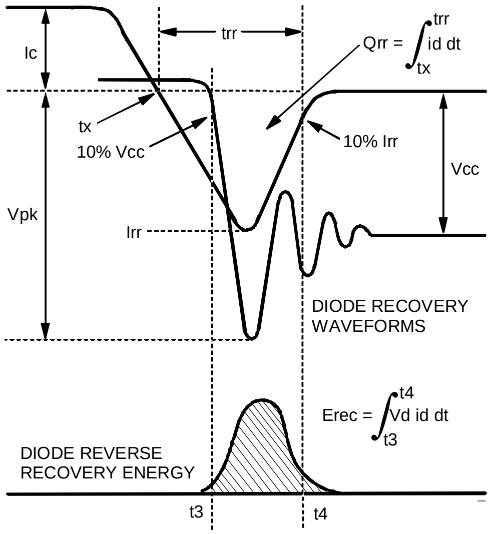


Fig. 18d - Test Waveforms for Circuit of Fig. 18a, Defining E_{rec}, t_{rr}, Q_{rr}, I_{rr}

Refer to Section D for the following:
Appendix B: Section D - page D-4

Fig. 18e - Macro Waveforms for Test Circuit Fig. 18a
Fig. 19 - Clamped Inductive Load Test Circuit
Fig. 20 - Pulsed Collector Current Test Circuit

Package Outline 3 - JEDEC Outline TO-247AC **Section D - page D-13**