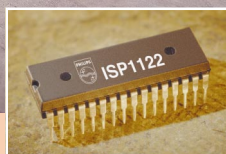


Providing manufacturers with a simple solution for integrating a Universal Serial Bus (USB) hub into their applications with minimum design risk and engineering effort, these highly integrated (USB) hub devices are fully compliant with the USB Specification Revision 1.1 and feature a configurable number of downstream ports.



The ISP1122 is available in both through-hole package (SDIP-32) and surface-mount (SO-32).



#### Features

- High performance USB hub device with integrated hub repeater, hub controller, Serial Interface Engine (SIE), data transceivers and 3.3 V voltage regulator
- Complies with "Universal Serial Bus Specification Rev. 1.1" and ACPI™, OnNow™ and USB power management requirements
- Configurable from 2 to 5 downstream ports with automatic speed detection (ISP1121 supports up to 4 downstream ports)
- Supports bus-powered, hybrid-powered and self-powered application
- Individual or global power switching for downstream ports
- Individual or ganged port overcurrent protection with built-in sense circuits (ganged protection only on ISP1121)
- 6 MHz crystal oscillator with on-chip PLL for low EMI
- Visual USB traffic monitoring (GoodLink™) for hub and downstream ports
- I<sup>2</sup>C-bus interface to read vendor ID, product ID and configuration bits from external EEPROM
- Operation over the extended USB bus voltage range (4.0 to 5.5 V)
- Operating temperature range -40 to +85 °C
- 8 kV in-circuit ESD protection for lower cost of external components
- Available in SDIP and SO packages (ISP1122, 32-pin; ISP1121, 20-pin)

## ISP112x stand-alone USB hub devices

#### Description

The ISP112x stand-alone Universal Serial Bus (USB) hub devices feature an integrated Serial Interface Engine (SIE), hub repeater, hub controller, USB data transceivers and a 3.3 V voltage regulator. Compliant with USB Specification Rev. 1.1, they have a configurable number of downstream ports, ranging from 2 to 5 on the ISP1122 and from 2 to 4 on the ISP1121, which can be used to connect low or full-speed USB peripherals.

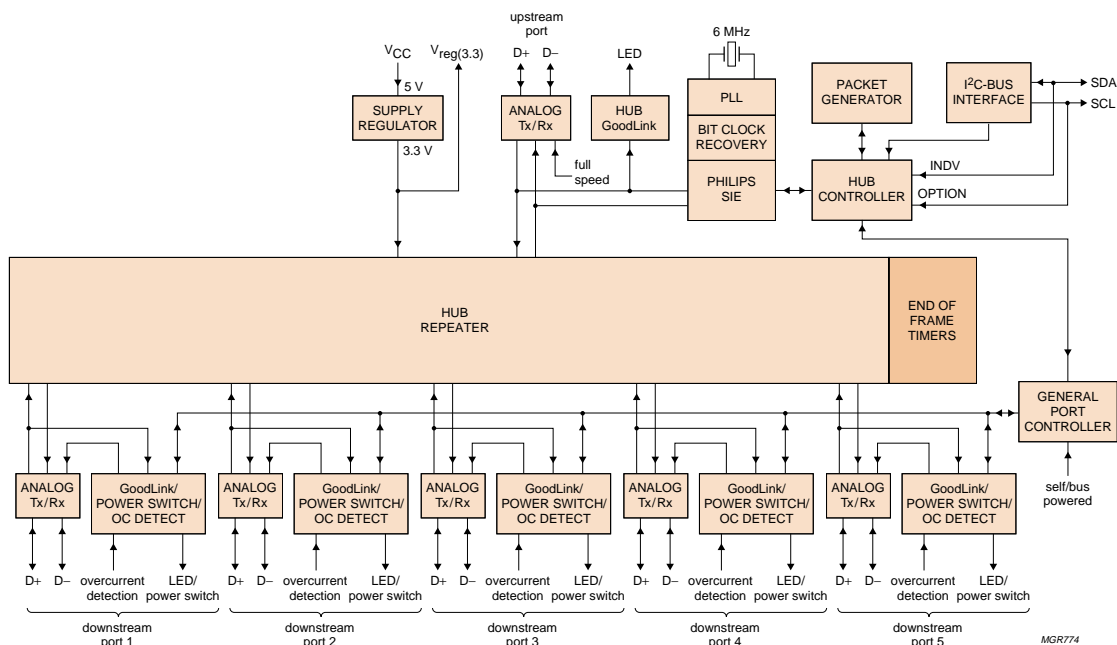
The integrated Philips Serial Interface Engine (SIE) implements the full USB protocol layer and is completely hardwired for speed, requiring no firmware intervention. Its functions include: synchronization pattern recognition, parallel/serial conversion, bit (de)stuffing, CRC checking/generation, Packet Identifier (PID) verification/generation, address recognition, handshake evaluation/generation.

The ISP112x requires only a single supply voltage, with an internal 3.3 V regulator providing the supply voltage for the analog USB data transceivers. These interface directly to the USB cables through external termination resistors and are capable of transmitting and receiving serial data at both 'full-speed' (12 Mbits/s) and 'low-speed' (1.5 Mbits/s) data rates. Slew rates are adjusted according to the speed of the connected device, as specified in the USB Specification Rev. 1.1.

They can operate in bus-powered, self-powered or hybrid-powered modes. When using bus-powered operation a downstream port cannot supply more than 100 mA to a peripheral. In self-powered operation an external 5 V supply is used to power the downstream ports, allowing a maximum current consumption of 500 mA per port. In the hybrid-powered mode, the hub functions are powered by the upstream power supply (VBUS), while the downstream ports are powered by the local power supply.

Built-in overcurrent sense inputs provide overcurrent protection for the downstream ports, eliminating the need for external special-function overcurrent sensing and port power switching devices. All ports on the ISP1122 (including upstream) have GoodLink™ indicator outputs for easy visual monitoring of USB traffic. The ISP1121 features GoodLink™ on its upstream port only.

Low power consumption in 'suspend' mode allows easy design of equipment compliant with ACPI™, OnNow™ and USB power management requirements. A serial I<sup>2</sup>C-bus interface is provided for reading vendor ID, product ID and configuration bits from external EEPROM. An integrated 6 to 48 MHz clock multiplier Phase-Locked Loop (PLL) allows for the use of low-cost 6 MHz crystals, allowing significant cost savings in system design, reduced design risk and easy implementation of advanced USB functionality in PC peripherals.



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