

June, 1998 Preliminary

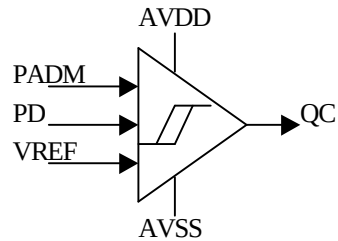
**AMI 0.6 micron CMOS
Single Ploy**

Comparator

Description

N-Channel differential input comparator with hysteresis. Digital model is designed for a differential input.

Schematic Symbol



TRUTH TABLE

PD	PADM	VREF	QC
1	X	X	0
0	0	0	X
0	0	1	0
0	1	0	1
0	1	1	X

PIN DESCRIPTION

NAME	TYPE	DESCRIPTION
PADM	Analog input	Positive Comparator Input
VREF	Analog input	Negative Comparator Input
PD	Digital input	Power Down Input
QC	Digital output	Comparator Output
AVDD	Analog supply	VDD
AVSS	Analog supply	VSS

AC ELECTRICAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITION	MIN	TYP	MAX	UNITS
delay	Propagation delay	1.95pf load		2.9		ns
T	Temperature		-40	25	85	°C
Vdd	Supply Voltage		4.5		5.5	V
Ivdd	Supply Current		0.9	1.3	2.1	mA
VHYS	Hysteresis		500	620	750	mV
CMR	Input Common Mode Range		1.25	2.5	VDD	V
Voh	Output high		VDD			V
Vol	Output low				VSS	V



IBCNH3

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Cload	Capacitive load		0.046	1.17	2.34	pf
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