

QUICKSWITCH® PRODUCTS HIGH-SPEED CMOS QUICKSWITCH 16-BIT WIDEBUS™ COMPATIBLE BUS SWITCH

IDTQS316245

FEATURES:

- · Enhanced N channel FET with no inherent diode to Vcc
- 5Ω bidirectional switches connect inputs to outputs
- · Pin compatible with FCT16245
- · Flowthrough pinout for easy layout
- · Zero propagation delay, zero ground bounce
- · TTL-compatible input and output levels
- · Undershoot clamp diodes on all switch and control inputs
- · Available in SSOP package

APPLICATIONS:

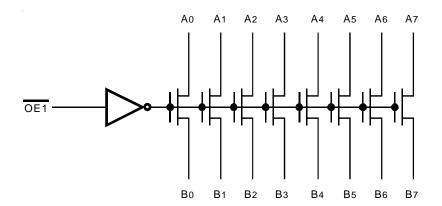
- · Hot-swapping, hot-docking
- · Voltage translation (5V to 3.3V)
- · Bus switching, isolation
- · Power conservation, clock gating
- · Logic replacement

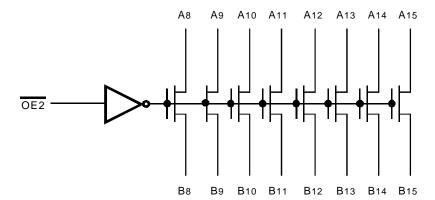
DESCRIPTION:

The QS316245 provides a set of 16 high-speed CMOS TTL-compatible bus switches (two banks of 8 switches each) in a flow-thru pinout. The low ON resistance of the QS316245 allows inputs to be connected to outputs without adding propagation delay and without generating additional ground bounce noise. The Output Enable (\overline{OEx}) signal turns the switches on similar to the \overline{OEx} signal of the 74′245. The QS316245 is ideally suited for 5V to 3.3V translation, bus switching and isolation, and hot insertion.

QuickSwitch devices provide an order of magnitude faster speed than conventional logic devices.

FUNCTIONAL BLOCK DIAGRAM



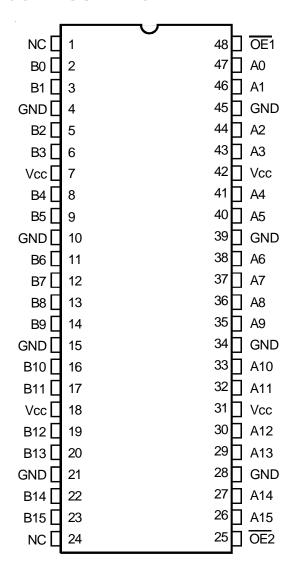


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INDUSTRIAL TEMPERATURE RANGE

JANUARY 2000

PIN CONFIGURATION



SSOP TOP VIEW

ABSOLUTE MAXIMUM RATINGS(1)

| Symbol | Description | Max | Unit |
|----------------------|--------------------------------------|-------------|------|
| VTERM ⁽²⁾ | Supply Voltage to Ground | -0.5 to +7 | V |
| VTERM ⁽³⁾ | DC Switch Voltage to Ground | -0.5 to +7 | V |
| VTERM ⁽³⁾ | DC Input Voltage VIN | -0.5 to +7 | V |
| VAC | AC Input Voltage (pulse width ≤20ns) | -3 | V |
| lout | DC Output Current | 120 | mA |
| Рмах | Maximum Power | 0.5 | W |
| Tstg | Storage Temperature | -65 to +150 | °C |

NOTES:

- $1. \ \, \text{Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause}$ permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.
- 2. Vcc terminals.
- 3. All terminals except Vcc .

CAPACITANCE (TA = +25°C, f = 1MHz, VIN = 0V, VOUT = 0V)

| Pins | Тур. | Max. ⁽¹⁾ | Unit |
|-----------------------------------|------|---------------------|------|
| Control Inputs | 3 | 5 | pF |
| Quickswitch Channels (Switch OFF) | 5 | 7 | pF |

NOTE:

1. This parameter is guaranteed but not production tested.

PIN DESCRIPTION

| Pin Names | I/O | Description | | |
|-----------|-----|-------------|--|--|
| Ax | I/O | Bus A | | |
| Вх | I/O | Bus B | | |
| ŌĒx | I | Bus Enable | | |

FUNCTION TABLE(1)

| ŌĒx | Ах | Function |
|-----|----|------------|
| L | Вх | Connect |
| Н | Z | Disconnect |

NOTE:

H = HIGH Voltage Level
L = LOW Voltage Level

X = High-Impedance

DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE

Following Conditions Apply Unless Otherwise Specified:

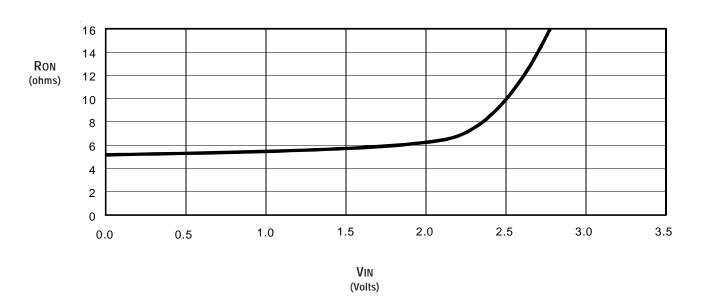
Industrial: TA = -40°C to +85°C, VCC = $5V \pm 10\%$

| Symbol | Parameter | Test Conditions | Min. | Typ. ⁽¹⁾ | Max. | Unit |
|--------|--|--|------|---------------------|------|------|
| VIH | Input HIGH Voltage | Guaranteed Logic HIGH for Control Inputs | 2 | _ | _ | V |
| VIL | Input LOW Voltage | Guaranteed Logic LOW for Control Inputs | _ | _ | 0.8 | V |
| lin | Input Leakage Current (Control Inputs) | 0V ≤ Vin ≤ Vcc | _ | _ | ±1 | μA |
| loz | Off-State Current (Hi-Z) | 0V ≤ Vouт ≤ Vcc, Switches OFF | _ | _ | ±1 | μA |
| Ron | Switch ON Resistance | Vcc = Min., VIN = 0V, ION = 30mA | _ | 5 | 7 | Ω |
| | | VCC = Min., VIN = 2.4V, ION = 15mA | _ | 10 | 12 | |
| VP | Pass Voltage ⁽²⁾ | $VIN = VCC = 5V$, $IOUT = -5\mu A$ | 3.7 | 4 | 4.2 | V |

NOTES:

- 1. Typical values are at Vcc = 5V and TA = 25°C.
- 2. Pass voltage is guaranteed but not production tested.

TYPICAL ON RESISTANCE vs Vin AT Vcc = 5V



POWER SUPPLY CHARACTERISTICS

| Symbol | Parameter | Test Conditions ⁽¹⁾ | | Unit |
|--------|--|---|------|--------|
| Icco | Quiescent Power Supply Current | Vcc = Max., Vin = GND or Vcc, f = 0 | 3 | μA |
| ∆lcc | Power Supply Current per Control Input HIGH ⁽²⁾ | Vcc = Max., Vin = 3.4V, f = 0 | 1.5 | mA |
| ICCD | Dynamic Power Supply Current per MHz ⁽³⁾ | Vcc = Max., A and B Pins Open, Control Inputs Toggling @ 50% Duty Cycle | 0.25 | mA/MHz |

NOTES:

- 1. For conditions shown as Min. or Max., use the appropriate values specified under DC Electrical Characteristics.
- 2. Per TTL-driven input (VIN = 3.4V). A and B pins do not contribute to ΔIcc .
- 3. This current applies to the control inputs only and represents the current required to switch internal capacitance at the specified frequency. The A and B inputs generate no significant AC or DC currents as they transition. This parameter is guaranteed but not production tested.

SWITCHING CHARACTERISTICS OVER OPERATING RANGE

 $T_A = -40^{\circ}C \text{ to } +85^{\circ}C, V_{CC} = 5V \pm 10\%$

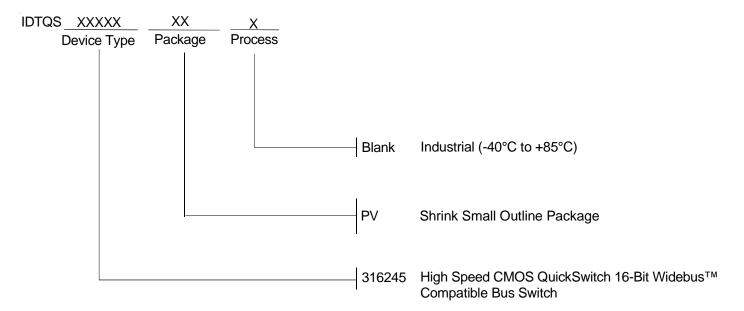
CLOAD = 50pF, RLOAD = 500Ω unless otherwise noted.

| Symbol | Parameter | Min. ⁽¹⁾ | Тур. | Max. | Unit |
|--------|---------------------------------------|---------------------|------|---------------------|------|
| tplh | Data Propagation Delay ⁽²⁾ | _ | _ | 0.25 ⁽³⁾ | ns |
| tphl | Ax to Bx, Bx to Ax | | | | |
| tpzl | Switch Turn-On Delay | 1.5 | _ | 6.5 | ns |
| tpzh | OEx to Ax/Bx | | | | |
| tplz | Switch Turn-Off Delay ⁽²⁾ | 1.5 | _ | 5.5 | ns |
| tphz | OEx to Ax/Bx | | | | |

NOTES:

- 1. Minimums are guaranteed but not production tested.
- 2. This parameter is guaranteed but not production tested.
- 3. The bus switch contributes no propagation delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns at C_L = 50pF. Since this time constant is much smaller than the rise and fall times of typical driving signals, it adds very little propagation delay to the system. Propagation delay of the bus switch, when used in a system, is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

ORDERING INFORMATION





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