



LOW-VOLTAGE 10-BIT BUS SWITCH

IDT74CBTLVR3384

FEATURES:

- Isolation Under Power-Off Conditions
- Over-voltage tolerant
- Latch-up performance exceeds 100mA
- $V_{CC} = 2.3V - 3.6V$, normal range
- ESD >2000V per MIL-STD-883, Method 3015; >200V using machine model ($C = 200pF$, $R = 0$)
- Available in SSOP, QSOP, and TSSOP packages

APPLICATIONS:

- 3.3V High Speed Bus Switching and Bus Isolation

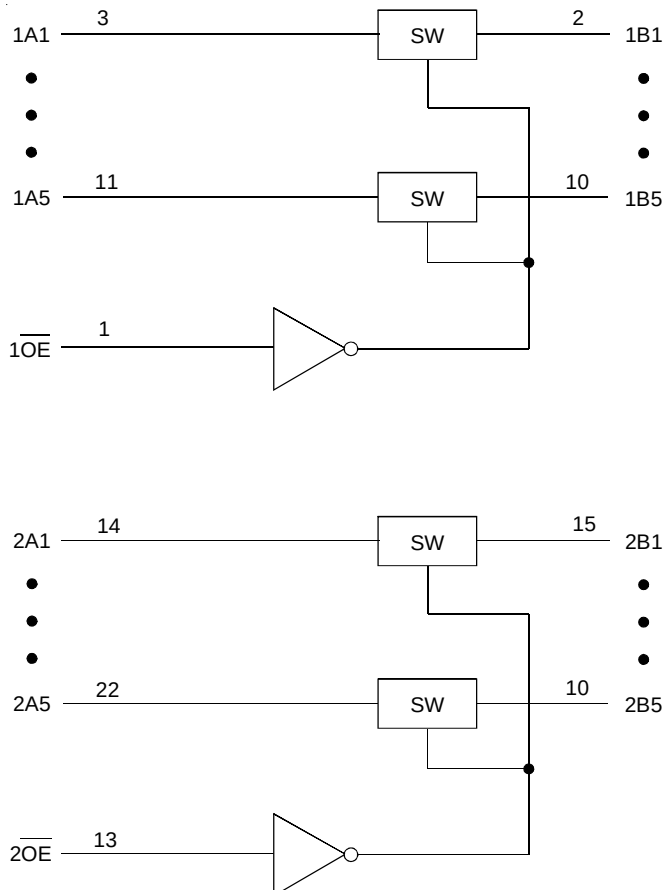
DESCRIPTION:

The CBTLVR3384 is a ten bit high-speed bus switch. It adds an internal series resistor with each switch to reduce reflection noise in high speed applications. When closed, the switch acts as a source (series) termination for the driver connected to it.

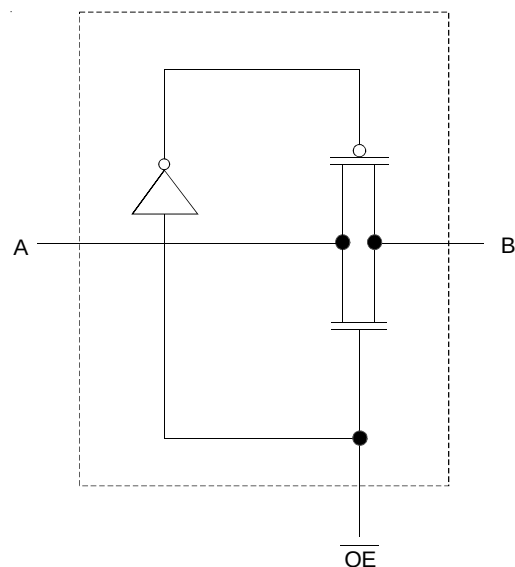
The device is organized as dual 5-bit bus switches with separate output-enable ($\overline{OE}$) inputs, to allow use as two 5-bit bus switches or one 10-bit bus switch. When $\overline{OE}$ is low, the associated 5-bit bus switch is on and A port is connected to B port. When $\overline{OE}$ is high, the switch is open, and a high-impedance state exists between the two ports.

To ensure the high-impedance state during power up or power down, $\overline{OE}$ should be tied to V_{CC} through a pullup resistor; the minimum value of the resistor is determined by the current-sinking capability of the driver.

FUNCTIONAL BLOCK DIAGRAM



SIMPLIFIED SCHEMATIC, EACH SWITCH

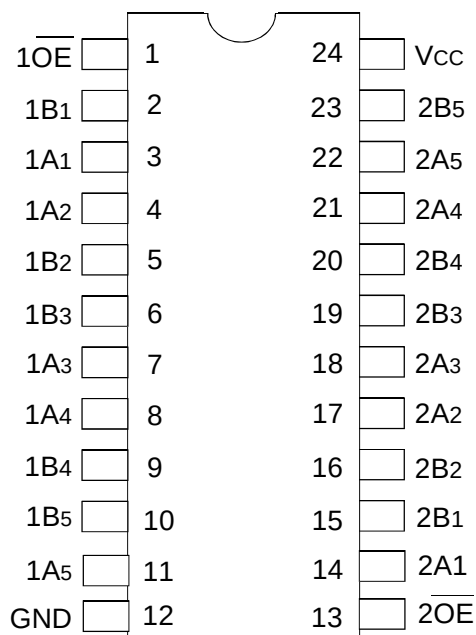


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INDUSTRIAL TEMPERATURE RANGE

AUGUST 2002

PIN CONFIGURATION



SSOP/ QSOP/ TSSOP
TOP VIEW

ABSOLUTE MAXIMUM RATINGS⁽¹⁾

Symbol	Description	Max.	Unit
V _{CC}	Supply Voltage Range	−0.5 to 4.6	V
V _I	Input Voltage Range	−0.5 to 4.6	V
	Continuous Channel Current	128	mA
I _{IK}	Input Clamp Current, V _{I/O} < 0	−50	mA
T _{STG}	Storage Temperature Range	−65 to +150	°C

NOTE:

- Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

FUNCTION TABLE⁽¹⁾

Inputs		Inputs/Outputs	
1OE	2OE	1B1-1B5	2B1-2B5
L	L	1A1-1A5	2A1-2A5
L	H	1A1-1A5	Z
H	L	Z	2A1-2A5
H	H	Z	Z

NOTE:

- H = HIGH Voltage Level
L = LOW Voltage Level
X = Don't Care
Z = High Impedance

OPERATING CHARACTERISTICS⁽¹⁾

Symbol	Parameter	Test Conditions	Min.	Max.	Unit
V _{CC}	Supply Voltage		2.3	3.6	V
V _{IH}	High-Level Control Input Voltage	V _{CC} = 2.3V to 2.7V	1.7	—	V
		V _{CC} = 2.7V to 3.6V	2	—	
V _{IL}	Low-Level Control Input Voltage	V _{CC} = 2.3V to 2.7V	—	0.7	V
		V _{CC} = 2.7V to 3.6V	—	0.8	
T _A	Operating Free-Air Temperature		−40	+85	°C

NOTE:

- All unused control inputs of the device must be held at V_{CC} or GND to ensure proper device operation.

DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE

Following Conditions Apply Unless Otherwise Specified:

Operating Condition: TA = -40°C to +85°C

Symbol	Parameter	Test Conditions		Min.	Typ. ⁽¹⁾	Max.	Unit
V _{IK}	Control Inputs, Data I/O	V _{CC} = 3V, I _I = -18mA		—	—	-1.2	V
I _I	Control Inputs	V _{CC} = 3.6V, V _I = V _{CC} or GND		—	—	±1	μA
I _{OZ}	Data I/O	V _{CC} = 3.6V, V _O = 0V or 3.6V switch disabled		—	—	5	μA
I _{OFF}		V _{CC} = 0V, V _I or V _O = 0V to 3.6V		—	—	10	μA
I _{CC}		V _{CC} = 3.6V, I _O = 0, V _I = V _{CC} or GND		—	—	10	μA
ΔI _{CC} ⁽²⁾	Control Inputs	V _{CC} = 3.6V, one input at 3V, other inputs at V _{CC} or GND		—	—	300	μA
C _I	Control Inputs	V _I = 3V or 0		—	4	—	pF
C _{IO(OFF)}		V _O = 3V or 0, $\overline{OE}$ = V _{CC}		—	7	—	pF
R _{ON} ⁽³⁾	Max. at V _{CC} = 2.3V Typ. at V _{CC} = 2.5V	V _I = 0	I _O = 64mA	—	30	47	Ω
			I _O = 24mA	—	30	47	
	V _{CC} = 3V	V _I = 1.7V	I _O = 15mA	—	36	80	
		V _I = 0	I _O = 64mA	—	30	42	
			I _O = 24mA	—	30	42	
		V _I = 2.4V	I _O = 15mA	—	32	47	

NOTES:

- Typical values are at 3.3V, +25°C ambient.
- The increase in supply current is attributable to each input that is at the specified voltage level rather than V_{CC} or GND.
- This is measured by the voltage drop between the A and B terminals at the indicated current through the switch. On-state resistance is determined by the lower of the voltages of the two (A or B) terminals.

SWITCHING CHARACTERISTICS

Symbol	Parameter	V _{CC} = 2.5V ± 0.2V		V _{CC} = 3.3V ± 0.3V		Unit
		Min.	Max.	Min.	Max.	
t _{PD} ⁽¹⁾	Propagation Delay A to B or B to A	—	0.9	—	1.5	ns
t _{EN}	Output Enable Time $\overline{OE}$ to A or B	1	6	1	5.3	ns
t _{DIS}	Output Disable Time $\overline{OE}$ to A or B	1	5.5	1	5.5	ns

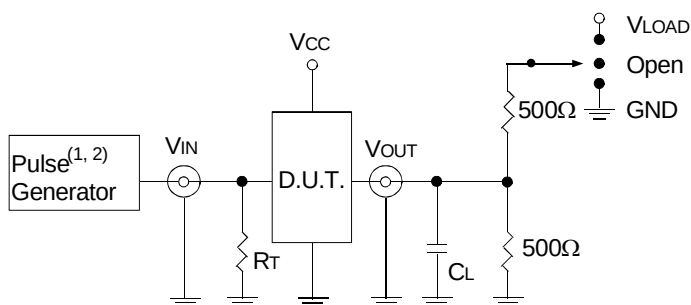
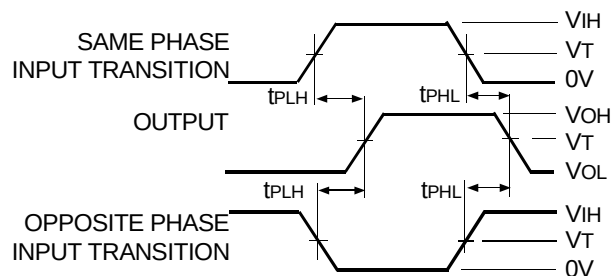
NOTE:

- The propagation delay is the calculated RC time constant of the typical on-state resistance of the switch and the specified load capacitance when driven by an ideal voltage source (zero output impedance).

TEST CIRCUITS AND WAVEFORMS

TEST CONDITIONS

Symbol	$V_{CC}^{(1)} = 3.3V \pm 0.3V$	$V_{CC}^{(2)} = 2.5V \pm 0.2V$	Unit
V_{LOAD}	6	$2 \times V_{CC}$	V
V_{IH}	3	V_{CC}	V
V_T	1.5	$V_{CC} / 2$	V
V_{LZ}	300	150	mV
V_{HZ}	300	150	mV
C_L	50	30	pF



Test Circuits for All Outputs

DEFINITIONS:

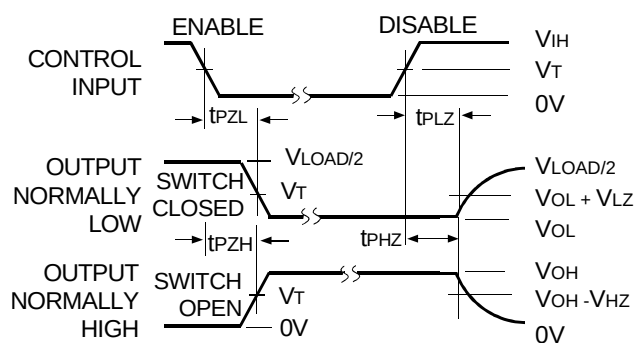
C_L = Load capacitance: includes jig and probe capacitance.

R_T = Termination resistance: should be equal to Z_{out} of the Pulse Generator.

NOTES:

1. Pulse Generator for All Pulses: Rate $\leq 10\text{MHz}$; $t_r \leq 2.5\text{ns}$; $t_f \leq 2.5\text{ns}$.
2. Pulse Generator for All Pulses: Rate $\leq 10\text{MHz}$; $t_r \leq 2\text{ns}$; $t_f \leq 2\text{ns}$.

Propagation Delay



NOTE:

1. Diagram shown for input Control Enable-LOW and input Control Disable-HIGH.

Enable and Disable Times

SWITCH POSITION

Test	Switch
t_{PLZ}/t_{PZL}	V_{LOAD}
t_{PHZ}/t_{PZH}	GND
t_{PD}	Open

ORDERING INFORMATION

IDT	XX	CBTLV	X	XXX	XX	
Temp. Range		Family		Device Type	Package	
					PY	Shrink Small Outline Package
					Q	Quarter-size Small Outline Package
					PG	Thin Shrink Small Outline Package
				3384		Low-Voltage 10-Bit Bus Switch
					R	25Ω series dampening resistors on A and B ports
					74	−40°C to +85°C



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