

Intel® IXF18104

10 Gigabit Ethernet LAN PHY

Product Features

10 GIGABIT ETHERNET LAN PHY

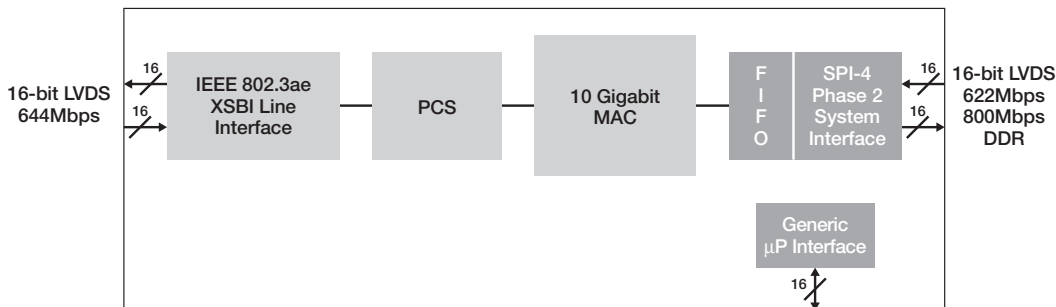
- Integrated 10Gbps full duplex MAC with statistics per IEEE 802.3ae
- Physical Coding Sub-Layer synchronization allows mapping Ethernet packets into 64B/66B codes for transmission over LAN PHY Optics per IEEE 802.3ae
- Support for 10GbE RMON/SNMP statistics including Frames transmitted/received, Frame Check Errors, Undersize Packets, Oversize Packets, VLAN, Multicast, Broadcast, Pause and Control
- Supports 9.6Kbyte Jumbo Frames
- Flow Control based on FIFO watermarks and external host control logic
- Footprint Compatible with IXF18101 (STS-192c POS/GFP and 10GbE LAN/WAN support), IXF18102 (STS-192c POS/GFP), IXF18103 (10GbE LAN and WAN support), for complete 10 Gigabit line card design flexibility

INTERFACES

- System side interface supports:
 - SPI-4 Phase 2 interface 16-bit LVDS @ 622–800Mbps with static or dynamic deskew
- Line side interface supports:
 - XSBI 16-bit @ 644Mbps operation for LAN PHY applications
 - 32Kbyte FIFO with programmable watermarks for transmit and receive FIFO status

GENERAL

- Generic 16-bit microprocessor interface
- 672-pin Ultra BGA package
- -40 to +85°C operation
- Smart power mode control
- 1.2V, 1.8V and 2.5V operation
- 3.3V tolerant I/O

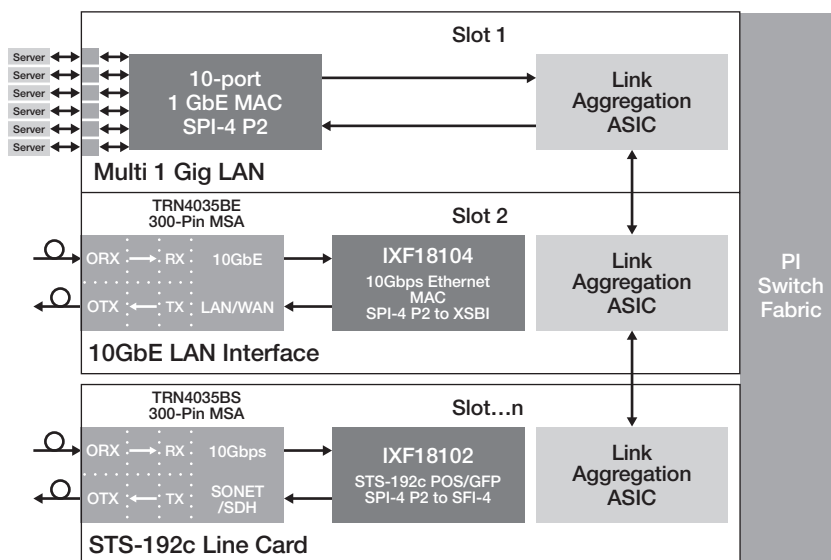


Applications

- Terabit Switch/Router Platforms
- Edge and Core Router Platforms
- Multi-Service Provisioning Platforms
- Metro POP Ethernet Switches
- Storage Area Networks
- Network Attached Storage

Typical Application

10 GIGABIT ETHERNET ENTERPRISE SWITCH APPLICATION



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Intel is a leading supplier of communications building blocks, adding value at many levels of integration. Through continuous innovations and advancements in Ethernet connectivity and processing in the network, Intel is delivering, along with its customers and developer community, a wide choice of solutions that enable faster time-to-market, longer time-in-market and increased revenue opportunity.

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