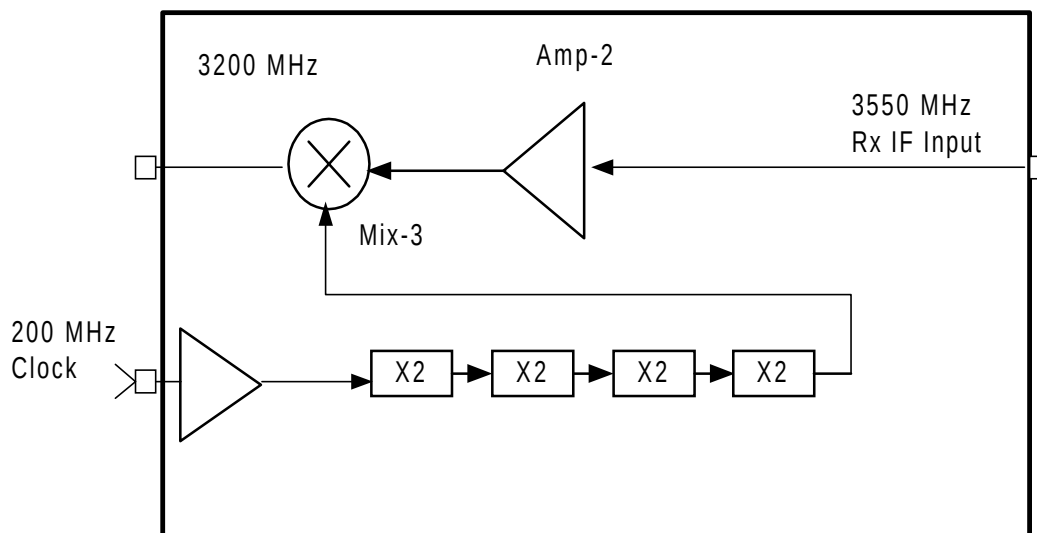


2004: Uniband Receiver RF IC



DESCRIPTION

The 2004 receiver IC contains an LNA, a mixer, and an AGC. A series of cascade frequency doublers are used to generate the 3.2GHz LO from a 200MHz input reference clock source. Integrated switched-AGC provides 15dB of gain adjustment.

SPECIFICATIONS

High Gain Mode

- Minimum/Maximum input power levels: -59/-31 dBm
- Noise Figure: 9.5dB
- IIP3: -1.2dBm
- Power gain: 7dB

Low Gain Mode

- Minimum/Maximum input power levels: -45/-17dBm
- IIP3: 14dBm
- Power Gain: -7dB
- Input/Output Frequencies: 3550MHz/350MHz
- Input impedance (single-ended): 50ohms
- Output impedance (differential): 500 ohms

FEATURES

- Low Gain/High Gain Mode
- 200MHz input reference clock with on-chip doublers
- LVDS compatible 1²C logic interface
- 3.3V operations

APPLICATIONS

- Local Multipoint distribution systems
- MMDS

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