

International
IR Rectifier
RADIATION HARDENED
POWER MOSFET
SURFACE MOUNT (LCC-18)

PD - 90881C

IRHE9130
100V, P-CHANNEL
REF: MIL-PRF-19500/630
RAD-Hard™ HEXFET®
MOSFETTECHNOLOGY

Product Summary

Part Number	Radiation Level	R _{d(on)}	I _D	QPL Part Number
IRHE9130	100K Rads (Si)	0.30Ω	-6.5A	JANSR2N7389U
IRHE93130	300K Rads (Si)	0.30Ω	-6.5A	JANSF2N7389U

International Rectifier's RAD-Hard™ HEXFET® MOSFET technology provides high performance power MOSFETs for space applications. This technology has over a decade of proven performance and reliability in satellite applications. These devices have been characterized for both Total Dose and Single Event Effects (SEE). The combination of low Rdson and low gate charge reduces the power losses in switching applications such as DC to DC converters and motor control. These devices retain all of the well established advantages of MOSFETs such as voltage control, fast switching, ease of parallelizing and temperature stability of electrical parameters.



LCC - 18

Features:

- Single Event Effect (SEE) Hardened
- Low R_{d(on)}
- Low Total Gate Charge
- Proton Tolerant
- Simple Drive Requirements
- Ease of Parallelizing
- Hermetically Sealed
- Ceramic Package
- Surface Mount
- Light Weight

Absolute Maximum Ratings

Pre-Irradiation

	Parameter		Units
I _D @ V _{GS} = 12V, T _C = 25°C	Continuous Drain Current	-6.5	A
I _D @ V _{GS} = 12V, T _C = 100°C	Continuous Drain Current	-4.1	
I _{DM}	Pulsed Drain Current ①	-26	
P _D @ T _C = 25°C	Max. Power Dissipation	25	W
	Linear Derating Factor	0.2	W/°C
V _{GS}	Gate-to-Source Voltage	±20	V
E _{AS}	Single Pulse Avalanche Energy ②	165	mJ
I _{AR}	Avalanche Current ①	-6.5	A
E _{AR}	Repetitive Avalanche Energy ①	2.5	mJ
dv/dt	Peak Diode Recovery dv/dt ③	-22	V/ns
T _J	Operating Junction	-55 to 150	°C
T _{STG}	Storage Temperature Range		
	Pckg. Mounting Surface Temp.	300 (for 5s)	
	Weight	0.42 (Typical)	g

For footnotes refer to the last page

Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (Unless Otherwise Specified)

	Parameter	Min	Typ	Max	Units	Test Conditions
BV_{DSS}	Drain-to-Source Breakdown Voltage	-100	—	—	V	$\text{V}_{\text{GS}} = 0\text{ V}$, $\text{I}_D = -1.0\text{mA}$
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	Temperature Coefficient of Breakdown Voltage	—	-0.112	—	$^\circ\text{C}$	Reference to 25°C , $\text{I}_D = -1.0\text{mA}$
$\text{R}_{\text{DS(on)}}$	Static Drain-to-Source On-State Resistance	—	—	0.30	Ω	$\text{V}_{\text{GS}} = -12\text{V}$, $\text{I}_D = -4.1\text{A}$ ④
		—	—	0.35		$\text{V}_{\text{GS}} = -12\text{V}$, $\text{I}_D = -6.5\text{A}$ ④
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	-2.0	—	-4.0	V	$\text{V}_{\text{DS}} = \text{V}_{\text{GS}}$, $\text{I}_D = -1.0\text{mA}$
g_{fs}	Forward Transconductance	2.5	—	—	S ($\text{S}(\text{G})$)	$\text{V}_{\text{DS}} > -15\text{V}$, $\text{I}_{\text{DS}} = -4.1\text{A}$ ④
I_{DSS}	Zero Gate Voltage Drain Current	—	—	-25	μA	$\text{V}_{\text{DS}} = -80\text{V}$, $\text{V}_{\text{GS}} = 0\text{V}$
		—	—	-250		$\text{V}_{\text{DS}} = -80\text{V}$ $\text{V}_{\text{GS}} = 0\text{V}$, $T_J = 125^\circ\text{C}$
I_{GSS}	Gate-to-Source Leakage Forward	—	—	-100	nA	$\text{V}_{\text{GS}} = -20\text{V}$
I_{GSS}	Gate-to-Source Leakage Reverse	—	—	100		$\text{V}_{\text{GS}} = 20\text{V}$
Q_g	Total Gate Charge	—	—	45	nC	$\text{V}_{\text{GS}} = -12\text{V}$, $\text{I}_D = -6.5\text{A}$
Q_{gs}	Gate-to-Source Charge	—	—	10		$\text{V}_{\text{DS}} = -50\text{V}$
Q_{gd}	Gate-to-Drain ('Miller') Charge	—	—	25		
$t_{\text{d(on)}}$	Turn-On Delay Time	—	—	30	ns	$\text{V}_{\text{DD}} = -50\text{V}$, $\text{I}_D = -6.5\text{A}$, $\text{V}_{\text{GS}} = -12\text{V}$, $R_G = 7.5\Omega$
t_r	Rise Time	—	—	50		
$t_{\text{d(off)}}$	Turn-Off Delay Time	—	—	70		
t_f	Fall Time	—	—	70		
$\text{L}_{\text{S}} + \text{L}_{\text{D}}$	Total Inductance	—	6.1	—	nH	Measured from the center of drain pad to center of source pad
C_{iss}	Input Capacitance	—	1200	—	pF	$\text{V}_{\text{GS}} = 0\text{V}$, $\text{V}_{\text{DS}} = -25\text{V}$ $f = 1.0\text{MHz}$
C_{oss}	Output Capacitance	—	290	—		
C_{rss}	Reverse Transfer Capacitance	—	76	—		

Source-Drain Diode Ratings and Characteristics

	Parameter	Min	Typ	Max	Units	Test Conditions
I_{S}	Continuous Source Current (Body Diode)	—	—	-6.5	A	
I_{SM}	Pulse Source Current (Body Diode) ①	—	—	-26		
V_{SD}	Diode Forward Voltage	—	—	-3.0	V	$T_J = 25^\circ\text{C}$, $\text{I}_{\text{S}} = -6.5\text{A}$, $\text{V}_{\text{GS}} = 0\text{V}$ ④
t_{rr}	Reverse Recovery Time	—	—	250	nS	$T_J = 25^\circ\text{C}$, $\text{I}_{\text{F}} = -6.5\text{A}$, $d\text{i}/dt \leq -100\text{A}/\mu\text{s}$ $\text{V}_{\text{DD}} \leq -25\text{V}$ ④
Q_{RR}	Reverse Recovery Charge	—	—	0.74	μC	
t_{on}	Forward Turn-On Time	Intrinsic turn-on time is negligible. Turn-on speed is substantially controlled by $\text{L}_{\text{S}} + \text{L}_{\text{D}}$.				

Thermal Resistance

	Parameter	Min	Typ	Max	Units	Test Conditions
R_{thJC}	Junction-to-Case	—	—	5.0	$^\circ\text{C/W}$	Solder to a copper clad PC Board
R_{thJPCB}	Junction-to-PC Board	—	19	—		

Note: Corresponding Spice and Saber models are available on the G&S Website.

For footnotes refer to the last page

Radiation Characteristics

IRHE9130

International Rectifier Radiation Hardened MOSFETs are tested to verify their radiation hardness capability. The hardness assurance program at International Rectifier is comprised of two radiation environments. Every manufacturing lot is tested for total ionizing dose (per notes 5 and 6) using the TO-3 package. Both pre- and post-irradiation performance are tested and specified using the same drive circuitry and test conditions in order to provide a direct comparison.

Table 1. Electrical Characteristics @ $T_j = 25^\circ\text{C}$, Post Total Dose Irradiation^{⑤⑥}

	Parameter	100K Rads(S) ¹		300K Rads (S) ²		Units	Test Conditions
		Min	Max	Min	Max		
BV_{DSS}	Drain-to-Source Breakdown Voltage	-100	—	-100	—	V	$\text{V}_{\text{GS}} = 0\text{V}, \text{I}_D = -1.0\text{mA}$
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	-2.0	-4.0	-2.0	-5.0		$\text{V}_{\text{GS}} = \text{V}_{\text{DS}}, \text{I}_D = -1.0\text{mA}$
I_{GSS}	Gate-to-Source Leakage Forward	—	-100	—	-100	nA	$\text{V}_{\text{GS}} = -20\text{V}$
I_{GSS}	Gate-to-Source Leakage Reverse	—	100	—	100		$\text{V}_{\text{GS}} = 20\text{ V}$
I_{DSS}	Zero Gate Voltage Drain Current	—	-25	—	-25	μA	$\text{V}_{\text{DS}} = -80\text{V}, \text{V}_{\text{GS}} = 0\text{V}$
$\text{R}_{\text{DS(on)}}$	Static Drain-to-Source ^④ On-State Resistance (TO-3)	—	0.259	—	0.259	Ω	$\text{V}_{\text{GS}} = -12\text{V}, \text{I}_D = -4.1\text{A}$
$\text{R}_{\text{DS(on)}}$	Static Drain-to-Source ^④ On-State Resistance (LCC-18)	—	0.30	—	0.30	Ω	$\text{V}_{\text{GS}} = -12\text{V}, \text{I}_D = -4.1\text{A}$
V_{SD}	Diode Forward Voltage ^④	—	-3.0	—	-3.0	V	$\text{V}_{\text{GS}} = 0\text{V}, \text{I}_S = -6.5\text{A}$

1. Part number IRHE9130 (JANSR2N7389U)

2. Part number IRHE9130 (JANSF2N7389U)

International Rectifier radiation hardened MOSFETs have been characterized in heavy ion environment for Single Event Effects (SEE). Single Event Effects characterization is illustrated in Fig. a and Table 2.

Table 2. Single Event Effect Safe Operating Area

Ion	LET MeV/(mg/cm ²)	Energy (MeV)	Range (μm)	V _{DS} (v)				
				@V _{GS} =0V	@V _{GS} =5V	@V _{GS} =10V	@V _{GS} =15V	@V _{GS} =20V
Cu	28	285	43	-100	-100	-100	-70	-60
Br	36.8	305	39	-100	-100	-70	-50	-40
I	59.8	343	32.6	-60	—	—	—	—

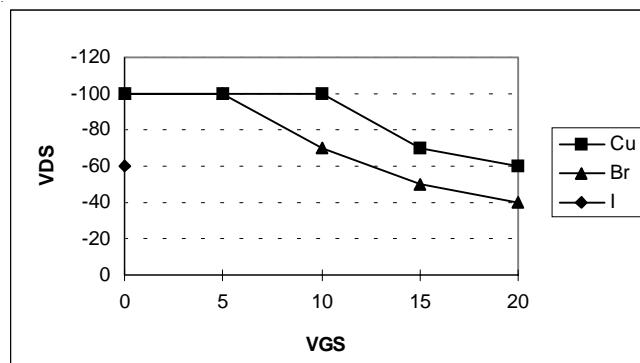


Fig a. Single Event Effect, Safe Operating Area

For footnotes refer to the last page

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Pre-Irradiation

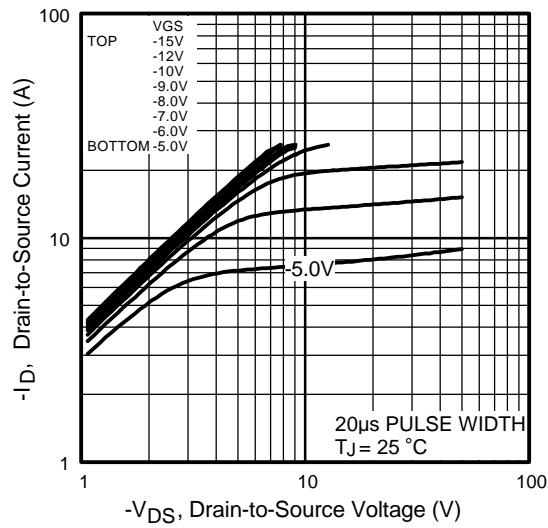


Fig 1. Typical Output Characteristics

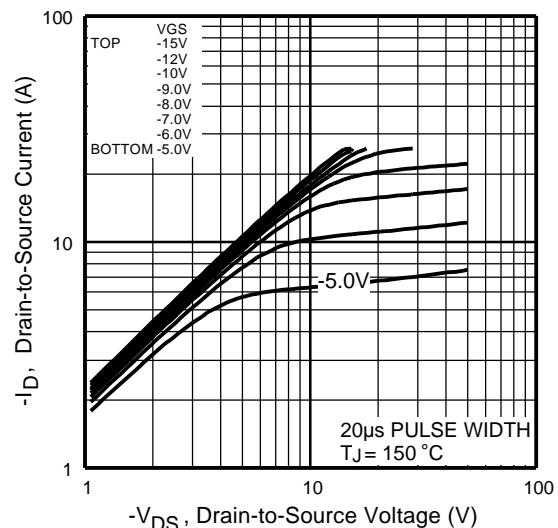


Fig 2. Typical Output Characteristics

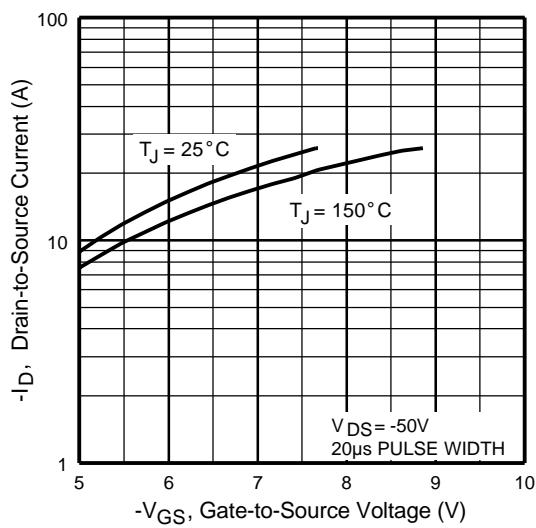


Fig 3. Typical Transfer Characteristics

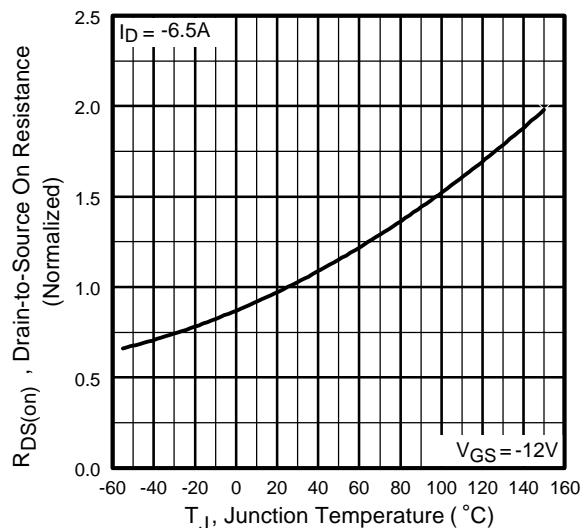


Fig 4. Normalized On-Resistance Vs. Temperature

Pre-Irradiation

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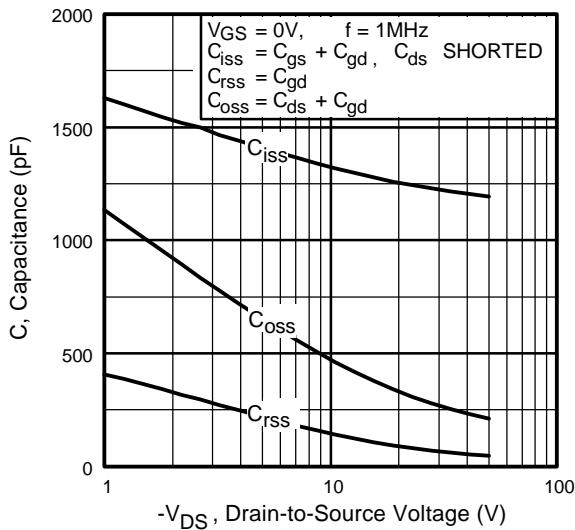


Fig 5. Typical Capacitance Vs.
Drain-to-Source Voltage

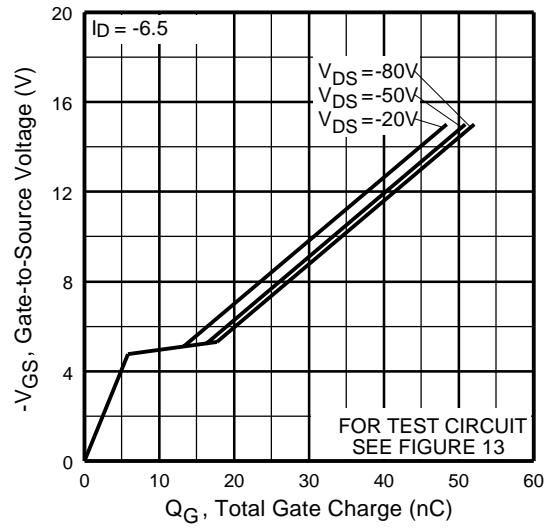


Fig 6. Typical Gate Charge Vs.
Gate-to-Source Voltage

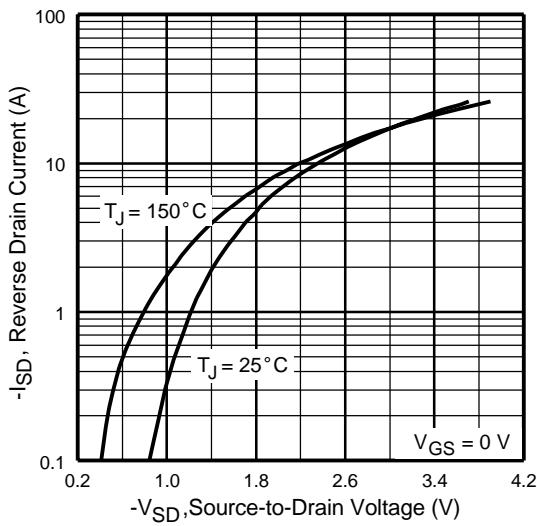


Fig 7. Typical Source-Drain Diode
Forward Voltage

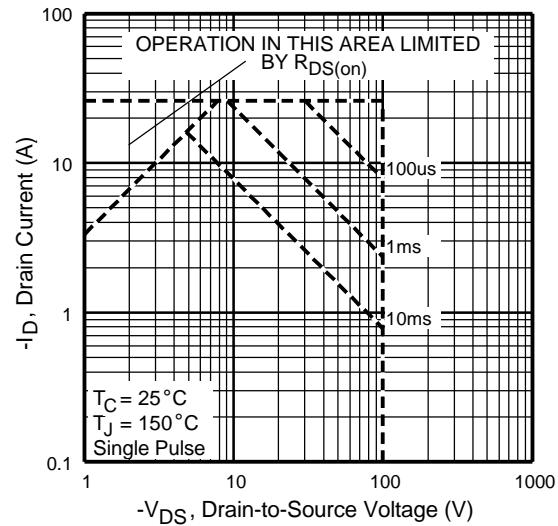


Fig 8. Maximum Safe Operating Area

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Pre-Irradiation

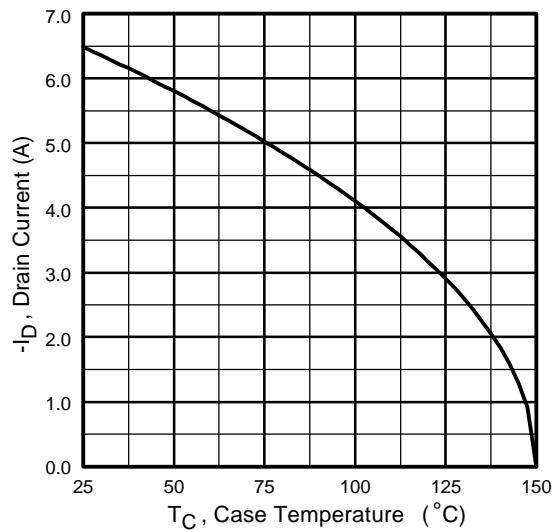


Fig 9. Maximum Drain Current Vs.
Case Temperature

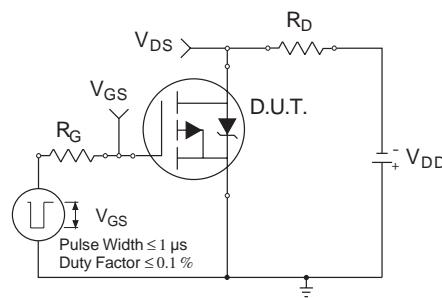


Fig 10a. Switching Time Test Circuit

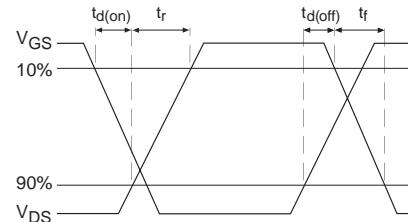


Fig 10b. Switching Time Waveforms

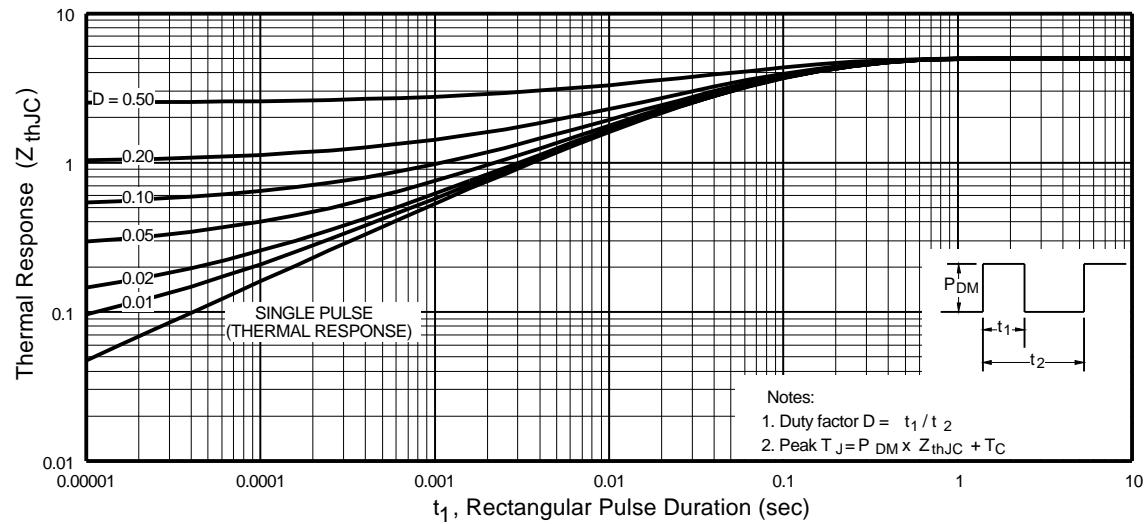


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

Pre-Irradiation

IRHE9130

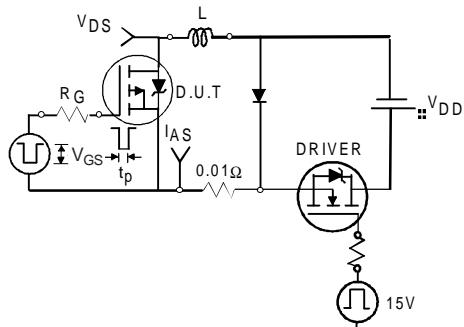


Fig 12a. Unclamped Inductive Test Circuit

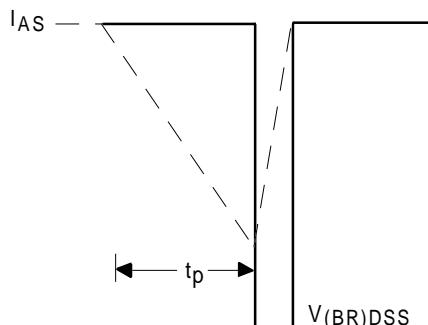


Fig 12b. Unclamped Inductive Waveforms

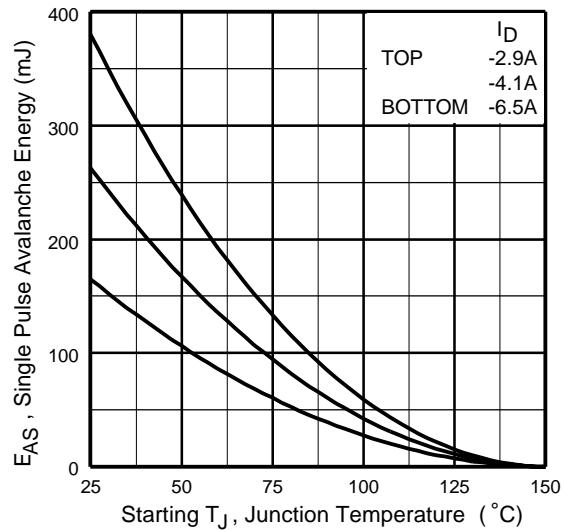


Fig 12c. Maximum Avalanche Energy Vs. Drain Current

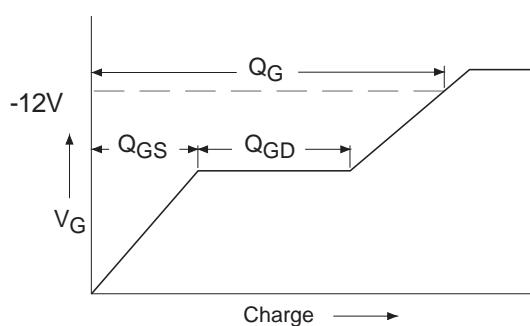


Fig 13a. Basic Gate Charge Waveform

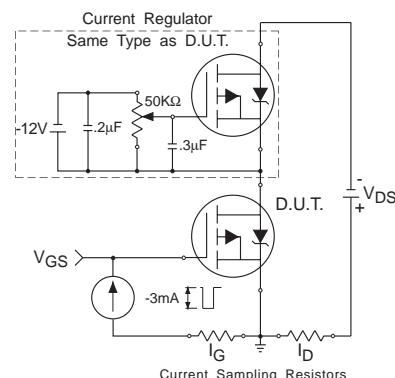
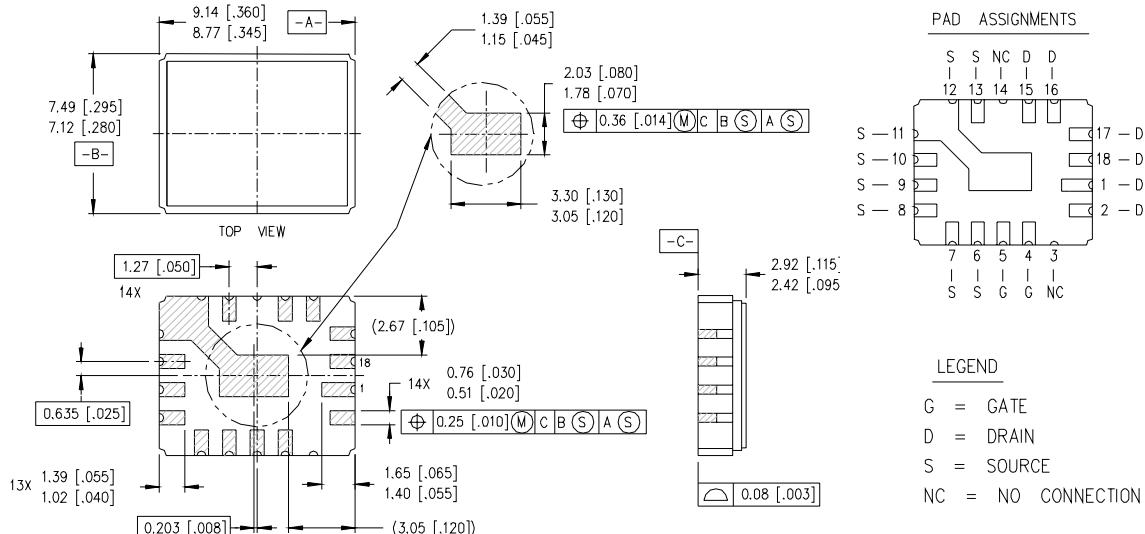


Fig 13b. Gate Charge Test Circuit

Footnotes:

- ① Repetitive Rating; Pulse width limited by maximum junction temperature.
- ② V_{DD} = -25V, starting T_J = 25°C, L = 7.8mH
Peak I_L = -6.5A, V_{GS} = -12V
- ③ ISD ≤ -6.5A, di/dt ≤ -430A/μs,
V_{DD} ≤ -100V, T_J ≤ 150°C

- ④ Pulse width ≤ 300 μs; Duty Cycle ≤ 2%
- ⑤ **Total Dose Irradiation with V_{GS} Bias.**
-12 volt V_{GS} applied and V_{DS} = 0 during irradiation per MIL-STD-750, method 1019, condition A.
- ⑥ **Total Dose Irradiation with V_{DS} Bias.**
-80 volt V_{DS} applied and V_{GS} = 0 during irradiation per MIL-STD-750, method 1019, condition A.

Case Outline and Dimensions — LCC-18

NOTES:

1. DIMENSIONING & TOLERANCING PER ANSI Y14.5M-1982.
2. CONTROLLING DIMENSION: INCH.
3. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].

International
IR Rectifier

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Data and specifications subject to change without notice. 02/03