



PD - 93789B

## RADIATION HARDENED POWER MOSFET THRU-HOLE (TO-39)

**IRHF57130**  
**100V, N-CHANNEL**  
**R5 TECHNOLOGY**



### Product Summary

Part Number	Radiation Level	R <sub>Ds(on)</sub>	I <sub>D</sub>
IRHF57130	100K Rads (Si)	0.08Ω	11.7A
IRHF53130	300K Rads (Si)	0.08Ω	11.7A
IRHF54130	600K Rads (Si)	0.08Ω	11.7A
IRHF58130	1000K Rads (Si)	0.10Ω	11.7A

International Rectifier's R5™ technology provides high performance power MOSFETs for space applications. These devices have been characterized for Single Event Effects (SEE) with useful performance up to an LET of 80 (MeV/(mg/cm<sup>2</sup>)). The combination of low R<sub>Ds(on)</sub> and low gate charge reduces the power losses in switching applications such as DC to DC converters and motor control. These devices retain all of the well established advantages of MOSFETs such as voltage control, fast switching, ease of paralleling and temperature stability of electrical parameters.

### Features:

- Single Event Effect (SEE) Hardened
- Ultra Low R<sub>Ds(on)</sub>
- Neutron Tolerant
- Identical Pre- and Post-Electrical Test Conditions
- Repetitive Avalanche Ratings
- Dynamic dv/dt Ratings
- Simple Drive Requirements
- Ease of Paralleling
- Hermetically Sealed

### Absolute Maximum Ratings

### Pre-Irradiation

	Parameter	Units
I <sub>D</sub> @ V <sub>GS</sub> = 12V, T <sub>C</sub> = 25°C	Continuous Drain Current	11.7
I <sub>D</sub> @ V <sub>GS</sub> = 12V, T <sub>C</sub> = 100°C	Continuous Drain Current	7.4
	I <sub>DM</sub>	47
P <sub>D</sub> @ T <sub>C</sub> = 25°C	Max. Power Dissipation	25
	Linear Derating Factor	0.2
V <sub>GS</sub>	Gate-to-Source Voltage	±20
EAS	Single Pulse Avalanche Energy ②	173
I <sub>AR</sub>	Avalanche Current ①	11.7
EAR	Repetitive Avalanche Energy ①	2.5
dv/dt	Peak Diode Recovery dv/dt ③	4.9
T <sub>J</sub>	Operating Junction	-55 to 150
T <sub>TSG</sub>	Storage Temperature Range	°C
	Lead Temperature	300 (0.063 in./1.6mm from case for 10s)
	Weight	0.98 (Typical)
		g

For footnotes refer to the last page

**Electrical Characteristics @  $T_j = 25^\circ\text{C}$  (Unless Otherwise Specified)**

	Parameter	Min	Typ	Max	Units	Test Conditions
BVDSS	Drain-to-Source Breakdown Voltage	100	—	—	V	$V_{GS} = 0V, I_D = 1.0\text{mA}$
$\Delta BVDSS/\Delta T_J$	Temperature Coefficient of Breakdown Voltage	—	0.12	—	V/ $^\circ\text{C}$	Reference to $25^\circ\text{C}$ , $I_D = 1.0\text{mA}$
RDS(on)	Static Drain-to-Source On-State Resistance	—	—	0.08	$\Omega$	$V_{GS} = 12V, I_D = 7.4\text{A}$ ④
VGS(th)	Gate Threshold Voltage	2.0	—	4.0	V	$V_{DS} = V_{GS}, I_D = 1.0\text{mA}$
gfs	Forward Transconductance	8.7	—	—	S ( $\text{S}(\text{O})$ )	$V_{DS} > 15V, I_{DS} = 7.4\text{A}$ ④
IDSS	Zero Gate Voltage Drain Current	—	—	10	$\mu\text{A}$	$V_{DS} = 80V, V_{GS}=0V$
		—	—	25		$V_{DS} = 80V, V_{GS} = 0V, T_J = 125^\circ\text{C}$
IGSS	Gate-to-Source Leakage Forward	—	—	100	nA	$V_{GS} = 20V$
IGSS	Gate-to-Source Leakage Reverse	—	—	-100		$V_{GS} = -20V$
Qg	Total Gate Charge	—	—	50	nC	$V_{GS} = 12V, I_D = 11.7\text{A}$
Qgs	Gate-to-Source Charge	—	—	7.4		$V_{DS} = 50V$
Qgd	Gate-to-Drain ('Miller') Charge	—	—	20	ns	
td(on)	Turn-On Delay Time	—	—	25		$V_{DD} = 50V, I_D = 11.7\text{A}$
tr	Rise Time	—	—	100		$V_{GS} = 12V, R_G = 7.5\Omega$
td(off)	Turn-Off Delay Time	—	—	35		
tf	Fall Time	—	—	30		
LS + LD	Total Inductance	—	7.0	—	nH	Measured from Drain lead (6mm /0.25in. from package) to Source lead (6mm /0.25in. from package) with Source wires internally bonded from Source Pin to Drain Pad
Ciss	Input Capacitance	—	1038	—	pF	$V_{GS} = 0V, V_{DS} = 25V$
Coss	Output Capacitance	—	362	—		$f = 1.0\text{MHz}$
Crss	Reverse Transfer Capacitance	—	45	—		

**Source-Drain Diode Ratings and Characteristics**

	Parameter	Min	Typ	Max	Units	Test Conditions
IS	Continuous Source Current (Body Diode)	—	—	11.7	A	
ISM	Pulse Source Current (Body Diode) ①	—	—	47		
VSD	Diode Forward Voltage	—	—	1.5	V	$T_j = 25^\circ\text{C}, I_S = 11.7\text{A}, V_{GS} = 0V$ ④
t <sub>rr</sub>	Reverse Recovery Time	—	—	202	ns	$T_j = 25^\circ\text{C}, I_F = 11.7\text{A}, di/dt \leq 100\text{A}/\mu\text{s}$
QRR	Reverse Recovery Charge	—	—	982	$\mu\text{C}$	$V_{DD} \leq 25V$ ④
t <sub>on</sub>	Forward Turn-On Time	Intrinsic turn-on time is negligible. Turn-on speed is substantially controlled by LS + LD.				

**Thermal Resistance**

	Parameter	Min	Typ	Max	Units	Test Conditions
R <sub>thJC</sub>	Junction-to-Case	—	—	5.0	$^\circ\text{C/W}$	
R <sub>thJA</sub>	Junction-to-Ambient	—	—	175		Typical socket mount

Note: Corresponding Spice and Saber models are available on the G&S Website.

For footnotes refer to the last page

## Radiation Characteristics

**IRHF57130**

International Rectifier Radiation Hardened MOSFETs are tested to verify their radiation hardness capability. The hardness assurance program at International Rectifier is comprised of two radiation environments. Every manufacturing lot is tested for total ionizing dose (per notes 5 and 6) using the TO-3 package. Both pre- and post-irradiation performance are tested and specified using the same drive circuitry and test conditions in order to provide a direct comparison.

**Table 1. Electrical Characteristics @  $T_j = 25^\circ\text{C}$ , Post Total Dose Irradiation<sup>⑤⑥</sup>**

	Parameter	Up to 600K Rads(Si) <sup>1</sup>				Units	Test Conditions
		Min	Max	1000K Rads (Si) <sup>2</sup>	Max		
$\text{BV}_{\text{DSS}}$	Drain-to-Source Breakdown Voltage	100	—	100	—	V	$\text{V}_{\text{GS}} = 0\text{V}, \text{I}_D = 1.0\text{mA}$
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	2.0	4.0	1.5	4.0		$\text{V}_{\text{GS}} = \text{V}_{\text{DS}}, \text{I}_D = 1.0\text{mA}$
$\text{I}_{\text{GSS}}$	Gate-to-Source Leakage Forward	—	100	—	100	nA	$\text{V}_{\text{GS}} = 20\text{V}$
$\text{I}_{\text{GSS}}$	Gate-to-Source Leakage Reverse	—	-100	—	-100		$\text{V}_{\text{GS}} = -20\text{ V}$
$\text{I}_{\text{DSS}}$	Zero Gate Voltage Drain Current	—	10	—	25	$\mu\text{A}$	$\text{V}_{\text{DS}} = 80\text{V}, \text{V}_{\text{GS}} = 0\text{V}$
$\text{R}_{\text{DS(on)}}$	Static Drain-to-Source <sup>④</sup> On-State Resistance (TO-3)	—	0.064	—	0.08	$\Omega$	$\text{V}_{\text{GS}} = 12\text{V}, \text{I}_D = 7.4\text{A}$
$\text{R}_{\text{DS(on)}}$	Static Drain-to-Source <sup>④</sup> On-State Resistance (TO-39)	—	0.08	—	0.10	$\Omega$	$\text{V}_{\text{GS}} = 12\text{V}, \text{I}_D = 7.4\text{A}$
$\text{V}_{\text{SD}}$	Diode Forward Voltage <sup>④</sup>	—	1.5	—	1.5	V	$\text{V}_{\text{GS}} = 0\text{V}, \text{I}_S = 11.7\text{A}$

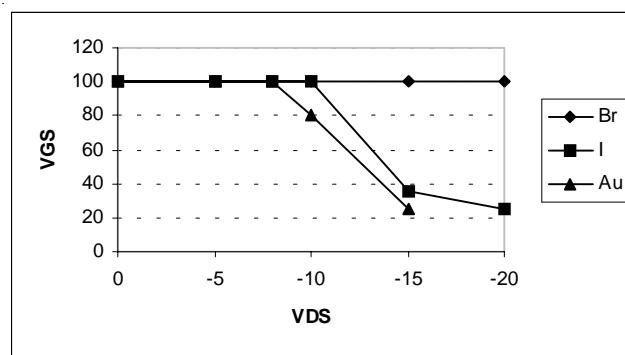
1. Part numbers IRHF57130, IRHF53130 and IRHF54130

2. Part number IRHF58130

International Rectifier radiation hardened MOSFETs have been characterized in heavy ion environment for Single Event Effects (SEE). Single Event Effects characterization is illustrated in Fig. a and Table 2.

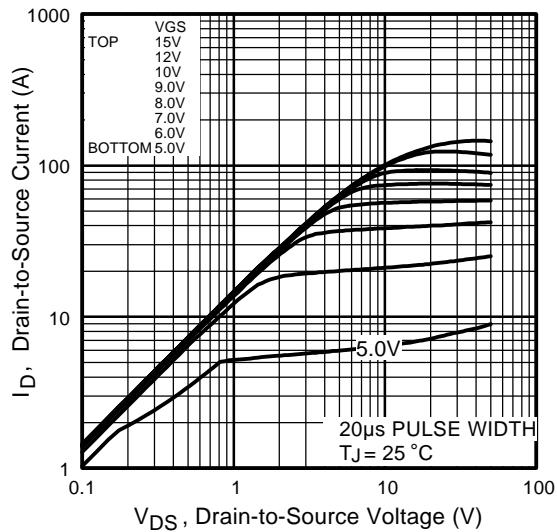
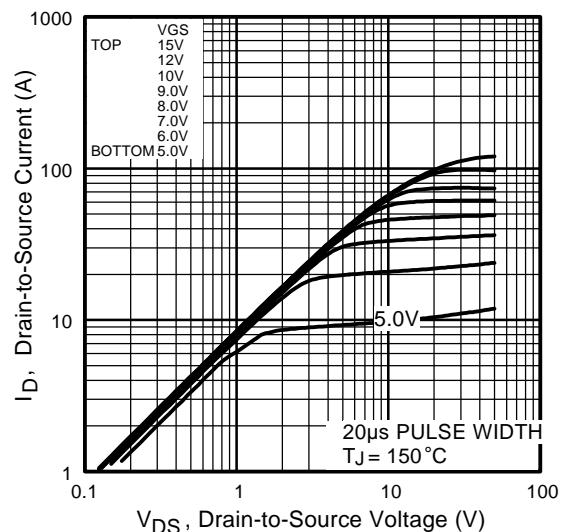
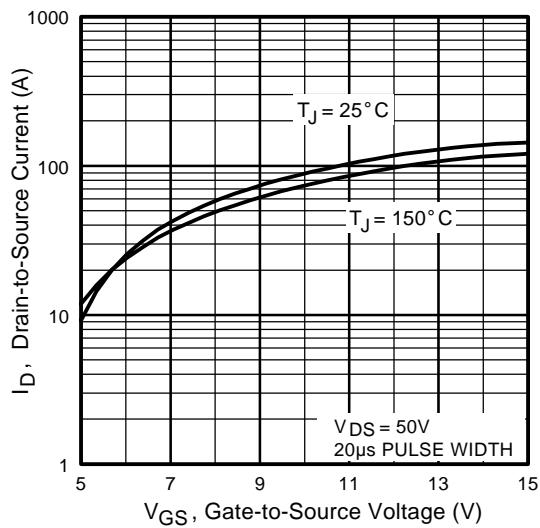
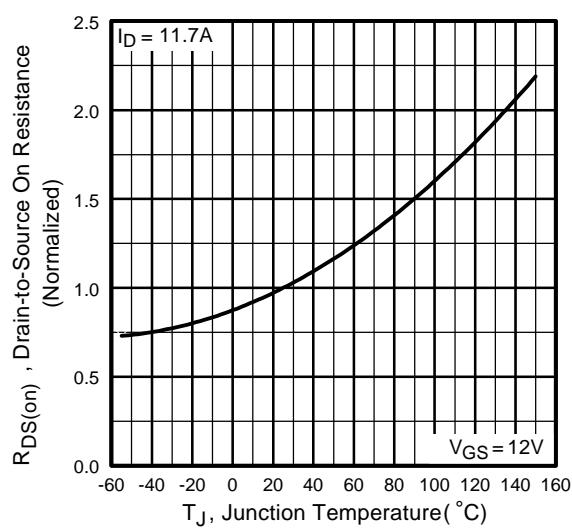
**Table 2. Single Event Effect Safe Operating Area**

Ion	LET (MeV/(mg/cm <sup>2</sup> ))	Energy (MeV)	Range ( $\mu\text{m}$ )	$\text{V}_{\text{DS}}$ (V)				
				@ $\text{V}_{\text{GS}} = 0\text{V}$	@ $\text{V}_{\text{GS}} = -5\text{V}$	@ $\text{V}_{\text{GS}} = -10\text{V}$	@ $\text{V}_{\text{GS}} = -15\text{V}$	@ $\text{V}_{\text{GS}} = -20\text{V}$
Br	36.7	309	39.5	100	100	100	100	100
I	59.8	341	32.5	100	100	100	35	25
Au	82.3	350	28.4	100	100	80	25	—



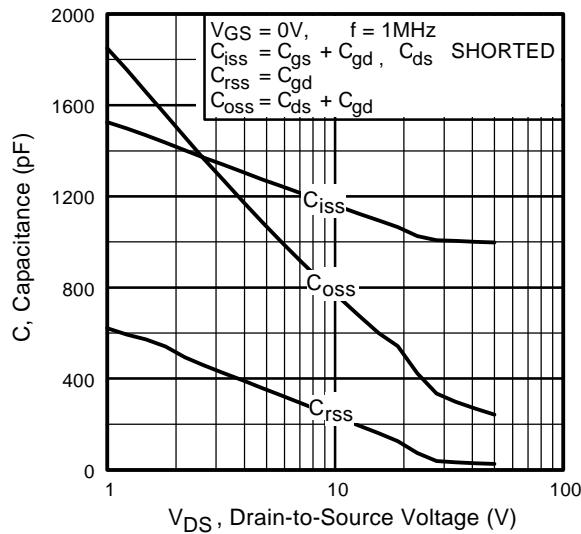
**Fig a.** Single Event Effect, Safe Operating Area

For footnotes refer to the last page

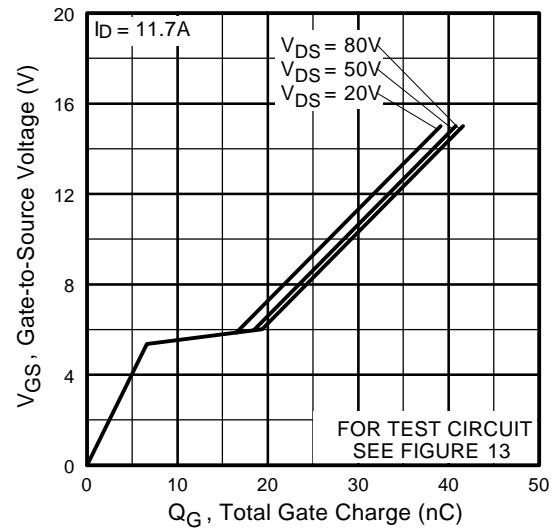
**IRHF57130****Fig 1.** Typical Output Characteristics**Pre-Irradiation****Fig 2.** Typical Output Characteristics**Fig 3.** Typical Transfer Characteristics**Fig 4.** Normalized On-Resistance Vs. Temperature

## Pre-Irradiation

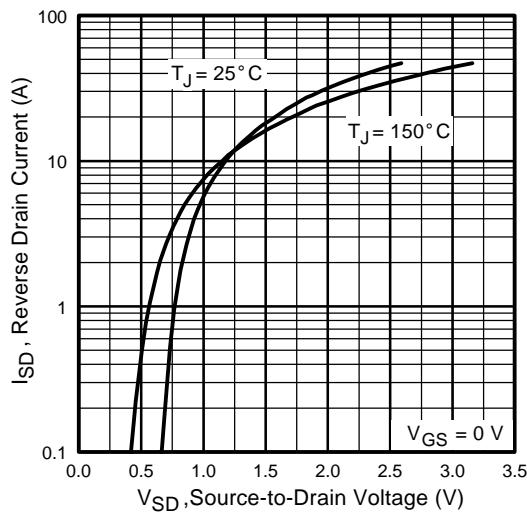
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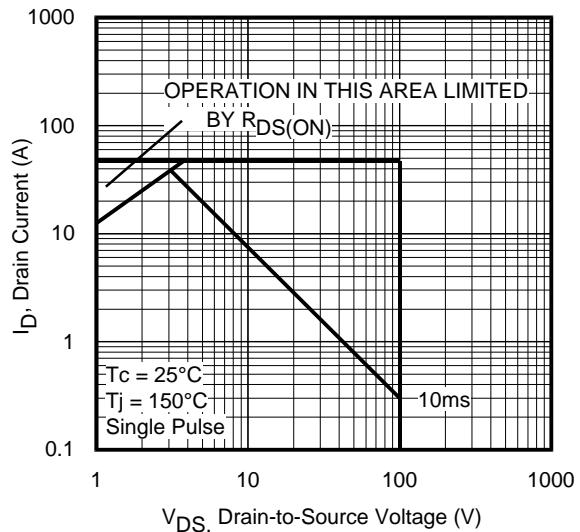
**Fig 5.** Typical Capacitance Vs.  
Drain-to-Source Voltage



**Fig 6.** Typical Gate Charge Vs.  
Gate-to-Source Voltage



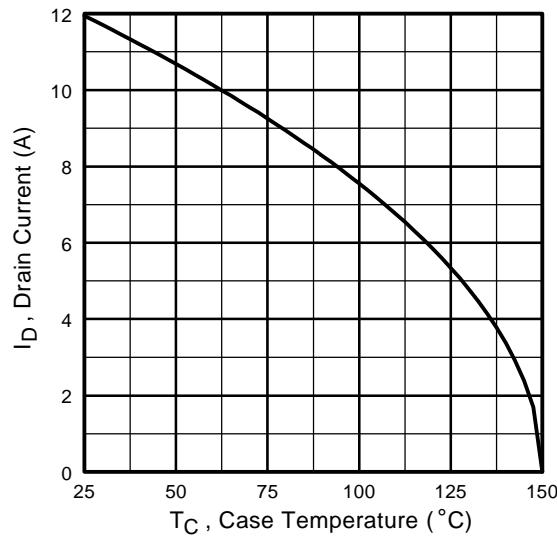
**Fig 7.** Typical Source-Drain Diode  
Forward Voltage



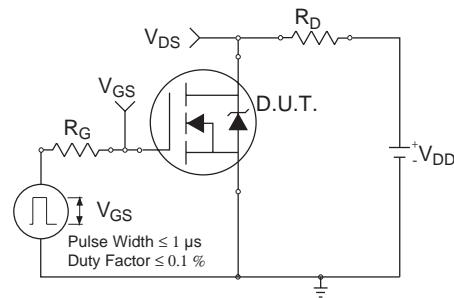
**Fig 8.** Maximum Safe Operating Area

## IRHF57130

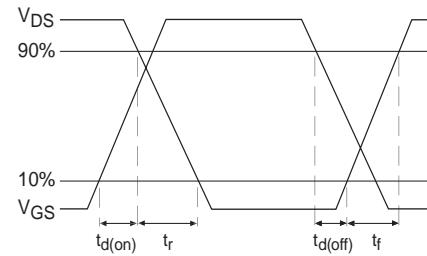
## Pre-Irradiation



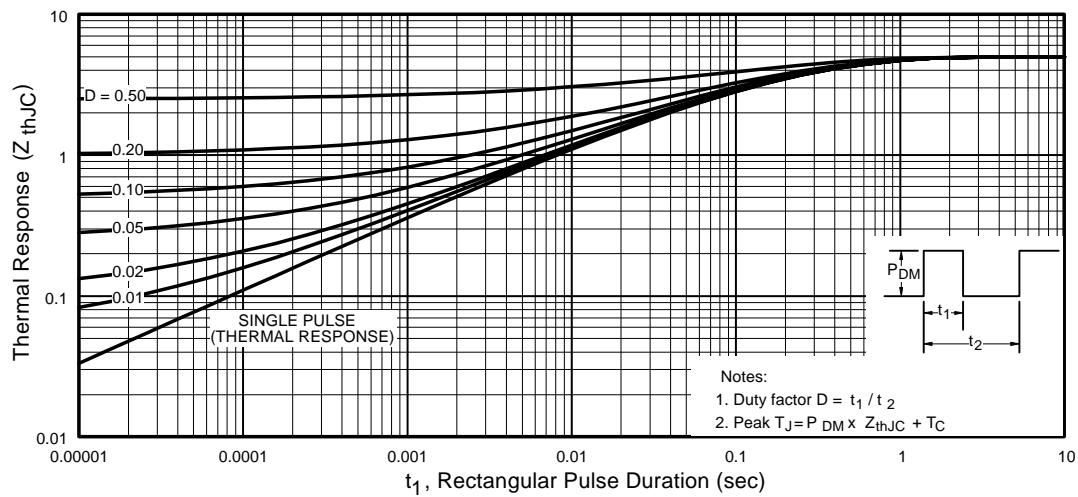
**Fig 9.** Maximum Drain Current Vs.  
Case Temperature



**Fig 10a.** Switching Time Test Circuit



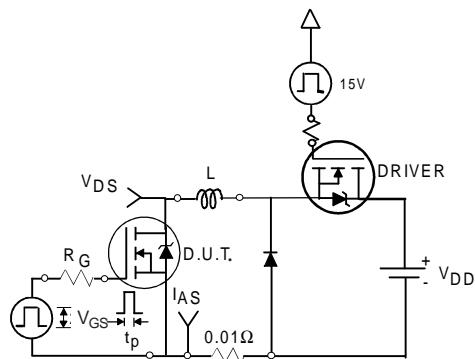
**Fig 10b.** Switching Time Waveforms



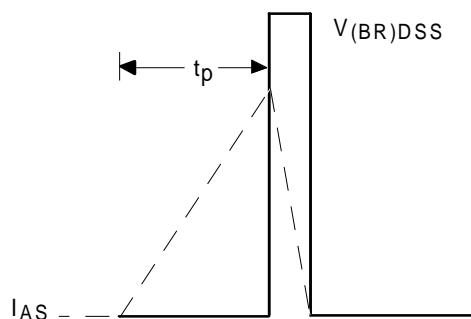
**Fig 11.** Maximum Effective Transient Thermal Impedance, Junction-to-Case

## Pre-Irradiation

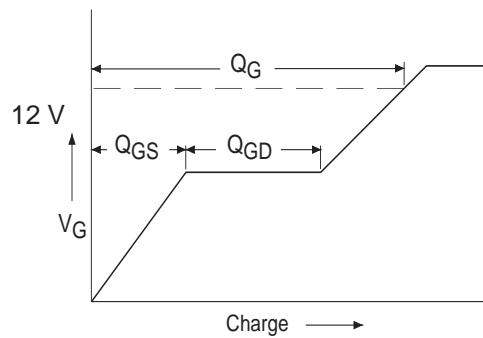
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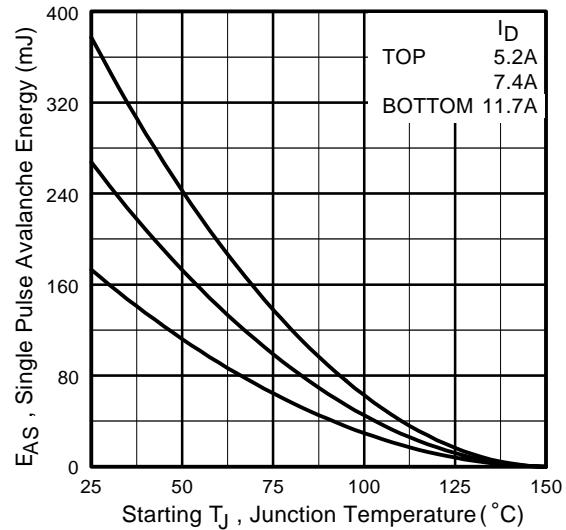
**Fig 12a.** Unclamped Inductive Test Circuit



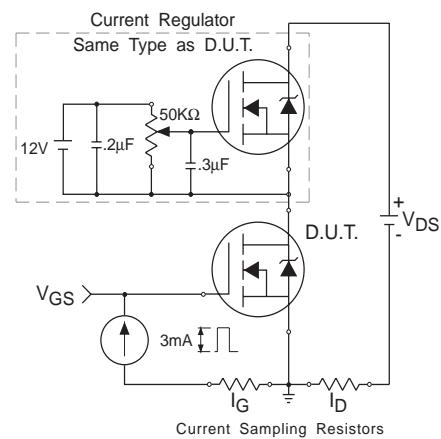
**Fig 12b.** Unclamped Inductive Waveforms



**Fig 13a.** Basic Gate Charge Waveform



**Fig 12c.** Maximum Avalanche Energy Vs. Drain Current

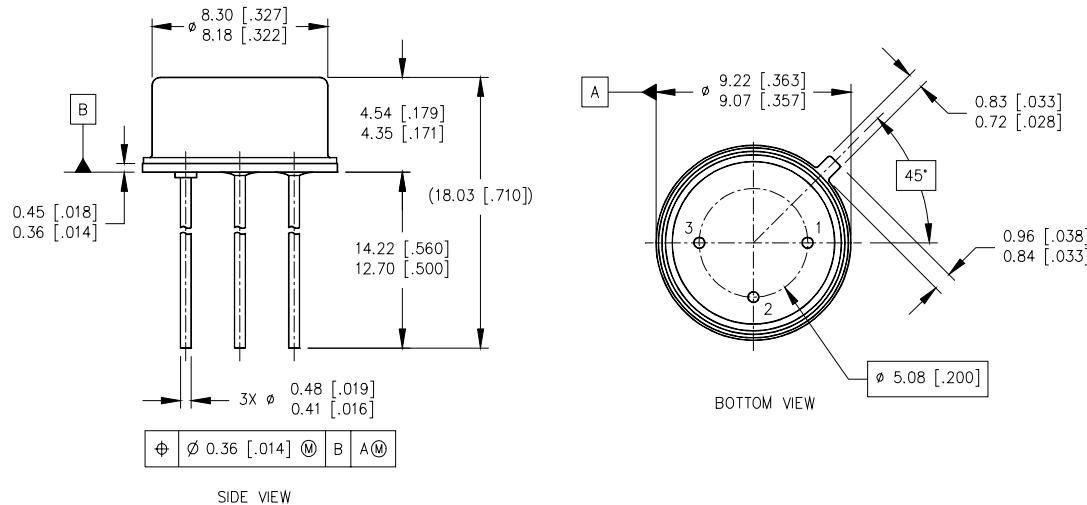


**Fig 13b.** Gate Charge Test Circuit

**Footnotes:**

- ① Repetitive Rating; Pulse width limited by maximum junction temperature.
- ② V<sub>DD</sub> = 50V, starting T<sub>J</sub> = 25°C, L= 2.53 mH  
Peak I<sub>L</sub> = 11.7A, V<sub>GS</sub> = 12V
- ③ ISD ≤ 11.7A, di/dt ≤ 216A/μs,  
V<sub>DD</sub> ≤ 100V, T<sub>J</sub> ≤ 150°C

- ④ Pulse width ≤ 300 μs; Duty Cycle ≤ 2%
- ⑤ **Total Dose Irradiation with V<sub>GS</sub> Bias.**  
12 volt V<sub>GS</sub> applied and V<sub>DS</sub> = 0 during irradiation per MIL-STD-750, method 1019, condition A.
- ⑥ **Total Dose Irradiation with V<sub>DS</sub> Bias.**  
80 volt V<sub>DS</sub> applied and V<sub>GS</sub> = 0 during irradiation per MIL-STD-750, method 1019, condition A.

**Case Outline and Dimensions — TO-205AF (Modified TO-39)**

## NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME 14.5M-1994.
2. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
3. CONTROLLING DIMENSION: INCH.
4. CONFORMS TO JEDEC OUTLINE TO-205AF (TO-39).

LEGEND

- 1- SOURCE
- 2- GATE
- 3- DRAIN

International  
**IR** Rectifier

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