

Low Skew Output Buffer

General Description

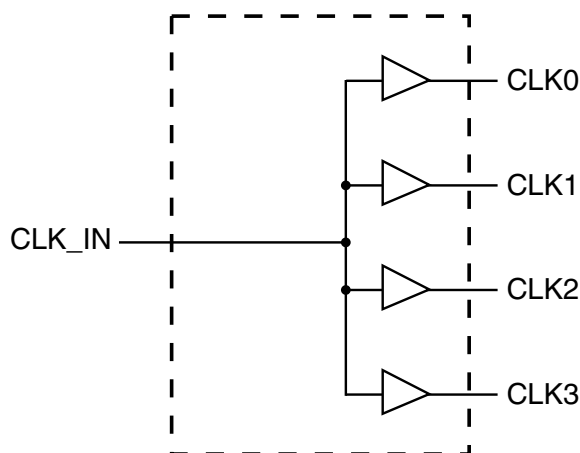
The ICS9112-26 is a high performance, low skew, low jitter clock driver. It is designed to distribute high speed clocks in PC systems operating at speeds from 0 to 133 MHz.

The ICS9112-26 comes in an eight pin 150 mil SOIC package. It has four output clocks.

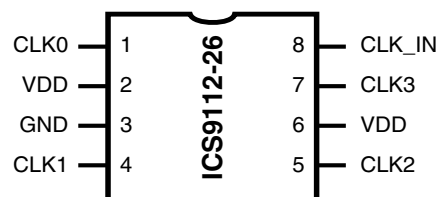
Features

- Frequency range 0 - 133 MHz (3.3V)
- Less than 200 ps Jitter between outputs
- Skew controlled outputs
- Skew less than 250 ps between outputs
- Available in 8 pin 150 mil SOIC & 173 mil TSSOP packages.
- 3.3V $\pm 10\%$ operation

Block Diagram



Pin Configuration



8 pin SOIC & TSSOP

Pin Descriptions

PIN NUMBER	PIN NAME	TYPE	DESCRIPTION
1	CLK0 ³	OUT	Buffered clock output
2,6	VDD	PWR	Power Supply (3.3V)
3	GND	PWR	Ground
4	CLK1 ¹	OUT	Buffered clock output
5	CLK2 ¹	OUT	Buffered clock output
7	CLK3 ¹	OUT	Buffered clock output
8	CLK_IN	IN	Input reference frequency.

Notes:

1. Weak pull-down on all outputs



Absolute Maximum Ratings

Supply Voltage	7.0 V
Logic Inputs	GND –0.5 V to $V_{DD} + 0.5$ V
Ambient Operating Temperature	0°C to +70°C
Storage Temperature	–65°C to +150°C

Stresses above those listed under *Absolute Maximum Ratings* may cause permanent damage to the device. These ratings are stress specifications only and functional operation of the device at these or any other conditions above those listed in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect product reliability.

Electrical Characteristics at 3.3V

$V_{DD} = 3.0 - 3.6$ V, $T_A = 0 - 70^\circ$ C unless otherwise stated

DC Characteristics						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
Input Low Voltage	V_{IL}				0.8	V
Input High Voltage	V_{IH}		2.0			V
Input Low Current	I_{IL}	$V_{IN} = 0$ V			50.0	μ A
Input High Current	I_{IH}	$V_{IN} = V_{DD}$			100.0	μ A
Output Low Voltage ¹	V_{OL}	$I_{OL} = 8$ mA			0.4	V
Output High Voltage ¹	V_{OH}	$I_{OH} = 8$ mA	2.4			V
Supply Current	I_{DD}	REF = 0 MHz			50.0	μ A
Supply Current	I_{DD}	Unloaded outputs at 66.66 MHz			40.0	mA

Notes:

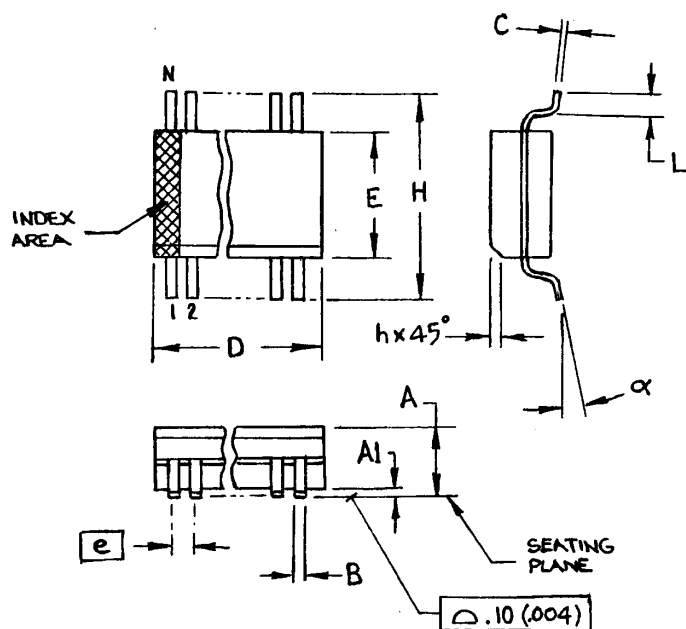
1. Guaranteed by design and characterization. Not subject to 100% test.
2. All Skew specifications are measured with a 50 Ω transmission line, load terminated with 50 Ω to 1.4V.
3. Duty cycle measured at 1.4V.
4. Skew measured at 1.4V on rising edges. Loading must be equal on outputs.

Switching Characteristics (3.3V Continued)

PARAMETER	SYMBOL	CONDITION	MIN	TYP	MAX	UNITS
Propagation delay	t_p		4.5		8.0	ns
Rise Time ¹	t_{r1}	Measured between 0.8V and 2.0V; CL=30pF @ all operating frequencies			1.5	ns
Fall Time ¹	t_{f1}	Measured between 2.0V and 0.8V; CL=30pF @ all operating frequencies			1.5	ns
Output to Output Skew ¹	T_{skew}	All outputs equally loaded, CL=20pF			250	ps
Device to Device Skew ¹	$T_{dsk} - T_{dsk}$	Measured at $V_{DD}/2$ on the CLKOUT pins of devices			700	ps

Notes:

1. Guaranteed by design and characterization. Not subject to 100% test.
2. CLK_IN input has a threshold voltage of 1.4V
3. All parameters expected with loaded outputs



150 mil (Narrow Body) SOIC

SYMBOL	In Millimeters COMMON DIMENSIONS		In Inches COMMON DIMENSIONS	
	MIN	MAX	MIN	MAX
A	1.35	1.75	.0532	.0688
A1	0.10	0.25	.0040	.0098
B	0.33	0.51	.013	.020
C	0.19	0.25	.0075	.0098
D	SEE VARIATIONS		SEE VARIATIONS	
E	3.80	4.0	.1497	.1574
e	1.27 BASIC		0.050 BASIC	
H	5.80	6.20	.2284	.2440
h	0.25	0.50	.010	.020
L	0.40	1.27	.016	.050
N	SEE VARIATIONS		SEE VARIATIONS	
α	0°	8°	0°	8°

VARIATIONS

N	D mm.		D (inch)	
	MIN	MAX	MIN	MAX
8	4.80	5.00	.1890	.1968

Ordering Information

ICS9112yM-26-T

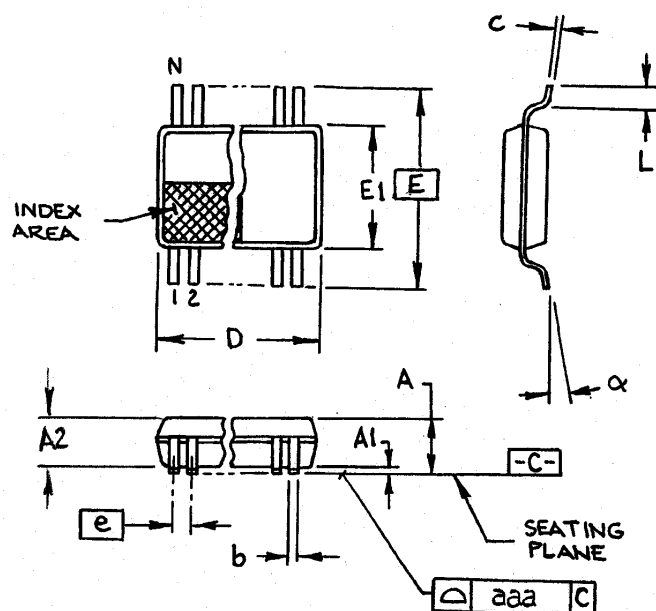
Example:

ICS XXXX y M - PPP - T

- Designation for tape and reel packaging
- Pattern Number (2 or 3 digit number for parts with ROM code patterns)
- Package Type
M=SOIC
- Revision Designator (will not correlate with datasheet revision)
- Device Type (consists of 3 or 4 digit numbers)
- Prefix
ICS, AV = Standard Device

ICS9112-26

Preliminary Product Preview



4.40 mm. Body, 0.65 mm. pitch TSSOP
(173 mil) (0.0256 mil)

SYMBOL	In Millimeters COMMON DIMENSIONS		In Inches COMMON DIMENSIONS	
	MIN	MAX	MIN	MAX
A	-	1.20	-	.047
A1	0.05	0.15	.002	.006
A2	0.80	1.05	.032	.041
b	0.19	0.30	.007	.012
c	0.09	0.20	.0035	.008
D	SEE VARIATIONS		SEE VARIATIONS	
E	6.40 BASIC		0.252 BASIC	
E1	4.30	4.50	.169	.177
e	0.65 BASIC		0.0256 BASIC	
L	0.45	0.75	.018	.030
N	SEE VARIATIONS		SEE VARIATIONS	
α	0°	8°	0°	8°
aaa	-	0.10	-	.004

VARIATIONS

N	D mm.		D (inch)	
	MIN	MAX	MIN	MAX
8	2.90	3.10	.114	.122

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7/6/00 Rev B

Ordering Information

ICS9112yG-26-T

Example:

ICS XXXX y G - PPP - T

Designation for tape and reel packaging

Pattern Number (2 or 3 digit number for parts with ROM code patterns)

Package Type

G=TSSOP

Revision Designator (will not correlate with datasheet revision)

Device Type

Prefix

ICS, AV = Standard Device