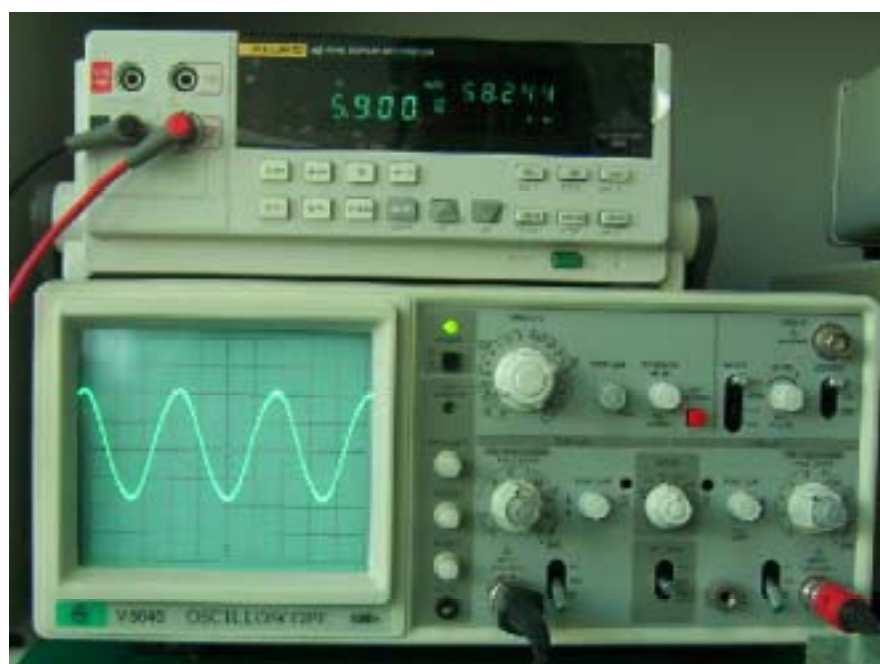


1. OTHER DIAGRAM INFORMATION :

1.1 PRODUCTION



1.2 .TEST WAVEFORM

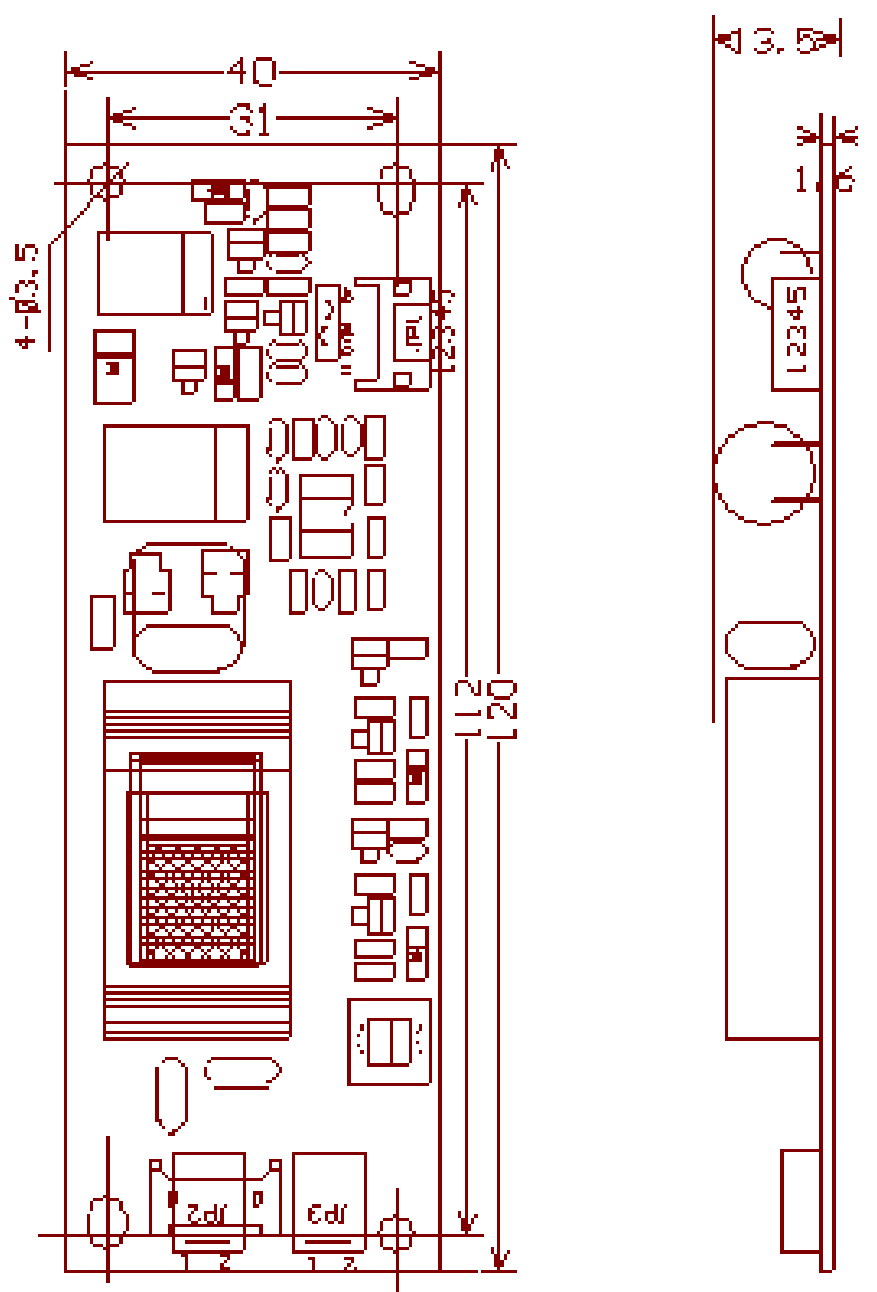


深圳市宏佳电子有限公司
 深圳市福田区金地工业区 116 栋 6 层北
 TEL:0755-83892182 FAX:0755-83892192
 E-MAIL:sales@grandwide.com.cn
www.grandwide.com.cn

制 表	设 计	审 核	批 准

2. MECHANICAL DRAWINGS

SIZE : Unit : mm Tolerance: $\pm 0.5\text{mm}$



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3. APPLIED DESCRIPTION

This DC-AC Inverter was developed for two CCFL lamps of LCD back lighting system.

4. SUITABLE LOAD

2 Lamps SHARP 11";14" - TFT LCD Module NO.:

5. ABSOLUTE MAXIMUM RATING LF ENVIRONMENT

Operating Temperature : 0°C -- 50°C

Storage Temperature: -20°C -- 60°C

Operating Humidity: 10% -- 80RH

Storage Humidity: 10% -- 80RH

6. INPUT ELECTRICAL CHARACTERISTICS

No.	PARAMETER	Symbol	MIN	TYPE	MAX	Unit
1	INPUT VOLTAGE	Vin	10.8	12.0	13.2	V
2	INPUT CURRENT	Iin	---	0.9	1.1	A
3	INPUT POWER	Pin	----	10.8	----	W
4	BRIGHTNESS VOLTAGE	Vbrit	0.0	---	5.0	V
5	CONTROL VOLTAGE	Enable Von=5.0V(2.5~Vin) Disable Voff=0V(0~2.0V)				

7. OUTPUT ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYPE	MAX	UNIT
CURRENT	Iout	Vin=12V ;Von=5V ; Vbrit=0.0V;	RL=125KΩ	5.5	6.0	7.0	mA
CURRENT	Iout	Vin=12V ;Von=5V ; Vbrit=5.0V;		2.0	2.5	3.0	mA
VOLTAGE	Vout	Vin=12V;Von=5V;; Vbrit=0.0V;		---	850	860	Vrms
IGNITION	Vig1	Vin=12V;Von=5V;Vbrit=0V/25		1300	---	---	Vrms
VOLTAGE	Vig2	Vin=12V; Von=5V;Vbrit=0V/ 0		1500	---	---	Vrms
FREQUENCY	Fpwm	Vin=12V; Von=5V; Vbrit=0V;		45	58	65	KHZ
EFFICIENCY	η	Vin=12V ;Von=5V ;Vbrit=0V;		70	78	85	%

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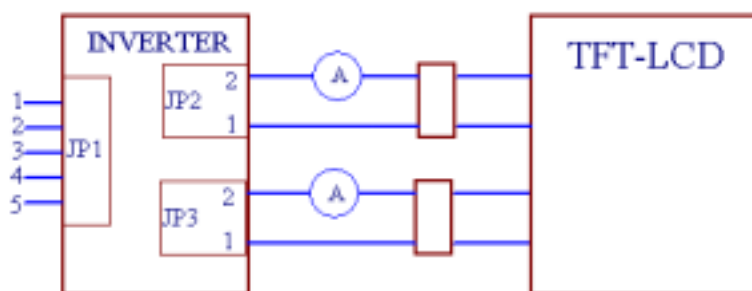
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制 表	设 计	审 核	批 准

8. PIN ASSIGNMENTS

INPUT: JP1(PH-5AWD-2.0) OR EQUIVALENT			
PIN	SYMBOL	DESCRIPTION	VOLTAGE
JP1-1,	Vin	Supply voltage	10.8--13.2V
JP1-2	Gnd	Ground	0 V
JP1-3	Von/off	Standby/operation	Von=5.0V(2.5~Vin); Voff=0V(0~2.0V)
JP1-4	NC		
JP1-5	Vbrit	Dimming control	0.0V=Bright. (Max.); 5.0V= Bright. (Min.)
OUTPUT: JP2;IP3;(LM403-002-TO-2) OR EQUIVALENT			
PIN	SYMBOL	DESCRIPTION	VOLTAGE
JP2-1	H-V	High voltage	H.V
JP2-2	L-V	Low voltage	RETURN

9. Test Circuit Diagram



INSTRUMENTS FOR TEST

- DC POWER SUPPLY: MCH-305D;
- AC CURRENT METER: FLUKE 45;
- FREQUENCY METER: FLUKE 45;
- H.V.PROBE: HP-9258X100 ONLY;
- OSCILLOSCOPE: V-5040;

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