
HAT1020R

Silicon P-Channel Power MOS FET

HITACHI

Preliminary
November 1996

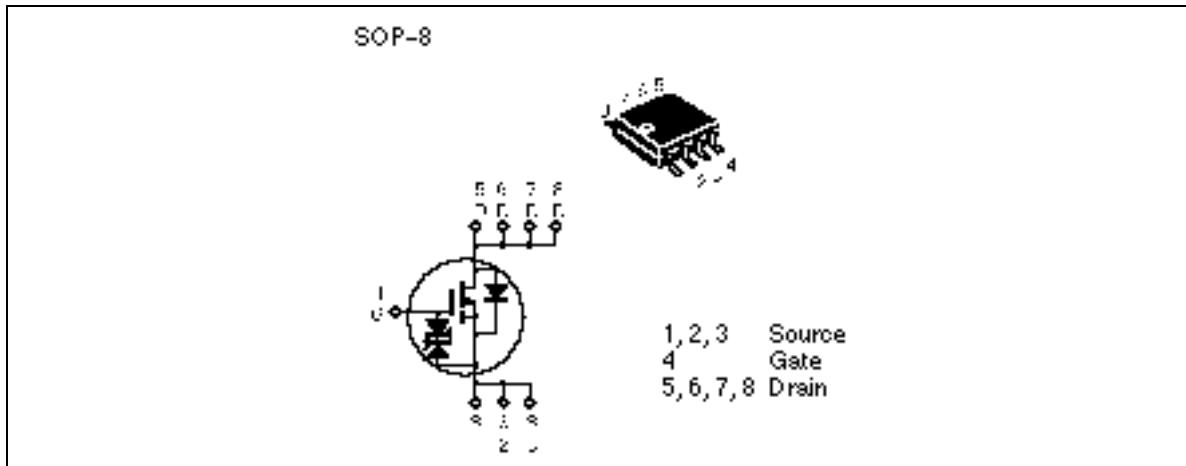
Application

High speed power switching

Features

- Low on-resistance
- Capable of 4 V gate drive
- Low drive current
- High density mounting

Outline



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Ordering Information

Hitachi Code	FP-8DA
EIAJ Code	—
JEDEC Code	MS-012AA

Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	-30	V
Gate to source voltage	V_{GSS}	± 20	V
Drain current	I_D	-4.5	A
Drain peak current	$I_{D(\text{pulse})}^{*1}$	-18	A
Body to drain diode reverse drain current	I_{DR}	-4.5	A
Channel dissipation	P_{ch}^{*2}	2.0	W
Channel temperature	T_{ch}	150	$^\circ\text{C}$
Storage temperature	T_{stg}	-55 to +150	$^\circ\text{C}$

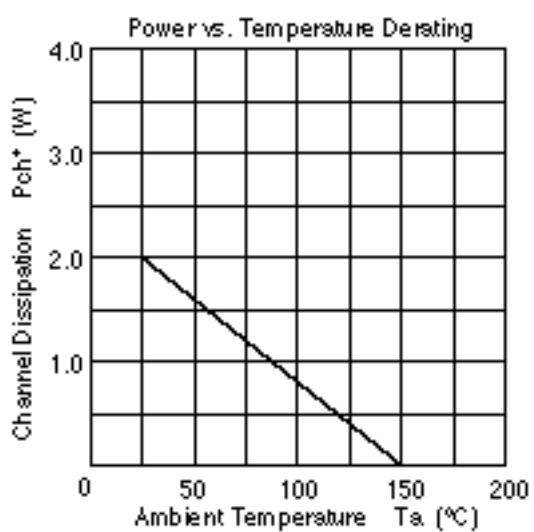
Notes 1. $PW \leq 10 \mu\text{s}$, duty cycle $\leq 1\%$
2. When using surface mounted on FR4 board

Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	V _{(BR)DSS}	-30	—	—	V	I _D = -10 mA, V _{GS} = 0
Gate to source breakdown voltage	V _{(BR)GSS}	±20	—	—	V	I _G = ±100 µA, V _{DS} = 0
Gate to source leak current	I _{GSS}	—	—	±10	µA	V _{GS} = ±16 V, V _{DS} = 0
Zero gate voltage drain current	I _{DSS}	—	—	-10	µA	V _{DS} = -30 V, V _{GS} = 0
Gate to source cutoff voltage	V _{GS(off)}	-1.0	—	-2.0	V	V _{DS} = -10 V, I _D = -1 mA
Static drain to source on state resistance	R _{DS(on)}	—	(0.05)	0.07	½	I _D = -3 A V _{GS} = -10 V ¹
		—	(0.09)	0.13	½	I _D = -3 A V _{GS} = -4 V ¹
Forward transfer admittance	y _{fs}	(5.0)	(8.0)	—	S	I _D = -3 A V _{DS} = -10 V ¹
Input capacitance	C _{iss}	—	(670)	—	pF	V _{DS} = -10 V
Output capacitance	C _{oss}	—	(440)	—	pF	V _{GS} = 0
Reverse transfer capacitance	C _{rss}	—	(170)	—	pF	f = 1 MHz
Turn-on delay time	t _{d(on)}	—	(35)	—	ns	V _{GS} = -4 V, I _D = -3 A
Rise time	t _r	—	(200)	—	ns	V _{DD} = -10 V
Turn-off delay time	t _{d(off)}	—	(35)	—	ns	
Fall time	t _f	—	(60)	—	ns	
Body to drain diode forward voltage	V _{DF}	—	(-0.8)	—	V	I _F = -4.5 A, V _{GS} = 0
Body to drain diode reverse recovery time	t _{rr}	—	(60)	—	ns	I _F = -4.5 A, V _{GS} = 0 dI/dt = 20 A/µs

Note 1. Pulse Test

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